Addendum P5BV3+

Rev. CE0+

Processors

• New Supported Processors

The rev. CE0 system board now supports WinChip2-233, K6-2/450 and K6-III/400 processors.

Correction

The external system bus clock and frequency ratio of WinChip2-266 and WinChip2-300 processors are incorrect, therefore the DIP switch settings for these processors has been changed.

Please refer to the table below for the updated settings.

Processor	SWI: 1-6
K6-2/450 - 100MHz - 4.5x	Q 2 3 4 5 6
K6-III/400 - 100MHz - 4x	0 1 2 3 4 5 6
WinChip2-233 - 66MHz - 3.5x	0 2 3 4 5 6
WinChip2-266 - 100MHz - 2.33x	0 2 3 4 5 6
WinChip2-300 - 100MHz - 2.5x	N 1 2 3 4 5 6

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