



DIP Switch Settings of the Processors

CPU Frequency	External System Bus Clock	Processor Frequency Ratio	SW1
233MHz	66MHz	3.5x	
266MHz	66MHz	4x	
300MHz	66MHz	4.5x	
333MHz	66MHz	5x	
366MHz	66MHz	5.5x	
400MHz	66MHz	6x	
433MHz	66MHz	6.5x	

Black rectangle denotes the part that is protruding, the "adjustable" switch



In the example above:

- Switch 1: On
- Switch 2: Off
- Switch 3: On
- Switch 4: Off

Note:

The voltage regulator will automatically be set according to the voltage of the processor.

JP1 (Wake-On-Keyboard/Mouse) - 1-2 On: Disable; 2-3 On: Enable (default)

If JP1 is set at 2-3 On, make sure "Keyboard/Mouse Power On" in the Integrated Peripherals setup of the Award BIOS is Enabled.

JP3 (Clear CMOS Data) - 1-2 On: Normal (default); 2-3 On: Clear CMOS Data

Before clearing the CMOS data, make sure to power-off your system and unplug the power cord.