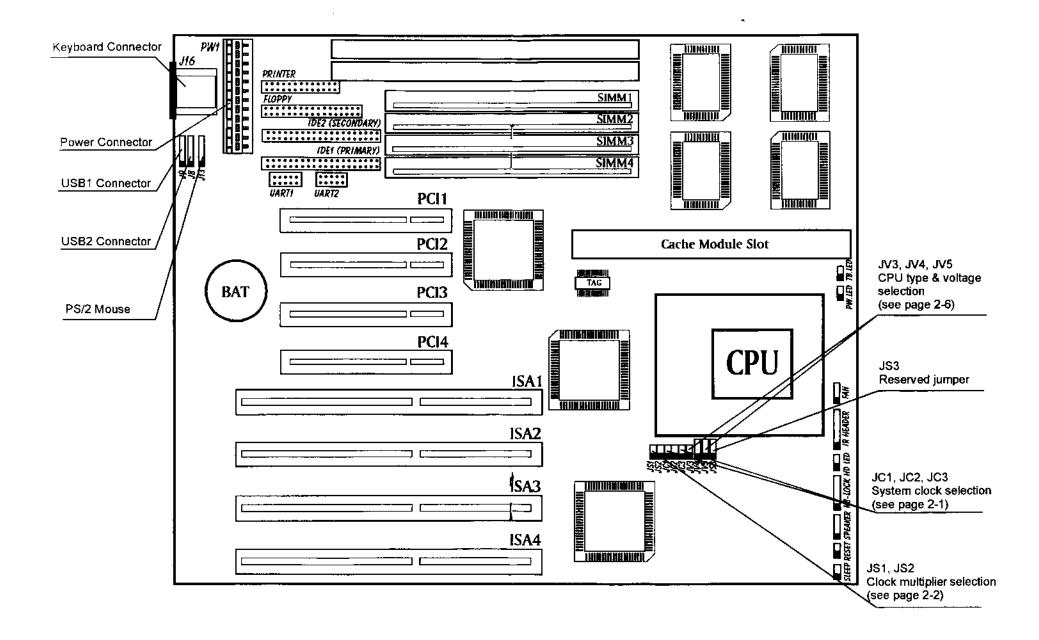


PENTIUM P5I430VX-280DM Explorer III





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Jumper Quick Setting

Install CPU

JC1, JC2 and JC3 are used for System Clock setting. JS1 and JS2 are used for CPU multiple clock setting. (Please refer to page 2-1 ~ page 2-5 in detail informations.)

	CPU FREQUENCY	Jsi	JS2	JC1	JC2	JC3
	75MHz	Open	Open	Close	Close	Open
	90MHz	Open	Open	Close	Open	Open
Intel	100MHz	Open	Ореп	Open	Open	Open
Pentium	120MHz	Open	Close	Close	Open	Open
	133MHz	Open	Close	Open	Open	Open
	150MHz	Close	Close	Close	Open	Open
	166MHz	Close	Close	Open	Open	Open
	180MHz	Close	Open	Close	Open	Ореп
	200MHz	Close	Open	Open	Open	Ореп
_	P120+ (100MHz)	Open	Close	Close	Close	Open
Cyrix 5x86	P133+ (110MHz)	Open	Close	Open	Close	Ореп
UXOO	P150+ (120MHz)	Open	Close	Close	Open	Open
	P166+ (133MHz)	Open	Close	Open	Open	Open
	*P200+ (150MHz)	Open	Close	Open	Open	Close
	PR75 (75MHz)	Open	Open	Close	Close	Open
AMD K5	PR90, PR120 (90MHz)	Open	Open	Close	Open	Open
	PR100, PR133 (100MHz)	Open	Ореп	Open	Open	Ореп
	PR150 (120MHz)	Open	Close	Close	Open	Open
	PR166 (133MHz)	Open	Close	Open	Open	Open

Remark:

If using SpeedEasy type P51430VX-280DM Explorer III Main Board, you can skip all the jumper settings including JC1, JC2, JC3, JS1, JS2, JS3, JV3, JV4 and JV5.

^{*} The maximum speed of Intel PCIset specification is 66MHz only, so it's not recommended by Intel to set system clock frequency up to J5MHz.

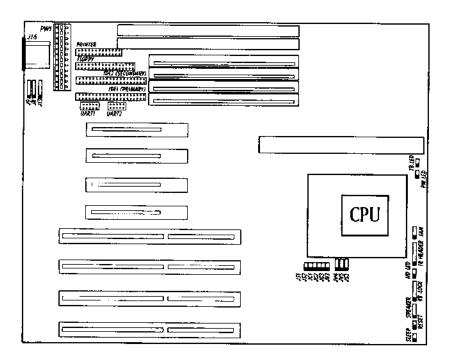
Select CPU Type & Voltage

JV3 and JV5 are used to select your CPU voltage.

(Please refer to page 2-6 in detail informations.)

Single Voltage CPU		l 	Dual '	Voltage	CPU		
	Voltage		I/O Voltage Core		re Volta	e Voltage	
	3.3V	3.5V	3.3V	3.5 V	2.5V	2.8V	2.9V
JVз	open	close	open	close			
JV5					open	1-2	2-3
JV4	1-2		<u>.</u>		2-3		

Note: J\$3(1-2) is reserved jumper.



Chapter 1 Introduction

Overview

P5I430VX-280DM Explorer III green main board provides a highly integrated solution for fully compatible, high performance PC/AT platforms, and supports Intel Pentium, Cyrix 6x86 and AMD K5 microprocessors. It features Write-Back Secondary Cache memory for 256KB/512KB in size. Flexible main memory size can be installed from 8MB up to 128MB DRAMs, so as to give full play to the advantages of the Pentium, Cyrix 6x86 and AMD K5 CPUs. The main board offers a wide range of interface to support integrated on-board IDE and on-board I/O function.

The current Green function is divided into three phases: Doze, Standby and Suspend.

Key Features

CPU

- Supports Intel Pentium 75, 90, 100, 120, 133, 150, 166, 180, 200 MHz and P54CTB, P55C(MMX) CPUs
- Supports Cyrix 6x86 100MHz (P120 Plus), 110MHz (P133 Plus), 120MHz (P150 Plus), 133MHz (P166 Plus)
 CPUs
- Supports AMD K5 CPU
- 2.5/2.8/2.9V circuit on board, ready for P55C and P55C compatible CPU support

Chipset

Intel's 82430 VX chipset

Main memory

- Supports 4x72pin SIMM modules and 2x168 pin DIMM module
- 64-bit data path for flexible memory size expanded from 8MB up to 128M DRAMs for SIMM socket
- Supports Fast Page mode DRAM and EDO DRAM for SIMM socket
- Supports from 8MB to 64MB 3.3V/unbuffered SDRAM DIMM or 3.3V/unbuffered EDO DIMM for DIMM slot

Cache memory - Provides 256KB L2 Pipelined Burst Cache on board, one cache module socket on board (COAST 3.0)

 Provides 6 kinds of cache sizes for user: Non-cache, 256K/512K cache on board, 256K/512K cache module, 256K cache on board + 256K cache module

On-board IDE - Supports 2 PCI Bus Master (Bus Master works as DMA Mode 2 type) IDE ports

- Supports PIO mode up to Mode 4 Timing
- Supports transfer rate up to 22 MByte/s
- Supports 2 Fast IDE interfaces for up to 4 IDE devices e.g. IDE hard disks and CD ROMs drives

Green function - Supports 3 Green modes: Doze, Standby and Suspend

On-board I/O - 4 x ISA Slots and 4 x PCI Slots

- Use NS Plug & Play IO chip PC87306
- Supports up to two 3.5" or 5.25" floppy drives 360K/720K/1.2M/1.44M/2.88M format
- All I/O ports can be enabled or disabled in BIOS
- Two high speed 16550 compatible UARTs (COM1/COM2/COM3/COM4 selectable) with 16-byte send/receive FIFOs and support MIDI mode
- One parallel port at I/O address 378H/278H/ 3BCH with additional bi-direction I/O capability and multi-mode selection (SPP/EPP/ECP) (IEEE1284 compliant)
- Provides protection circuit to prevent damage to the parallel port when a connected printer is powered up or operated at a higher voltage
- Real-time clock and keyboard controller built-in I/O chip
- Supports PS/2 mouse and PS/2 keyboard (optional)
- Supports IrDA TX/RX Header
- Supports USB (Universal Serial Bus) in specification

BIOS

- Licensed advanced AWARD BIOS. Supports Flash
ROM BIOS, Plug and Play ready, DMI ready.
Built-in NCR810 and Adaptec 7850 SCSI BIOS

Board size - 220mm x 280mm

Hardware Settings

There are some hardware settings on the board. They specify configuration options for various features. The settings are made using something called a 'jumper'. Jumpers on the system board provide information to your operation about installed options and system settings. A jumper is a set of two or more metal pins in a plastic base attached to the mainboard. A plastic jumper 'cap' with a metal plate inside fits over two pins to create an electrical contact between them. The contact establishes a hardware settings such as installing the CPU, selecting cache size.

Note: When you open a jumper, leave the plastic jumper cap attached to one of the pins so you don't lose it.

Jumpers and Caps







3-pin jumper



2-pin jumper

Graphic symbol

To rapidly give user a effective and direct way to set jumpers for your system, there are some diagrams used in the following chapters. All kind of jumper setting modes are simplified as the following relevant graphic symbols:









closed pin-1 and pin-2 of a jumper symbolizes as:





closed pin-2 and pin-3 of a jumper symbolizes as:





Jumper closed symbolizes as:





Jumper opened symbolizes as:



Remark:

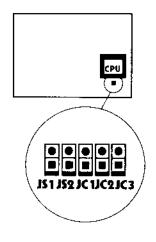
For SpeedEasy type P51430VX-280DM Explorer III main board, most jumper settings have been removed, you only need to set the related settings in BIOS Setup.

Chapter 2 Jumper Configuration

The main board offers a set of jumper settings to facilitate clock frequency adjustment and some important selections.

System Clock Selection

In this P5I430VX-280DM Explorer III main board, there are five selections of SC (System Clock). User has to set a group of jumpers as the following illustration to determine which system clock used.



System Clock 50MHz:



System Clock 55MHz:



JC 1 JC2 JC

System Clock 60MHz:



JC1JC2JC3

System Clock 75MHz:



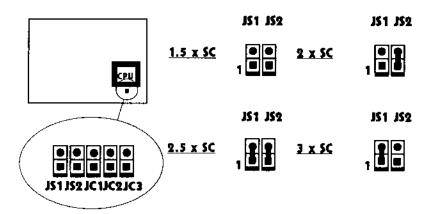
System Clock 66MHz:



Note: Please skip the above jumper settings if using SpeedEasy type P51430VX-280DM Explorer III Main Board.

Clock Multiplier Selection

The Intel Pentium CPU multiple clock settings are shown as belows: Note: SC -- System Clock.



CPU Frequency Selection

According to CPU's specification, set system clock and clock multiplier carefully. The following illustrations list almost all set of jumper settings for the major type CPUs.

For Intel Pentium 75~200MHz



Note: Please skip the above jumper settings If using SpeedEasy type P5I430VX-280DM Explorer III Main Board.

100= 1.5 x 66MHz:

120= 2 x 60MHz:



133= 2 x 66MHz:

150= 2.5 x 60MHz:



166= 2.3 x 66MHz:

 $180 = 3 \times 60MHz$:



200= 3 x 66MHz:

Note: Please skip the above jumper settings if using SpeedEasy type P51430VX-280DM Explorer III Main Board.

For Cyrlx 6x86 CPU

 $P120+(100MHz) = 2 \times 50MHz$:



 $P133+(110MHz)=2 \times 55MHz$:



P150+(120MHz)= 2 x 60MHz:



 $P166+(133MHz)=2 \times 66MHz$:



*P200+(150MHz)= 2 x 75MHz;



Note:

Please skip the above jumper settings if using SpeedEasy type P51430VX-280DM Explorer III Main Board.

*The maximum speed of Intel PCIset specification is 66MHz only, so it's not recommended by Intel to set system clock frequency up to 75MHz.

For AMD K5 CPU

PR75 (75MHz)= 1.5 x 50MHz:



PR90, PR120 (90MHz) = 1.5 x 60MHz:



PR100, PR133 (100MHz) = 1.5 x 66MHz:



 $PR150 (120MHz) = 2 \times 60MHz$:

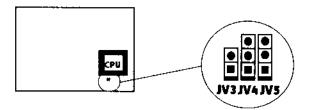


 $PR166 (133MHz) = 2 \times 66MHz$:

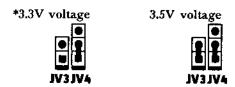


Note: Please skip the above jumper settings if using SpeedEasy type P51430VX-280DM Explorer III Main Board.

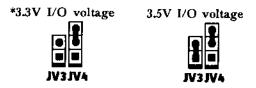
CPU Type & Voltage Selection



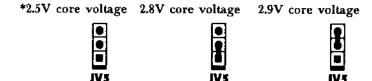
For single voltage CPU (P54C or compatible CPU):



For dual voltage CPU (P55C or compatible CPU): I/O voltage selection:



Core voltage selection:

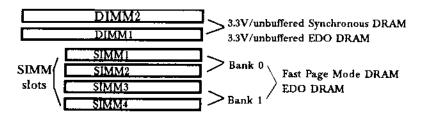


Note: If using SpeedEasy type P51430VX-280DM Explorer III Main Board, you can skip JV3, JV4 and JV5 jumpers setting.

Memory Configuration

The P5I430VX-280DM Explorer III main board provides 4 SIMM slots and 2 DIMM slots for providing a flexible memory size from 8MB up to 128MB main memory. Please do not plug in two different brands of SIMMs on a bank simultaneously.

This motherboard supports 72-pin SIMM of 4MB, 8MB, 16MB or 32MB. The DRAM can be 60ns/70ns Fast Page mode or EDO DRAM. SIMMs must be installed in pairs so that each bank contains two of the same size memory modules. Two slots support 3.3V Synchronous DRAM (SDRAM) or 3.3V unbuffered EDO DIMM of 8MB, 16MB, 32MB, 64MB.



If using DIMM together with SIMM, you must install DIMM as the following table:

DIMM1	DIMM2	SIMM1&2	SIMM3&4
None	None	Double-row/ Single-row/None	Double-row/ Single-row/None
Single-row	None	Single-row/None	Double-row/ Single-row/None
Double-row	None	None	Double-row/ Single-row/None
None	Single-row	Double-row/ Single-row/None	Single-row/None
None	Double-row	Double-row/ Single-row/None	None
Single-row	Single-row	Single-row/None	Single-row/None
Double-row	Single-row	None	Single-row/None
Single-row	Double-row	Single-row/None	None
Double-row	Double-row	None	None

Jumper Configuration

Note:

- 1. Memory setup is required in BIOS chipset setup page.
- The SDRAM that you install must have 5V signal tolerance when used with SIMM simultaneous, otherwise you may cause damage to the SDRAM.
- 3. Normally, AMB and 16MB SIMMs are single-row, SMB and 32MB SIMMs are double-row in market.
- 4. This main board supports up to 64MB SDRAM DIMM on one slot in logic. However, due to loading reason, it is not recommended to use 32MB single-row and 64MB double-row DIMM on 60 and 66MHz system clock.

Chapter 3 Connector Configuration

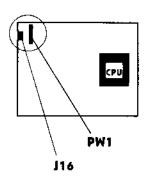
This section lists all connector pin assignments and port descriptions on the main board. The situations of the connectors and ports are illustrated in the following figures. Before inserting these connectors, please pay attention to their directions.

Power Connector (PW1)

PIN NUMBER	FUNCTION
1	POWER GOOD
2	+5V
3	+12V
4	-12V
5	GND
6	GND
7	GND
8	GND
9	-5 V
10	+5V
11	+5V
12	+5V

Keyboard Connector (J16)

PIN NUMBER	FUNCTION
1	CLOCK
2	DATA
3	NC
4	GND
5	+5 V

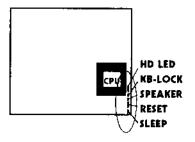


Hard Disk LED Connector (HD-LED)

PIN NUMBER	FUNCTION
1	LED ANODE
2	LED CATHODE

Keylock Connector (KB-LOCK)

PIN NUMBER	FUNCTION
1	+5V
2	NC
3	GND
4	KEYLOCK
5	GND



Speaker Connector (SPEAKER)

PIN NUMBER	FUNCTION
1	SPKDATA
2	GND
3	GND
4	VCC

Hardware Green (SLEEP)

SETTING	FUNCTION
CLOSE	HARDWARE GREEN (Close once)
OPEN	NORMAL

Reset Switch (RESET)

SETTING	FUNCTION
CLOSE ONCE	RESET THE SYSTEM
OPEN	NORMAL

IrDA Connector (IR HEADER)

PIN NUMBER	FUNCTION
1	VCC
2	NC
3	IRRX
4	GND
5	IRTX
6	vcc

FAN Connector (FAN)

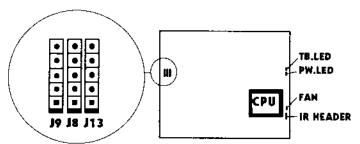
PIN NUMBER	FUNCTION
1	GND
2	+12V
3	GND

Turbo LED Connector (TB.LED)

PIN NUMBER	FUNCTION
1	LED ANODE
2	LED CATHODE

Power LED Connector (PW-LED)

PIN NUMBER	FUNCTION
1	LED ANODE
2	LED CATHODE



Connector Configuration

PS2 Mouse (J13)

PIN NUMBER	FUNCTION
1	DATA
2	CLOCK
3	GND
4	NC
5	+5V

USB1/USB2 Connector (J8/J9)

PIN NUMBER	FUNCTION
1	VCC
2	Key
3	DATA -
4	DATA+
5	GND

I/O Port Description

CONNECTOR	FUNCTION
PRIMARY	Primary IDE Port
SECONDARY	Secondary IDE Port
FLOPPY	Floppy Drive Port
PRINTER	Parallel Port
UART 1	COM1/COM2/COM3/COM4
UART 2	COM2/COM3/COM4/COM1

Chapter 4 AWARD BIOS Description

Entering BIOS Setup

Power on the computer, when the below message appears briefly at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press <Ctrl> + <Alt> + <Esc> keys.

Press to enter SETUP

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 1) will be appeared on the screen. The Main Menu allows you to select from twelve setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

ROM PCI/ISA BIOS (2A59GQ1A)				
CMOS SETUP UTILITY				
AWARD SOF	rware, inc.			
STANDARD CMOS SETUP	INTEGRATED PERIPHERALS			
BIOS FEATURES SETUP	SUPERVISOR PASSWORD			
CHIPSET FEATURES SETUP	USER PASSWORD			
POWER MANAGEMENT SETUP	IDE HDD AUTO DETECTION			
PNP/PCI CONFIGURATION	HDD LOW LEVEL FORMAT			
LOAD BIOS DEFAULT	SAVE & EXIT SETUP			
LOAD SETUP DEFAULT	EXIT WITHOUT SAVING			
Esc : Quit	↑↓→← : Select Item			
F10: Save & Exit Setup (Shift) F2: Change Color				
Time, Date, Hard Disk Type				

Figure 1 Main Menu For BIOS Setup

Note:

If you use SpeedEasy type P51430VX-280DM Explorer III main board, Main Menu of BIOS Setup will be described as the following illustration. Please refer to the "SpeedEasy type P51430VX-280DM Explorer III Main Board User's Manual" in detailed information.

ROM PCI/ISA BI	OS (2A59GQ1A)			
CMOS SETUP UTILITY				
AWARD SOF	TWARE, INC.			
STANDARD CMOS SETUP	PNP/PCI CONFIGURATION			
SPEEDEASY CPU SETUP	INTEGRATED PERIPHERALS			
BIOS FEATURES SETUP	USER PASSWORD			
CHIPSET FEATURES SETUP	IDE HDD AUTO DETECTION			
POWER MANAGEMENT SETUP	HDD LOW LEVEL FORMAT			
LOAD BIOS DEFAULT	SAVE & EXIT SETUP			
LOAD SETUP DEFAULT	EXIT WITHOUT SAVING			
Esc : Quit	↑↓			
F10 : Save & Exit Setup (Shift) F2 : Change Color				
Time, Date, Hard Disk Type				

Figure -2 Main Menu For SpeedEasy Type BIOS Setup

Standard CMOS Setup

Use the arrow keys to highlight the item, then use the <PgUp> or <PgDn> keys to select the value you want in each item.

Date (mm:dd:yy): Thu, May 14 1996								
Time (hh:mm:ss):	00:00:00	1						
HARD DISKS		-		HEAD	PRECOMP	LANDZ	SECTOR	MODE
Primary Master	: Auto	0	0	0	0	0	0	AUTO
Primary Slave	: Auto	0	0	0	0	0	0	AUTO
Secondary Master	: Auto	0	0	0	0	0	0	AUTO
Secondary Slave	: Auto	0	0	0	0	0	0	AUTO
Drive A	: 1.44M	l, 3.5 in	٦.		Ba	se Memo	ry: 640K	
Drive B	: None				Extended	Memory	: 7168K	
Video	: EGA/	VGA			Othe	r Memor	y:384K	
Halt On	: All E	rrors			Total	Memory	: 8192 K	-
ESC: Quit		Γυ- > 4 ·	: Se	lect Iten		PU/PD/-	-/- : Modify	<i>?</i>
F1: Help	1	Shift)	F2 : C	hange C	Color			

Figure 2 Standard CMOS Setup Menu

Hard Disk

Primary Master/Primary Slave/Secondary Master/Secondary Slave

The categories identify the types of 2 channels that have been installed in the computer. There are 45 predefined types and 4 user definable types are used for Enhanced IDE BIOS. Type 1 to Type 45 are predefined. Type "User" is user-definable. If your hard disk drive type is not matched with drive table or listed in it, you can use Type "User" to define your own drive type manually.

If you select Type "Auto", BIOS will Auto-Detect the HDD & CD-ROM drive at the POST stage and show the IDE for HDD & CD-ROM drive. If you select Type "User", related information is asked to be entered to the following items. Enter the information directly from the keyboard and press <Enter>:

If the controller of HDD interface is ESDI, the type shall be set to "1". If the controller of HDD interface is SCSI, the type shall be set to "None". Or directly set to "Auto" whatever the HDD interface is.

AWARD BIOS Description

CYLS	number of cylinders	HEAD	number of heads
PRECOMP	write precom	LANDZ	landing zone
SECTOR	number of sectors	MODE	HDD access mode

Video

The category selects the type of video adapter used for the primary system monitor. Although secondary monitors are supported, you do not have to select the type in Setup.

EGA/VGA	Enhanced Graphics Adapter/Video Graphic Array.
	For EGA, VGA, SEGA, or PGA monitor adapters.
CGA 40	Color Graphic Adapter, power up in 40 column mode.
CGA 80	Color Graphic Adapter, power up in 80 column mode.
MONO	Monochrome adapter, includes high resolution monochrome adapters.

Error Halt

The category determines whether the computer will stop if an error is detected during power up.

No errors	The system boot will not be stopped for any error that may be detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will be stopped and you will be prompted.
All, but Keyboard	The system boot will not stop for a keyboard error, but it will stop for all other errors.
All, but Diskette	The system boot will not stop for a disk error; but it will stop for all other errors.
All, but Disk/Key	The system boot will not stop for a keyboard or disk error; but it will stop for all other errors.

Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

Base Memory	The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system.
Extended Memory	The BIOS determines how much extended memory is presented during the POST.
Other Memory	This is memory that can be used for different applications. Most use for this area is Shadow RAM.
Total Memory	The system total memory is the sum of above memory.

BIOS Features Setup

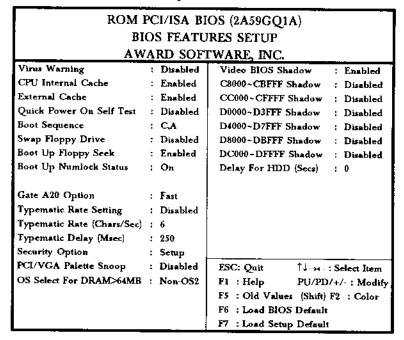


Figure 3 BIOS Features Setup

AWARD BIOS Description

The following pages tell you the options of each item and describe the meaning of each option.

Item	Option	Description
Virus Warning	Enabled	Activate automatically when the system boots up causing a warning message to appear when anything attempts to access the boot sector or hard disk partition table.
	Disabled	No warning message to appear when anything attempts to access the boot sector or hard disk partition table.
		Note: This function is available only for DOS and other OSes that do not trap INT13.
CPU Internal Cache	Enabled, Disabled	This item speeds up memory access. However, it depends on CPU/chipset design. The default value is enabled.
External Cache	Enabled	Enable external cache.
	Disable d	Disable external cache,
Quick Power On Self Test	Enabled	Enable quick POST. BIOS will shorten or skip some check items during POST to speed up POST after you power on the computer.
	Disabled	Normal POST.
Boot Sequence	C,A	The system will firstly search for hard disk drive then floppy disk drive.
	A,C	The system will firstly search for floppy disk drive then hard disk drive.
Swap Floppy Drive	Enabled	It will exchange the assignment of A&B floppy drives.
	Disabled	The assignment of A&B floppy drives are normal.
Boot Up Floppy Seek	Enabled	BIOS searches for floppy disk drive to determine if drive is ready for diskette read/write during booting.
	Disabled	Skip drive seeking to speed up system booting.
Boot Up Numlock Status	On	Keypad is used as number keys.
	Off	Keypad is used as arrow keys.
Gate A20 Option	Normal	The A20 signal is controlled by keyboard controller or chipset hardware.

	Fast	It is default. The A20 signal is controlled by Port 92 or chipset specific method.
Typematic Rate Setting	Enabled	Enable typematic rate and typematic delay programming.
	Disabled	Disable typematic rate and typematic delay programming. The system BIOS will use default value of these two items.
Typematic Rate (Chars/Sec)	6 ~ 30	Set the speed of the typematice rate (characters per second).
Typematic Delay (Msec)	250~1000	Set the time of the typematic delay
Security Option	System	The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt.
	Setup	The system will boot, but access to Setup will be denied if the correct password is not entered at the prompt.
		Note: To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.</enter>
PCI/VGA Palette Snoop	Enabled	Enable PCI/VGA palette snoop.
	Disabled	Disable PCI/VGA palette snoop.
OS Select For DRAM>64MB	Non-OS/2	If your operating system is not OS/2, please select this item.
	OS/2	If system DRAM is more than 64MB and operating system is OS/2, please select this item.
Video BIOS Shadow	Enabled	Video BIOS will be copied to RAM. Video Shadow will increase the video speed.
	Disabled	Video shadow is disabled.
C8000~CBFFF Shadow /	Enabled	Option shadow is enabled. Optional ROM will be copied to RAM by 16K byte per unit.
DC000~DFFFF Shadow	Disabled	The shadow function is disabled.
Delay For HDD(Secs)	0~15	This item allows you to set additional delay time (0~15 seconds) for HDD detection. If you find HDD detection problem, you may try to add delay time.

Chipset Features Setup

ROM PCI/ISA BIOS (2A59GQ1A) CHIPSET FEATURES SETUP				
AWA	ARD SOFTY	VARE, INC.		
Auto Configuration	: Enabled			
DRAM Timing	: 70ns			
DRAM RAS# Precharge Time	: 4			
DRAM R/W Leadoff Timing	: 6			
Fast RAS To CAS Delay	: 3			
DRAM Read Burst (EDO/FP)	: x333/x444			
DRAM Write Burst Timing	: x333			
Fast MA to RAS# Delay CLK	: 1			
Fast EDO Path Select	: Disabled			
Refresh RAS# Assertion	: 5 Clks			
ISA Bus Clock	: PCICLK/4			
System BIOS Cacheable	: Disabled			
Video BIOS Cacheable	: Disabled			
8 Bit I/O Recovery Time	: 1	ESC: Quit ^↓→←: Select Item		
16 Bit I/O Recovery Time	: 1	F1 : Help PU/PD/+/-: Modify		
Memory Hole At 15M-16M	: Disabled	F5 : Old Values (Shift)F2 : Color		
Peer Concurrency	: Enabled	F6 : Load BIOS Default		
Chipset NA# Asserted	: Enabled	F7 : Load Setup Default		

Figure 4 Chipset Features Setup

The following pages tell you the option of each item and describe the meanings of each option.

ltem	Option	Description
Auto Configuration	Enabled	Enable auto configuration of DRAM timing
	Disabled	Manually set DRAM timing.
		Warning: You'd better not set DRAM timing too fast which may affect your system stability
DRAM Timing	60ns	This item is of selected DRAM
	70ns	read/write timing. You must ensure that your SIMMs is as fast as 60ns, otherwise you have to select 70ns.

DRAM RAS#	Precharge Tin	re ~	ISA	Bus	Clock:	
-----------	---------------	------	-----	-----	--------	--

		All these items are about DRAM Timing and show-only for user reference.
System BIOS Cacheable	Enabled	Beside conventional memory, the system BIOS area is also cacheable.
	Disabled	The system BIOS area is not cacheable.
Video BIOS Cacheable	Enabled	Beside conventional memory, video BIOS area is also cacheable.
	Disabled	Video BIOS area is not cacheable.
8 Bit I/O Recovery Time	1~4	It is the ISA Bus 8 bit I/O operating recovery time.
	NA	8 bit I/O recovery time is not exist.
16 Bit I/O Recovery Time	1~8	It is the ISA Bus 16 bit I/O operating recovery time.
	NA	16 bit I/O recovery time is not exist.
Memory Hole at 15M~16M	Enabled	Memory Hole at 15M~16M is reserved for expanded PCI card.
	Disabled	Do not set this memory hole.
Peer Concurrency/ Chipset NA# Asserted	Enabled, Disabled	These items enabled will accelerate operation speed of PCI bus, thus benefit to the system performance. But perhaps don't support some expanded cards.

Power Management Setup

ROM PCI/ISA BIOS (2A59GQ1A)			
P	OWER MANAG	EMENT SETUP	
	AWARD SOFT	WARE, INC.	
Power Management	: Disable	** Wake up Events In Su	spend **
PM Control by APM	: Yes	IRQ3 (COM2)	: ON
Video Off Method	: V/H SYNC	IRQ4 (COM1)	: ON
	+ Blank	IRQ5 (LPT 2)	; ON
Video Off Option	: Susp, Stby->Off	IRQ6 (Floppy Disk)	; ON
Doze Mode	: Disabled	IRQ7 (LPT1)	: ON
Standby Mode	: Disabled	IRQ8 (RTC Alarm)	: OFF
Suspend Mode	: Disabled	IRQ9 (IRQ2 Redir)	: OFF
HDD Power Down	: Disabled	IRQ10 (Reserved)	: OFF
		IRQ11 (Reserved)	: OFF
** Wake up Events In Doz	e & Standby **	IRQ12 (PS/2 Mouse)	: ON
IRQ3 (Wake-Up Event)	: ON	IRQ13 (Coprocessor)	: OFF
IRQ4 (Wake-Up Event)	: ON	IRQ14 (Hard Disk)	: ON
IRQ8 (Wake-Up Event)	: ON	IRQ15 (Reserved)	: ON
IRQ12 (Wake-Up Event)	: ON	ESC: Quit ↑↓→←: S	elect Item
		F1 : Help PU/PD/+/	
•		F5 : Old Values (Shift)F	-
		F6 : Load BIOS Default	
		F7 : Load Setup Default	

Figure 5 Power Management Setup

The following pages tell you the option of each item and describe the meanings of each option.

item	Option	Description
Power Management	Disabled	Global Power Management will be disabled.
	User Define	Users can configure their own Power Management Timer.
	Min Saving	Pre-defined timer values are used such that all timers are in their MAX values.
	Max Saving	Pre-defined timer values are used such that all timers are in their MIN values.
PM Control by APM	No	System BIOS will ignore APM when power managing the system.

	Yes	System BIOS will wait for APM's prompt before it enter any PM mode, such as Standby or Suspend. Note: If APM is installed (choose "Yes"), and if there is a task running, even the timer is time out, the APM will not prompt the BIOS to put the system into any power saving mode. But if APM is not installed (choose "No"), this option has no effect.
Video Off Method	Blank Screen	The system BIOS will only blank off the screen when disabling video.
	V/H SYN C+ Blank	In addition to Blank Screen, BIOS will also turn off the V-SYNC & H-SYNC signals from VGA cards to monitor.
	DPMS	This function is enabled for only the VGA card supporting DPMS.
		Note: Green monitors detect the V/H-SYNC signals to turn off its electron gun.
Video Off Option	Always On	System BIOS will never turn off the screen.
	Suspend->Off	Screen off when system is in Suspend mode.
	Susp, Stby->Off	Screen off when system is in Standby or Suspend mode.
	All Modes->Off	Screen off when system is in Standby or Suspend mode.
Doze Mode	Disabled	The system will never enter Doze mode.
	1 Min ~ 1 Hr	Define the continuous idle time before the system entering Doze mode. If any item defined in "Wake Up Events In Doze & Suspend" is On and activated, the system will be waken up.
Standby Mode	Disabled	The system will never enter Standby mode.
	1 Min ~ 1 Hr	Define the continuous idle time before the system entering Standby mode. If any item defined in "Wake Up Events In Doze & Standby" is On and activated, the system will be waken up.
Suspend Mode	Disabled	The system will never enter Suspend mode.

AWARD BIOS Description

	1 Min ~1 Hr	Define the continuous idle time before the system entering Suspend mode. If any item defined in "Wake Up Events In Suspend" is On and activated, the system will be waken up.
HDD Power Down	Disabled	HDD's motor will not be off.
	1Min-15Min	Define the continuous HDD idle time before the HDD entering power saving mode (motor off).
IRQ3~12 (Doze & Standby)	OFF	The specified event's activity will not make the system wake up from Doze & Standby mode.
	ON	The specified event's activity will make the system wake up from Doze & Standby mode.
IRQ3 ~ IRQ15 (Suspend)	OFF	The specified event's activity will not make the system wake up from Suspend mode.
	ON	The specified event's activity will make the system wake up from Suspend mode.

PNP/PCI Configuration

ROM PCI/ISA BIOS (2A59GQ1A)				
PNP/PCI CONFIGURATION				
AWARD SOFTWARE, INC.				
Resources Controlled By	: Manual	PCI IRQ Active By	: Level	
Force Update ESCD	: Disabled	PCI IDE IRQ Map To	: PCI-AUTO	
		Primary IDE INT#	: A	
IRQ-3 assigned to	: Legacy ISA	Secondary IDE INT#	: B	
IRQ-4 assigned to	: Legacy ISA			
IRQ-5 assigned to	: PCL/ISA Pap			
IRQ-7 assigned to	: Legacy ISA	•		
IRQ-9 assigned to	: PCI/ISA PnP			
IRQ-10 assigned to	: PCI/ISA PhP			
IRQ-11 assigned to	: PCI/ISA PhP			
IRQ-12 assigned to	: PCI/ISA PnP			
IRQ-14 assigned to	: Legacy ISA			
IRQ-15 assigned to	: Legacy ISA			
DMA-0 assigned to	: PCI/ISA PnP			
DMA-1 assigned to	: PCI/ISA PhP			
DMA-3 assigned to	: PCI/ISA PnP	ESC: Quit ↑↓→←	Select Item	
DMA-4 assigned to	: PCI/ISA PaP		+/-: Modify	
DMA-5 assigned to	: PCI/ISA PnP	F5 : Old Values (Shift)	, ,	
DMA-6 assigned to	: PCI/ISA PnP	F6 : Load BIOS Default		
DMA-7 essigned to	: PCI/ISA PnP	F7: Load Setup Defau	lt	

Figure 6 PNP/PCI Configuration Setup

The following pages tell you the options of each item and describe the meaning of each option.

ltem	Option	Description
Resources Controlled By	Manual	Assign system resources (IRQ and DMA) manually by user.
	Auto	Assign system resources (IRQ and DMA) automatically by BIOS.
Force Updating ESCD	Enabled	The system BIOS will force updating ESCD once, then automatically set this item Disable.
	Disabled	Disable force update ESCD function.
IRQ-3 ~ IRQ-15 assigned to	Legacy ISA	The specified IRQ-x will be assigned to ISA only.

	PCI/ISA PnP	The specified IRQ-x will be assigned
		to ISA or PCL
DMA-0 ~ DMA-7 assigned to	Legacy ISA	The specified DMA-x will be assigned to ISA only.
	PCI/ISA PnP	The specified DMA-x will be assigned to ISA or PCI.
PCI IRQ Active By	Level, Edge	To tell the chipset that the IRQ signals input is level or edge trigger.
PCI IDE IRQ Map To	PCI-AUTO	The BIOS will scan for PCI IDE devices and determine the location of the PCI IDE device.
	PCI-SLOT 1~4	The BIOS will assign IRQ 14 for primary IDE INT# and IRQ15 for secondary IDE INT# for the specified slot.
	ISA	The BIOS will not assign any IRQs even if PCI IDE card is found. Because some IDE cards connect the IRQ 14&15 directly from ISA slot through a card.
Primary IDE INT#	$A \sim D$	To tell which INT# the PCI IDE card is used for its interrupt of 1st IDE channel.
Secondary IDE INT#	A~D	To tell which INT# the PCI IDE card is used for its interrupt of 2nd IDE channel.

Load BIOS Defaults

The BIOS Defaults is conventional and safe setting.

Load Setup Defaults

The Setup Defaults is common and efficient setting.

Integrated Peripherals

ROM PCI/ISA BIOS (2A59GQ1A)			
INT	EGRATED I	PERIPHERALS	
AV	VARD SOFT	WARE, INC.	
IDE HDD Block Mode	: Enabled		
IDE Primary Master PIO	: Auto		
IDE Primary Slave PIO	: Auto		
IDE Secondary Master PIO	: Auto	i i	
IDE Secondary Slave PIO	: Auto	j	
On-Chip Primary PCI IDE	: Enabled		
On-Chip Secondary PCI IDE	: Enabled		
PCI Slot IDE 2nd Channel	: Disabled		
Onboard FDC Controller	: Enabled		
Onboard Serial Port 1	: COM1/3F8		
Onboard Serial Port 2	: COM2/2F8		
Onboard Parallel Port	: 378H/TRQ7	i	
Parallel Port Mode	: Compatible		
ECP Mode Use DMA	: 1	ESC: Quit ↑↓ → : Select Item	
EPP Version	: 1.7	F1: Help PU/PD/+/-: Modify	
Infrared Duplex Type	: Disabled	F5 : Old Values (Shift)F2 : Color	
		F6: Load BIOS Default	
		F7: Load Setup Default	

Figure 7 Integrated Peripherals

The following pages tell you the options of each item and describe the meaning of each option.

item	Option	Description
IDE HDD Block Mode	Enabled	Allow IDE HDD read/write several sectors one time.
	Disabled	IDE HDD only reads/writes a sector one time.
IDE Primary /Secondary Master /Slave PIO	Mode 0~4	Define the IDE primary/secondary master/slave PIO mode.
	Auto	The IDE PIO mode is defined according to auto-detect.

AWARD BIOS Description

On-chip Primary/ Secondary PCI IDE	Enabled	On-chip primary/secondary PCI IDE port is enabled.
	Disabled	On-chip primary/secondary PCI IDE port is disabled.
PCI Slot IDE 2nd Channel	Enabled	The second IDE channel on PCI slot is enabled.
	Disable	The second IDE channel on PCI slot is disabled.
Onboard FDC Controller	Enabled	Onboard floppy disk is enabled.
	Disabled	Onboard floppy disk is disabled.
Onboard Serial Port 1/2	COM1/3F8,	Define onboard serial port address.
	COM2/2F8,	
	COM3/3E8,	
	COM4/2E8	
	Disabled	Onboard serial port is disabled.
Onboard Parallel Port	378/IRQ7, 3BC/IRQ7, 278/IRQ5, 378/IRQ5	Define onboard parallel port address and IRQ channel.
	Disabled	Onboard parallel port is disabled.
Parallel Port Mode	Compatible, Extended, EPP, ECP	Define the parallel port mode is Standard Parallel Port (SPP), Enhanced Parallel Port.(EPP), or Extended Capabilities Port (ECP). Both Compatible mode and Extended mode are SPP mode, except that the later has a latchable buffer between I/O data pins and CPU.
ECP Mode Use DMA	<i>I</i> , <i>3</i>	Define channel 1 or channel 3 used for DMA.
EPP Version	1.7, 1.9	Define EPP version,
Infrared Duplex	Disabled, Half, Full	Define Infrared communication mode: disabled, half-duplex, or full-duplex.

Supervisor/User Password

When you select Supervisor/User Password function, the following message will appear at the center of the screen to assist you in creating a password.

ENTER PASSWORD

Type the password, up to eight characters, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable password, just press <Enter> when you are prompted to enter password. The following message will confirm the password being disabled. If both Supervisor and User Password are disabled, the system will boot and you can enter CMOS Setup freely.

PASSWORD DISABLED

If you select "System" at "Security Option" of "BIOS Features Setup" Menu, you will be prompted for the password every time the system is rebooted or any time you try to enter "CMOS Setup".

If you select "Setup" at "Security Option" of "BIOS Features Setup" Menu, you will be prompted only when you try to enter "CMOS Setup".

Supervisor Password has higher priority than User Password. You can use Supervisor Password when booting system or entering "CMOS Setup" to modify all settings, Also you can use User Password when booting system or entering "CMOS Setup" but can not modify any setting if Supervisor Password is enabled.

IDE HDD Auto Detection

The Enhance IDE features was included in all Award BIOS. Below is a brief description of this features.

ROM/PCI/ISA BIOS (2A59GQ1A) CMOS SETUP UTILITY AWARD SOFTWARE, INC.

HARD DISKS TYPE SIZE CYLS HEAD PRECOMP LANDZ SECTOR MODE
Primary Master:

Select Primary Master Option (N = Skip) : N							
OPTIONS	SIZE	CYLS	HEADS	PRECOMP	LANDZONE	SECTOR	MODE
1(Y)	516	1120	16	65535	1119	59	NORMAL
2	516	524	32	0	1119	63	LBA
3	516	560	3 2	65536	1119	59	LARGE

Note: Some OSes (like SCO UNIX) must use "NORMAL" for installation

Figure 8 IDE HDD Auto Detection

1. Setup Changes

With auto-detection

- BIOS setup will display all possible modes that is supported by the HDD including NORMAL, LBA & LARGE.
- If HDD does not support LBA modes, no "LBA" option will be shown.
- If number of cylinders is less than or equal to 1024, no "LARGE" option will be shown.
- Users can select a mode which is appropriate for them.

With Standard CMOS Setup

	CYLS	HEADS	PRECOMP	LAND ZONE	SECTOR	MODE
Drive C: User(516MB)	1120	16	65535	1119	59	NORMAL
Drive D: None(203MB)	684	16	65535	685	38	

When HDD type is in "user" type, the "MODE" option will be opened for user to select their own HDD mode.

2. HDD Modes

The Award BIOS supports 3 HDD modes: NORMAL, LBA and LARGE, and Auto detect.

NORMAL

Generic access mode in which neither the BIOS nor the IDE controller will make any transformation during accessing. The maximum number of cylinder, head and sectors for NORMAL mode are 1024, 16 and 63.

If user set his HDD to NORMAL mode, the maximum accessible HDD size will be 528 Megabytes even though its physical size may be greater than that.

LBA (Logical Block Addressing) mode

A new HDD accessing method to overcome the 528 Megabyte bottleneck. The number of cylinders, head and sectors shown in setup may not be the number physically contained in the HDD.

During HDD accessing, the IDE controller will transform the logical address described by sector, head and cylinder number into its own physical address inside the HDD. The maximum HDD size supported by LBA mode is 8.4 Gegabytes.

LARGE mode

Some IDE HDDs contain more than 1024 cylinder without LBA support (in some cases, user do not wait LBA). The Award BIOS provides another alternative to support these kinds of HDD.

BIOS tricks DOS (or other OS) that the number of cylinders is less than 1024 by dividing it by 2. At the same time, the number of heads is multiplied by 2. A reverse transformation process will be made inside INT13h in order to access the right HDD address.

Auto detect

If using Auto detect, the BIOS will auto detect IDE hard disk mode and set it to one kind of HDD modes.

3. Remark

To support LBA or LARGE mode of HDDs, there must be some software involved. All these software are located in the Award HDD Service Routine (INT 13h). It may be failed to access a HDD with LBA (LARGE) mode selected if you are running under a Operating System which replaces the whole INT 13h.

Hard Disk Low Level Format Utility

This Award Low-Level-Format Utility is designed as a tool to save your time formatting your disk. The Utility automatically looks for the necessary information of the drive you selected. Utility also searches for bad tracks and list them for your reference.

Shown below is the Main Menu after you enter into the Award Low-Level-Format Utility.

77 10:17							
Hard Disk Low-		nat Uti	lity _	ļ	NO. CY	LS HEAD	
SELECT 1							
BAD TRA	CK LIST						
PREFORM	TAN			Ĺ			
Current sele) : 0				
	SIZE	CYL	HEAD	PRECOMP	LANDZ	SECTORS	MODE
Primary Master	: 40MB	977	5	300	977	17	NORMAL
Primary Slave	: None	0	0	0	0	0	AUTO
Secondary Master	: None	0	0	0	0	0	AUTO
Secondary Slave	: None	0	0	0	0	0	AUTO
Up/Down · Select item Enter · A			ccept	ESC - Ex	it/Abort		
				992-1994 All			

Figure 9 Hard Disk Low Level Format Utility

SELECT DRIVE

Select from installed hard disk drive C or D. List at the bottom of the screen is the drive automatically detected by the utility.

BAD TRACK LIST

Auto scan bad track

The utility will automatically scan bad tracks and list the bad tracks in the window at the right side of the screen.

Add bad track

Directly type in the information of the known bad tracks in the window at the right side of the screen.

Modify bad track

Modify the information of the added bad tracks in the window at the right side of the screen.

Delete bad track

Delete the added bad tracks in the window at the right side of the screen.

Clear bad track table

Clear the whole bad track list in the window at the right side of the screen.

PREFORMAT

Interleave

Select the interleave number of the hard disk drive you wish to perform low level format. You must select from 1 to 8. Check the documentation that came with the drive for the correct interleave number, or select 0 for utility automatic detection.

Auto scan bad track

This allows the utility to scan bad track or not.

Start

Press <Y> to start low level format.

Power-On Boot

If you have made all the changes to CMOS values and the system cannot boot with the CMOS values selected in Setup, restart the system by turning it OFF then ON or pressing the "RESET" button on the system case.

You may also restart by simultaneously press <Ctrl>, <Alt>, and <Delete> keys.

Appendix BIOS Upgrade Diskette

You can use this diskette to update your BIOS.

For the most update and additional information about BIOS upgrade, please refer to "README" in the "BIOS Upgrade Diskette".

Warning: Before you update your BIOS, you should look over the "README" file to avoid making mistake.

SpeedEasy P51430VX-280DM Explorer III



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SpeedEasy Quick Setup

Procedures:

- 1. Insert the CPU correctly.
- 2. Plug in others Configurations and restore the system.
- 3. Power on the system and press key immediately to enter BIOS Setup.
- 4. Enter "SpeedEasy CPU Setup" menu to set up CPU speed.

Note: If you don't set CPU speed, your system will run at default setting (75MHz for Pentium and AMD CPU, 100MHz for Cyrlx 6x86CPU).

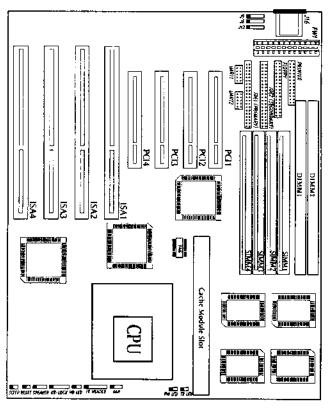
5. Save and exit BIOS Setup, then your system can boot successfully as you expected.

Identify SpeedEasy

SpeedEasy Type Main Board Layout

Note: Before setting up your main board, you must first confirm whether your P51430VX-280DM Main Board is SpeedEasy type or common type.

The following jumperless board is P5I430VX-280DM main board layout.



SpeedEasy Technology Introduction

What is SpeedEasy Technology

SpeedEasy is a new technology and Plug & Play version in CPU speed setup. It brings the first jumperless main board in the world.

1. It's quite easy:

- . Just plug a CPU in the socket, and it can play without any jumper setting.
- . Select CPU speed as your favourite anywhere anytime without opening the system case.

2. It's more safe:

- . You have no chance to make any mistake for the jumper setting.
- . BIOS can automatically set CPU core & I/O voltage according to the CPU model.

How To Build a SpeedEasy System

Just like a normal system, plug CPU, DRAM, and other adapter cards in the main board, then connect the cables of all devices, power supply and etc.. But you need not set any jumpers for CPU frequency, voltage ...etc. on the main board.

When all the devices are rightly connected, you can turn power on, then system will boot at a safe low speed.

On any one of the following conditions, system will run at safe low speed:

- . The first time for system setup;
- . CPU type or model has been changed;
- . Press while you turn power on;
- . CMOS lost.

Remarks:

 is a hot-key to let your system run at safe low speed.
You should press it before you power on your system,
otherwise the system will only let you enter BIOS Setup.

SpeedEasy Usage

Entering SpeedEasy Main Menu

Power on the computer, when the below message appears briefly at the bottom of the screen during the POST (Power On Self Test), Press key or simultaneously press <Ctrl> + <Alt> + <Esc> keys.

Press to enter SETUP

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 1) will be appeared on the screen. The Main Menu allows you to select from twelve setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

ROM PCI/ISA BIOS (2A59GQ1E) CMOS SETUP UTILITY				
AWARD SOF	FTWARE, INC.			
STANDARD CMOS SETUP	PNP/PCI CONFIGURATION			
SPEEDEASY CPU SETUP	INTEGRATED PERIPHERALS			
BIOS FEATURES SETUP	USER PASSWORD			
CHIPSET FEATURES SETUP	IDE HDD AUTO DETECTION			
POWER MANAGEMENT SETUP	HDD LOW LEVEL FORMAT			
LOAD BIOS DEFAULT	SAVE & EXIT SETUP			
LOAD SETUP DEFAULT	EXIT WITHOUT SAVING			
Esc ; Quit	↑↓-+← : Select Item			
F10 : Save & Exit Setup (Shift) F2 : Change Color				
Time, Date, Hard Disk Type				

Figure-1 BIOS Setup Main Menu

SpeedEasy CPU Setup Menu

Select <SpeedEasy CPU Setup> item from the main menu and enter the sub-menu:

ROM PCI/ISA BIOS (2A59GQ1E) SPEEDEASY CPU SETUP QDI Innovative Technology			
CPU Type	: Intel Pentium (P55C)		
CPU Speed	: 166MHz (66×2.5)		
CPU I/O Voltage	: 3.3		
CPU Core Voltage	: ; 2.8		
	specify a frequency complies with the		
Option with '*' spe	cify an extended one.		
If you don't under	stand exactly what		
it means, don't use	the option.	ESC: Quit	↑↓→← : Select Item
			PU/PD/+/-: Modify
			(Shift) F2 : Color

Figure -2 SpeedEasy CPU Setup Menu

For SpeedEasy main board, BIOS will provide you a set of basic values for your CPU selection without configuring any jumpers. To make your system run as fast as possible, you can manually increase CPU Speed values in "CPU Speed" item on "SpeedEasy CPU Setup" menu screen.

Note: In addition, if your system can not boot up again because of wrong CPU setting, you can hold down the hot-key while power on the system. The system will reboot and run at basic values.

Warning:

You'd better not to set CPU frequency higher than its working frequency. Otherwise, we aren't responsible for any damage happened.

SpeedEasy Type Introduction

The following pages tell you the options of each item and describe the meaning of each option.

CPU Type

BIOS can automatically detect your CPU model, so this item is shown only, and you needn't manually select it. These models are:

- . Intel Pentium (P54C, P54CTB and P55C)
- . AMD
- . Cyrix

CPU Speed

In fact, this item is to set CPU frequency. Different model of CPU have different working frequency, so you should select CPU speed according to your CPU brand and type.

Note: CPU speed = System Clock x Clock Multiplier

1. For Intel Pentium CPU, the following CPU speed can be selected:

75MHz (50x1.5)	83MHz (55x1.5)*
90MHz (60x1.5)	100MHz (66x1.5)
100MHz (50x2)*	110MHz (55x2)*
120MHz (60x2)	125MHz (50x2.5) ³
133MHz (66x2)	150MHz (60x2.5)
150MHz (50x3)*	166MHz (66x2.5)
180MHz (60x3)	200MHz (66x3)

For Cyrix 6x86 and 6x86L CPUs, the following CPU speed can be selected:

```
P120+, 100MHz (50x2) P133+, 110MHz (55x2)
P150+, 120MHz (60x2) P166+, 133MHz (66x2)
P200+, 150MHz (75x2) P200+, 150MHz (50x3)*
```

3. For AMD-K5 CPUs, the following CPU speed can be selected:

PR75, 75MHz (50x1.5)	PR83, 83MHz (55x1.5)
PR90, 90MHz (60x1.5)	PR100, 100MHz (66x1.5)
PR100, 75MHz (50x1.5)	PR120, 90MHz (60x1.5)
PR133, 100MHz (66x1.5)	PR150, 120MHz (60x2)
PR166, 133MHz (66x2)	

^{&#}x27;*': it is not recommended to select this option for CPU speed.

CPU Voltage

If "CPU Voltage" is shown on "SpeedEasy CPU Setup" menu, that means your CPU is single voltage CPU.

Please notice that the CPU voltage of AMD and Cyrix CPU can be changed according to CPU mark, but the CPU voltage of Intel CPU is automatically set by BIOS, and you can not change it.

- 3.5V For P54C CPU
 - AMD-K5 CPU (BIOS default setting, but you may change it to 3.3V according to CPU mark).
- 3.3V For P54CTB CPU;

 Cyrix 6x86 CPU (BIOS default setting, but you may change it to 3.5V according to CPU mark);

CPU I/O Voltage

When "CPU I/O Voltage" and "CPU Core Voltage" are shown on "SpeedFasy CPU Setup" menu, that means your CPU is dual voltage CPU.

BIOS can automatically set "CPU I/O Voltage" and "CPU Core Voltage" for your CPU. But you still can change these parameters according to CPU mark.

- 3.5V Reserved
- 3.3V For P55C CPU

CPU Core Voltage

For dual voltage CPU:

- 2.9V Reserved
- 2.8V For P55C CPU
- 2.7V Reserved
- 2.5V Reserved

Remask:

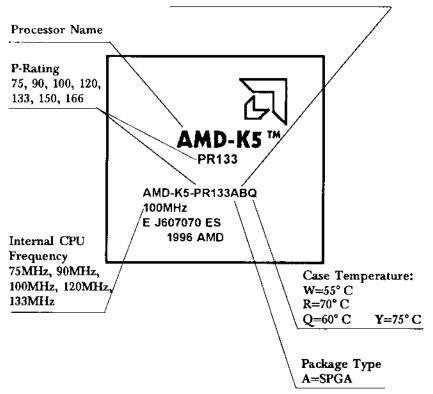
For Intel CPU, CPU I/O voltage and core voltage are automatically set by BIOS, user can not change these settings.

For AMD and Cyrix CPU, the voltage is shown as default settings but user can change these according to the following Appendix A and B.

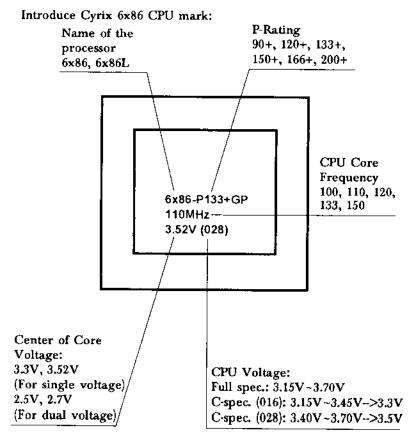
Appendix A.

Introduce AMD-K5 CPU mark:

Operating Voltage: B=3.45V~3.60V->3.5V C=3.30V~3.465->3.3V F=3.135V~3.465V->3.3V G=x/y H=2.86V~3.00V/3.30V~3.465V->2.9/3.3 J=2.57V~2.84V/3.30V~3.465V->2.7/3.3 K=2.38V~2.63V/3.30V~3.465V->2.5/3.3



Appendix B.



In page 5, the SpeedEasy CPU Setup Menu has been changed as follows:

ROM PCI/ISA BIOS (2A59GQ1E) SPEEDEASY CPU SETUP QDI Innovative Technology			
CPU Model	: Intel Pentium MMX		
Speed Mode	: SpeedEasy	Warning: B	e sure your selection
CPU Speed	: 166MHz	is	right. CPU over speed
		w	ill be dangerous.
CPU Voltage Ctrl : Auto			
CPU I/O Voltage: 3.3V			
CPU Core Voltage: 2.8V			
		ESC: Quit	↑↓→← : Select Item
			PU/PD/+/-: Modify
			(Shift) F2 : Color

Figure -2 SpeedEasy CPU Setup Menu

The following pages tell you the options of each item and describe the meaning of each option.

ltem	Option	Description
CPU Model:		BIOS can automatically detect your CPU model and you need not manually select it.
Speed Mode:	SpeedEasy	Sets CPU speed according to your CPU mark.
	Jumper Emulation	If you select jumper Emulation mode, SpeedEasy CPU Setup Menu will present "Bus Clock" and "Multiplier" items for your selection as follows.
Bus Clock:	50MHz	
	55MHz	
	60MHz	
	66MHz	
	75MHz	
Multiplier:	x 1.5, BF1/BF0=1/I	Left table is only for Pentium
	x 2, BF1/BF0=1/0	CPU. The other CPU Manufac- turers' definitions of BF1/BF0
	x 2.5, BF1/BF0=0/0	should be referred to your
	x 3, BF1/BF0=0/1	CPU Vendor.
CPU Speed:	75MHz-200MHz	It is for Pentium CPU

SpeedEasy Type Introduction

P120+-P200+

PR75 -PR200

+ It is for Cyrix CPU It is for AMD CPU

K6 CPU M2 CPU

CPU Voltage Ctrl: Auto

BIOS sets your CPU Voltage

automatically.

Manual

Sets your CPU voltage by manual mode. If your CPU is single voltage, only one voltage can be changed. If your CPU is dual voltage, you should set

 V_{10} and V_{Core} .

■ Warning:

- 1. You'd better not to set CPU frequency higher than its working frequency. Otherwise, it is dangerous for CPU over speed.
- 2. If you use Jumper Emulation Mode, you should make sure that you understand each parameter. Otherwise, use SpeedEasy Mode please. It is possible that your settings are out of the CPU Specification.



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