# **GA - 586IS**

**USER'S MANUAL** 

**PCI - ISA SOLUTION** 

PENTIUMÄ PCI - ISA BUS MAINBOARD

# **REV. 2 First Edition**

3	
February 3, 1994 Taipei, Taiwan	
IBM PC/AT, PC/XT are trademarks of International Business Machine Corporation. PENTIUM is a trademark of Intel Corporation. AWARD is a trademark of Award Software, Inc. MS-DOS WINDOWS NT are registered trademarks of Microsoft Corporation. UNIX is a trademark of Bell Laboratories.	
The author assumes no responsibility for any errors or omissions which man appear in this document nor does it make a commitment to update the information contained herein.	

# **TABLE OF CONTENTS**

1.	INTR	ODUCTION	5
	1.1.	PREFACE	5
	1.2.	KEY FEATURES	5
	1.3.	PERFORMANCE LIST	6
	1.4.	BLOCK DIAGRAM	7
	1.5.	INTRODUCE THE PCI - BUS	7
	1.6.	FEATURES	8
2.	SPEC	CIFICATION	9
	2.1.	HARDWARE	9
	2.2.	SOFTWARE	9
	2.3.	ENVIRONMENT	. 10
3.	HAR	DWARE INSTALLATION	. 11
	3.1.	UNPACKING	.11
	3.2.	MAINBOARD LAYOUT	12
	3.3.	QUICK REFERENCE FOR JUMPERS & CONNECTORS	12
	3.4.	DRAM INSTALLATION	.14
	3.5.	SRAM INSTALLATION AND JUMPERS SETUP	16
	3.6.	CPU INSTALLATION AND JUMPERS SETUP	16
	3.7.	CMOS RTC & ISA CFG CMOS SRAM	. 17
	3.8.	SPEAKER CONNECTOR INSTALLATION	. 17
	3.9.	POWER LED & KEY LOCK CONNECTOR INSTALLATION	.18
	3.10.	TURBO SWITCH CONNECTOR INSTALLATION	.18
	3.11.	TURBO LED CONNECTOR INSTALLATION	.18
	3.12.	HARDWARE RESET SWITCH CONNECTOR INSTALLATION	.18
	3.13.	PERIPHERAL DEVICE INSTALLATION	.18
4.	BIOS	CONFIGURATION	20
	4.1.	ENTERING SETUP	20
	4.2.	CONTROL KEYS	20
	4.3.	GETTING HELP	21
	4.	3.1. Main Menu	21
	4.	3.2. Status Page Setup Menu / Option Page Setup Menu	21
	4.4.	THE MAIN MENU	21
	4.5.	STANDARD CMOS SETUP MENU	23

	4.6.	BIOS FEATURES SETUP	28
	4.7.	CHIPSET FEATURES SETUP	32
	4.8.	PCI SLOT CONFIGURATION	34
	4.9.	LOAD SETUP DEFAULTS	35
	4.10.	PASSWORD SETTING	36
	4.11.	IDE HDD AUTO DETECTION	37
	4.12.	SAVE & EXIT SETUP	38
	4.13.	EXIT WITHOUT SAVING	38
	4.14.	KEYBOARD SETTING FUNCTION	39
5.	AT T	ECHNICAL INFORMATION	40
	5.1.	I/O BUS CONNECTOR PIN OUT	40
	5.	1.1. ISA SLOT PIN OUT	40
	5.	1.2. PCI - BUS SLOT PIN OUT	41
	5.2.	I/O & MEMORY MAP	42
	5.3.	TIMER & DMA CHANNELS MAP	42
	5.4.	INTERRUPT MAP	43
	5.5.	RTC & CMOS RAM MAP	43
ΑF	PENE	DIX A: POST MESSAGE	45
ΑF	PENE	DIX B: POST CODES	51
ΑF	PENE	DIX C: BIOS DEFAULT DRIVE TABLE	54
ΑF	PENI	DIX D: PROBLEM SHEET	56

# 1. INTRODUCTION

# 1.1. PREFACE

Welcome to use the GA - 586IS motherboard. The motherboard is a 256 / 512 KB CACHE PENTIUM™ CPU based PC / AT compatible system with ISA bus and PCI Local Bus, and has been designed to be the fastest PC / AT system. There are some new features allow you to operate the system with just the performance you want.

This manual also explains how to install the motherboard for operation, and how to set up your CMOS CONFIGURATION with BIOS SETUP program.

# 1.2. KEY FEATURES

Pentium™ based PC / AT compatible mainboard with PCI - ISA Bus.
4 Master / Slave PCI Bus slots, 4 ISA Bus slots.
Supports Pentium / P5T running at 60 / 66 MHz.
Supports true 64 bits CACHE and DRAM access mode.
Supports 273 Pins (Socket 4) ZIF white socket on board.
Supports 256 / 512 KB second cache memory.
CPU Write-Back cache operation.
Supports 2 - 192 MB DRAM memory on board.
Supports shadow RAM for BIOS & VIDEO BIOS.
Supports shadow RAM cacheable function to improve performance.
Supports Hardware and Software speed change function.
Licensed AWARD BIOS.

6

\_\_\_\_\_

☐ BENCHMARQ 3287 RTC on board.

■ BABY AT size, 6 layers PCB.

## 1.3. PERFORMANCE LIST

The following performance data list is the testing results of some popular benchmark testing programs. These data are just referred by users, and there is no responsibility for different testing data values gotten by users. (The different Hardware & Software configuration will result in different benchmark testing results.)

CPU Pentium<sup>™</sup> 60 / 66 MHz

• DRAM 16 MB - 70 ns

CACHE SIZE 256 KB

DISPLAY GA-306 PCI VGA

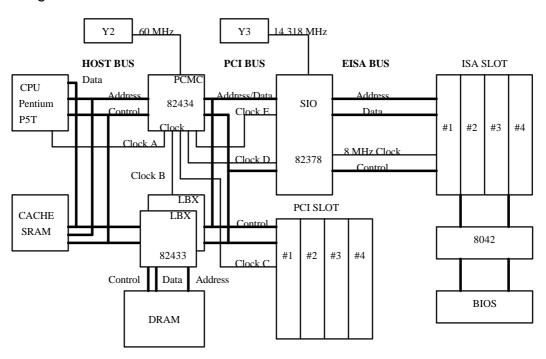
STORAGE GA - 410 PCI - Bus SCSI

• O.S. MS DOS V6.2

Testing Program	Item	Unit	66 MHz	60 MHz
LandMark V1.14	CPU Speed	MHz	200+	200+
LandMark V2.0	CPU Speed	MHz	384.88	346.50
	FPU Speed	MHz	1132.35	1019.43
SI V7.0	CPU Index		211.2	190.1
Byte V2.1	CPU Sieve		448.36	403.49
	CPU Sort		33.12	29.81
	CPU Int. Math		1239724.23	1116158.87
	FPU Fmath		1149044.65	1034650.97
	FPU Fourier		15.22	13.70
BenchMark V7.01	Processor Harmonic		28590.99	25722.52
	Memory Harmonic		7508.90	6758.38
PM V1.7	MIPS	Mips	44.8	40.6
	DhryStone	K-Dstone/s	57.4	51.5
	WhetStone	K-Wstone/s	13148.9	11756.6

#### 1.4. BLOCK DIAGRAM

≺Figure 1.1≻



## 1.5. INTRODUCE THE PCI - BUS

Connecting devices to a CPU local bus can dramatically increase the speed of I/O-bound peripherals with only a slight increase in cost over traditional systems. This price / performance point has created a vast market potential for local bus products. The main barrier to this market has been the lack of an accepted standard for local bus peripherals. Many mainboard and chipset manufactures developed their own local bus implementations, but they are incompatible with each other. The VL (Video Electronics Standards Association) local bus and PCI (Peripheral Component Interconnect) bus specification was created to end this confusion.

The PCI - bus standard, under development since Jun. 1992, which is designed to bring workstation-level performance to standard PC platform. The PCI - bus removes many of the bottlenecks that have hampered PC for several years. On the PCI - bus, peripherals operate at the native speed of the computer system,

\_\_\_\_

thus enabling data transfer between peripherals and the system at maximum speed. This performance is critical for bandwidth-constrained devices such as video, multimedia, mass storage, and networking adapters.

PCI - bus standard provides end-users with a low-cost, extendible and portable local bus design, which will allow system and peripherals from different manufactures to work together.

# 1.6. FEATURES

32 bits bus transfer mode.
Bus Master or Slave access.
Memory burst transfer to 132 MB/sec.
33 MHz operation speed.
10 device loading ability.
CPU independent.

\_\_\_\_\_

# 2. SPECIFICATION

#### 2.1. HARDWARE

CPU – Pentium™ 60 / 66 MHz, P5T.

273 pins (socket 4) ZIF white socket on board.

COPROCESSOR – Included in Pentium.

SPEED – 60 / 66 MHz system and 30 / 33 PCI-Bus speed.

- 7.5 / 8 MHz AT bus speed.

Hardware and Software speed switchable function

(cache or non-cache).

DRAM MEMORY – 3 double banks 72 pins SIMM module socket on board.

 Use 1 / 2 / 4 / 8 / 16 / 32 MB 70 ns SIMM module DRAM.

2 ~ 192 MB DRAM size.

Support Fast Page DRAM access mode.

CACHE MEMORY – 16 KB cache memory included in Pentium.

256 / 512 KB second cache memory on board.

Support Pentium Burst Read / Write mode on

second cache memory access.

SHADOW RAM – Main BIOS shadow function.

Video BIOS shadow function programmable.

Shadow RAM cacheable function programmable.

I/O BUS SLOTS – 4 Master / Slave PCI-Bus.

4 16 bits ISA Bus .

DIMENSION – Baby AT size / 6 layers.

## 2.2. SOFTWARE

BIOS – Licensed AWARD BIOS.

AT CMOS Setup, BIOS / Chipset Setup and Hard

Disk Utility included.

O.S.
 Operation with MS-DOS, WINDOWS NT, OS/2,

NOVELL and SCO UNIX.

# 2.3. ENVIRONMENT

Ambient Temp. – 0°C to +50°C (Operating).
 Relative Hum. – 0 to +85% (Operating).

● Altitude – 0 to 10,000 feet (Operating).

Vibration – 0 to 1,000 Hz.
 Electricity – 4.9 V to 5.2 V.

5 A to 7 A current.

\_\_\_\_\_

# 3. HARDWARE INSTALLATION

#### 3.1. UNPACKING

The mainboard package should contain the following:

- The GA 586IS mainboard.
- USER'S MANUAL.

The mainboard contains sensitive electric components which can be easily damaged by static electricity, so the mainboard should be left in its original packing until it is installed.

Unpacking and installation should be done on a grounded anti-static mat. The operator should be wearing an anti static wristband, grounded at the same point as the anti-static mat.

Inspect the mainboard carton for obvious damage. Shipping and handling may cause damage to your board. Be sure there are no shipping and handling damages on the board before proceeding.

After opening the mainboard carton, extract the system board and place it only on a grounded anti-static surface component side up. Again inspect the board for damage. Press down on all of the socket IC's to make sure that they are properly seated. Do this only on with the board placed on a firm flat surface.

#### ● DO NOT APPLY POWER TO THE BOARD IF IT HAS BEEN DAMAGED.

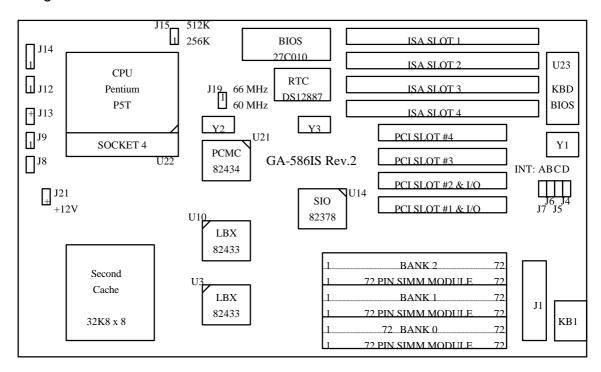
You are now ready to install your maniboard. The mounting hole pattern on the mainboard matches the IBM-AT system board. It is assumed that the chassis is designed for a standard IBM XT/AT mainboard mounting.

Place the chassis on the anti-static mat and remove the cover. Take the plastic clips, Nylon stand-off and screws for mounting the system board, and keep them separate.

\_\_\_\_\_

# 3.2. MAINBOARD LAYOUT

# ≺Figure 3.1≻



# 3.3. QUICK REFERENCE FOR JUMPERS & CONNECTORS

♦ KB1: K	♦ KB1: Keyboard Connector	
Pin No.	Function	
1	Key Clock.	
2	Key Data.	
3	NC.	
4	VCC (+5V).	
5	GND.	

# GA-586IS

♦ J1: Powe	♦ J1: Power Connector	
Pin No.	Function	
1	Power Good signal	
2,10,11,12	VCC (+5V)	
3	+12V	
4	-12V	
5,6,7,8	GND	
9	-5V	

♦ J4 ~ J7	: PCI Device Interrupt (IRQ) Selection
Jump No.	Function
J4	Closed for Enable PCI INT-D connect to ISA IRQ-14.
J5	Closed for Enable PCI INT-C connect to ISA IRQ-11.
J6	Closed for Enable PCI INT-B connect to ISA IRQ-10.
J7	Closed for Enable PCI INT-A connect to ISA IRQ-9.

♦ J8: Reset Switch	
Open For normal operation.	
Close	For hardware reset system.

♦ J9: Po\	♦ J9: Power LED and Key-Lock Connector	
Pin No.	Function	
1	LED anode (+).	
2	NC.	
3	LED cathode (-).	
4	Key lock.	
5	GND.	

♦ J12: Tu	urbo Switch
Pin No.	Function
1-2	For low speed (Non-cache).
2-3	For high speed.

♦ J13: Turbo LED Connector		
Pin No.	Function	
1	LED cathode (-).	
2	LED anode (+).	

Pin No.	Function		
1	Data.		
2	NC.		
3	GND.		
4	VCC (+5V).		

♦ J15: Second Cache Size Selection		
Pin No.	Function	
1-2	Close for 256 KB cache size.	
2-3	Close for 512 KB cache size.	

♦ J19: System Speed Selection		
Pin No.	Function	
1-2	Close for 66 MHz system speed.	
2-3	Close for 60 MHz system speed.	

♦ J21: CPU Cooling Fan Power Connector		
Pin No.	Function	
1	+12V	
2	GND	

# 3.4. DRAM INSTALLATION

The mainboard can be installed with 1/2/4/8/16/32 MB 72 pins SIMM module DRAM, and the DRAM speed must be 70 ns. The DRAM memory system on mainboard consists of bank 0, 1 & bank 2. The DRAM of any bank can be

installed first. Each bank consist of 2 PCs, 72 pins SIMM module DRAM. Because the 72 pins SIMM module is 32 bits width, using 2 PCs which can match a 64 bits system. The total memory size is 2 - 192 MB, and various configuration of DRAM types in the following TABLE are available:

Size	Bank 0	Bank 1	Bank 2
2 MB	1 MB x 2	None	None
4 MB	1 MB x 2	1 MB x 2	None
4 MB	2 MB x 2	None	None
6 MB	1 MB x 2	1 MB x 2	1 MB x 2
8 MB	2 MB x 2	2 MB x 2	None
8 MB	4 MB x 2	None	None
12 MB	2 MB x 2	2 MB x 2	2 MB x 2
12 MB	4 MB x 2	2 MB x 2	None
16 MB	4 MB x 2	2 MB x 2	2 MB x 2
16 MB	4 MB x 2	4 MB x 2	None
16 MB	8 MB x 2	None	None
20 MB	4 MB x 2	4 MB x 2	2 MB x 2
20 MB	8 MB x 2	2 MB x 2	None
24 MB	4 MB x 2	4 MB x 2	4 MB x 2
24 MB	8 MB x 2	2 MB x 2	2 MB x 2
24 MB	8 MB x 2	4 MB x 2	None
32 MB	16 MB x 2	None	None
32 MB	8 MB x 2	8 MB x 2	None
32 MB	8 MB x 2	4 MB x 2	4 MB x 2
48 MB	16 MB x 2	8 MB x 2	None
48 MB	16 MB x 2	4 MB x2	4 MB x 2
48 MB	8 MB x 2	8 MB x 2	8 MB x 2
64 MB	32 MB x 2	None	None
64 MB	16 MB x 2	16 MB x 2	None
64 MB	16 MB x 2	8 MB x 2	8 MB x 2
96 MB	32 MB x 2	16 MB x 2	None
96 MB	32 MB x 2	8 MB x 2	8 MB x 2
96 MB	16 MB x 2	16 MB x 2	16 MB x 2
128 MB	32 MB x 2	32 MB x 2	None
128 MB	32 MB x 2	16 MB x 2	16 MB x 2
192 MB	32 MB x 2	32 MB x 2	32 MB x 2

The DRAM installation position refer to Figure 3.1, and notice the Pin 1 of SIMM module must match with the Pin 1 of SIMM socket when the DRAM SIMM

module is installed.

Insert the DRAM SIMM module into the SIMM socket at 45 degree angle. If there is a wrong direction of Pin 1, the DRAM SIMM module couldn't be inserted into socket completely. After completely insert SIMM module into socket, then press the SIMM module in vertical direction until the left and right metal holders can keep the SIMM module standing up con-firmly.

#### 3.5. SRAM INSTALLATION AND JUMPERS SETUP

The cache memory system consists of SRAM, the SRAM type is 32Kx8-15 / 20 ns, 64Kx8-15 / 20 ns or 128Kx8-15 / 20 ns.

The mainboard can be installed 256 KB or 512 KB cache memory when using 32Kx8 or 64Kx8 (128Kx8) type DATA SRAM separately. Please refer to the following table to install cache memory system:

SIZE	SIZE TYPE	
256 KB	32 K x 8	1 - 2
512 KB	64 K x 8 or 128 K x 8	2 - 3

Because there are not standard IC's coding number of SRAM, refer to the following table to identify the SRAM component:

32 K x 8	64 K x 8	128 K x 8
W24257AK-15/20	W24512AK-15/20	W241024AK-15/20
UM61256-15/20	AS7C512-15/20	AS7C1024-15/20
AS7C256-15/20		
CY7C199-20		
MCM6206-20		
KM68257BP-20		
TC555328P-20		

#### 3.6. CPU INSTALLATION AND JUMPERS SETUP

The system's speed depends on the frequency of CLOCK GENERATOR. The user can change the JUMPER (J19) selection to set up the system speed to 60

MHz or 66 MHz for 60 or 66 MHz PENTIUM™ CPU.

The mainboard can use PENTIUM™ or P5T CPU, and the CPU speed must match with the frequency of CLOCK GEN. It will cause system hanging up if the CLOCK GEN.'S frequency is higher than CPU's.

J19	CPU Type	CLK. GEN. Speed	CPU Speed
1-2	PENTIUM™ - 66	66.667 MHz	66.667 MHz
2-3	PENTIUM™ - 60	60 MHz	60 MHz

- The CPU is a sensitive electric component and it can be easily damaged by static electricity, so users must keep it away from metal surface when the CPU is installed onto mainboard.
- When the user installs the CPU on socket, please notice the PIN 1 of CPU is in the same corner as the PIN 1 of socket!
- **●** Before the CPU is installed, the mainboard must be placed on a flat plane in order to avoid being broken by the pressure of CPU installation.

#### 3.7. CMOS RTC & ISA CFG CMOS SRAM

There're RTC & CMOS SRAM on board, they have a power supply from internal battery to keep the DATA inviolate & effective. The RTC is a REAL-TIME CLOCK device which provides the DATE & TIME to system. The CMOS SRAM is used for keeping the information of ISA device system configuration, so the system can automatically boot OS. every time.

Due to the life-time of RTC internal battery is 10 years, the user can change a new RTC to replace old one after it can not work. The new one's brand and type must be same with old one.

## 3.8. SPEAKER CONNECTOR INSTALLATION

There is always a speaker in AT system for sound purpose. The 4 - Pins connector J14 is used to connect speaker. The speaker can work well in both direction of connector when it is installed to the connector J14 on mainboard.

\_\_\_\_\_\_

## 3.9. POWER LED & KEY LOCK CONNECTOR INSTALLATION

There are a system power LED lamp and a key on the panel of case. The power LED will light on when system is powered-on, and the key can lock the keyboard input or unlock it, both of them are connected to a 5 PIN connector. The connector should be installed to J9 of mainboard in correct direction.

## 3.10. TURBO SWITCH CONNECTOR INSTALLATION

The TURBO switch on the panel is used for controlling the system speed. Some program developed on XT should be executed with a low speed system, so a high speed system needs the speed switching function to change its running speed.

Because a PENTIUM cannot accept real clock speed change when program is executed, so the mainboard uses simulation method to implement TURBO switching function. The J12 on mainboard should be connected to the TURBO switch on panel, and user can push in or pop out the TURBO switch to enable or disable the turbo function of system.

• If not necessary, please don't use De-turbo mode. (always keep system in Turbo mode)

#### 3.11. TURBO LED CONNECTOR INSTALLATION

The TURBO LED on panel can indicate the current speed status of system. The TURBO LED connector should be installed to J13 in correct direction.

#### 3.12. HARDWARE RESET SWITCH CONNECTOR INSTALLATION

The RESET switch on panel provides users with HARDWARE RESET function which is almost the same as power-on/off. The system will do a cold start after the RESET switch is pushed and released by user. The RESET switch is a 2 PIN connector and should be installed to J8 on mainboard.

## 3.13. PERIPHERAL DEVICE INSTALLATION

#### **GA-586IS**

After the device installation and jumpers setup, the mainboard can be mounted into the case and fixed by screw. To complete the mainboard installation, the peripheral device could be installed now. The basic system needs a display interface card and a disk control interface card.

If the PCI - Bus device is to be installed in the system, any one of three PCI -Bus slots can be used no matter Slave or Master PCI - Bus device being installed.

After installing the peripheral device, the user should check everything again, and prepare to power-on the system.

\_\_\_\_\_\_

# 4. BIOS CONFIGURATION

Award's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS SRAM so that it retains the Setup information when the power is turned off.

#### 4.1. ENTERING SETUP

Power ON the computer and press <Del> immediately will allow you to enter Setup. The other way to enter Setup is to power on the computer, when the below message appears briefly at the bottom of the screen during the POST (Power On Self Test), press <Del> key or simultaneously press <Ctrl>, <Alt>, and <Esc> keys.

#### • TO ENTER SETUP BEFORE BOOT PRESS CTRL-ALT-ESC OR DEL KEY

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" bottom on the system case. You may also restart by simultaneously press <Ctrl>,<Alt>, and <Del> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to,

PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP

## 4.2. CONTROL KEYS

Up arrow	Move to previous item		
Down arrow	Move to next item		
Left arrow	Move to the item in the left hand		
Right arrow	Move to the item in the right hand		
Esc key	Main Menu - Quit and not save changes into CMOS		
	Status Page Setup Menu and Option Page Setup Menu - Exit		
	current page and return to Main Menu		
PgUp key	Increase the numeric value or make changes		
PgDn key	Decrease the numeric value or make changes		

#### **GA-586IS**

F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Change color from total 16 colors
F3 key	Calendar, only for Status Page Setup Menu
F4 key	Reserved
F5 key	Restore the previous CMOS value from CMOS, only for Option
	Page Setup Menu
F6 key	Load the default CMOS value from BIOS default table, only for Option Page Setup Menu
F7 key	Load the default
F8 key	Reserved
F9 key	Reserved
F10 key	Save all the CMOS changes, only for Main Menu

## 4.3. GETTING HELP

## 4.3.1. Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

# 4.3.2. Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc>.

# **4.4. THE MAIN MENU**

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 4.1) will appear on the screen. The Main Menu allows you to select from seven setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

# Figure 4.1: Main Menu

#### ROM ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	PASSWORD SETTING	
BIOS FEATURES SETUP	IDE HDD AUTO DETECTION	
CHIPSET FEATURES SETUP	SAVE & EXIT SETUP	
PCI SLOT CONFIGURATION	EXIT WITHOUT SAVING	
LOAD SETUP DEFAULTS		
ESC: Save & Exit Setup F10: Quit		
Time, Date, Hard Disk Type,		

• Standard CMOS setup

This setup page includes all the items in a standard compatible BIOS.

• BIOS features setup

This setup page includes all the items of Award special enhanced features.

Chipset features setup

This setup page includes all the items of chipset special features.

• PCI slot configuration

This setup page includes all the configurations of PCI slots parameters

• Load setup defaults

BIOS defaults indicates the most appropriate value of the system

#### **GA-586IS**

\_\_\_\_\_

parameter which the system would be in maximum performance.

Password setting

Change, set, or disable password. It allows you to limit access to the system and Setup, or just to Setup.

IDE HDD auto detection

Automatically configure hard disk parameter.

• Save & exit setup

Save CMOS value changes to CMOS and exit setup.

• Exit without save

Abandon all CMOS value changes and exit setup.

#### 4.5. STANDARD CMOS SETUP MENU

The items in Standard CMOS Setup Menu (Figure 4.2) are divided into 9 categories. Each category includes no, one or more than one setup items. Use the arrows to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

\_\_\_\_\_

Figure 4.2: Standard CMOS Setup Menu

#### ROM ISA BIOS STANDARD CMOS SETUP AWARD SOFTWARE, INC.

Date (mm:dd:yy) : Mon, F Time (hh:mm:ss) : 10 :					
	CYLS.	HEADS	PRECOMP	LANDZONE	SECTORS
Drive C: 1 (10Mb) Drive D: None (0 Mb)	306 0	4 0	128 0	305 0	17 0
Drive A: 1.44 M 3.5 in. Drive B: 1.2 M, 5.25 in.  Video: EGA/VGA			Extended Expanded	Memory: 640 Memory: 7168 Memory: 0 Memory: 384	K K
Halt On: All Errors			Total	Memory: 8192	K
ESC: Quit	$\uparrow \downarrow \rightarrow \cdot$	: Select	Item	PU/PD/+/	- : Modify
F1 : Help	(Shift)F2	: Chang C	Color	F3: Tag	gle Calender

#### • Date

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	The day, from Sun to Sat, determined by the BIOS and is
	display-only
date	The date, from 1 to 31 (or the maximum allowed in the month)
month	The month, Jan. through Dec.
year	The year, from 1900 through 2099

#### • Time

The time format in <hour> <minute> <second>. The time is calculated base on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

#### • Drive C type / Drive D type

The category identify the types of hard disk drive C or drive D that has been installed in the computer. There are 46 pre-defined types and a user definable type. Type 1 to Type 46 are pre-defined. Type User is user-definable.

Press PgUp or PgDn to select a numbered hard disk type or type the number and press <Enter>. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.

If you select Type User, related information is asked to be entered to the following items. Enter the information directly from the keyboard and press <Enter>. Those information should be provided in the documentation form your hard disk vendor or the system manufacturer.

CYLS.	number of cylinders
HEADS	number of heads
PRECOMP	write precom
LANDZONE	landing zone
SECTORS	number of sectors

If a hard disk has not been installed select NONE and press <Enter>.

#### Drive A type / Drive B type

The category identify the types of floppy disk drive A or drive B that has been installed in the computer.

360K, 5.25	No floppy drive installed 5-1/4 inch PC-type standard drive; 360 kilobyte
in.	capacity
1.2M, 5.25	5-1/4 inch AT-type high-density drive; 1.2 megabyte
in.	capacity
720K, 3.5 in.	· 1 1
1.44M, 3.5	3-1/2 inch double-sided drive; 1.44 megabyte capacity
in.	

2.88M, 3.5	3-1/2 inch double-sided drive; 2.88 megabyte capacity
in.	

#### Video

The category detects the type of adapter used for the primary system monitor that must matches your video display card and monitor. Although secondary monitors are supported, you do not have to select the type in setup.

EGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA,	
	VGA, SVGA, or PGA monitor adapters	
CGA 40	Color Graphics Adapter, power up in 40 column mode	
CGA 80	Color Graphics Adapter, power up in 80 column mode	
MONO	Monochrome adapter, includes high resolution monochrome	
	adapters	

#### • Halt on

The category determines whether the computer will stop if an error is detected during power up.

NO errors	Whenever the BIOS detects a non-fatal error the
	system will be stopped and you will be prompted
All errors	The system boot will not be stopped for any error
	that may be detected
All,	The system boot will not stop for a keyboard error;
But Keyboard	it will stop for all other errors
All, But	The system boot will not stop for a disk error; it
Diskette	will stop for all other errors
All, But	The system boot will not stop for a keyboard or disk
Disk/Key	error; it will stop for all other errors

#### Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

#### Base Memory

The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system. The value of the base memory is typically 512 K for systems with 512 K memory

installed on the motherboard, or 640 K for systems with 640 K or more memory installed on the motherboard.

#### Extended Memory

The BIOS determines how much extended memory is present during the POST. This is the amount of memory located above 1 MB in the CPU's memory address map.

#### Expanded Memory

Expanded Memory in memory defined by the Lotus/Intel/Microsoft (LIM) standard as EMS. Many standard DOS applications can not utilize memory above 640 K, the Expanded Memory Specification (EMS) swaps memory which not utilized by DOS with a section, or frame, so these applications can access all of the system memory. Memory can be swapped by EMS is usually 64 K within 1 MB or memory above 1 MB, depends on the chipset design.

Expanded memory device driver is required to use memory as Expanded Memory.

#### Other Memory

This refers to the memory located in the 640 K to 1024 K address space. This is memory that can be used for different applications. DOS uses this area to load device drivers to keep as much base memory free for application programs. Most use for this area is Shadow RAM.

## 4.6. BIOS FEATURES SETUP

#### ROM ISA BIOS BIOS FEATURES SETUP AWARD SOFTWARE, INC.

Virus Warning : Enabled **CPU Internal Cache** : Enabled Video BIOS Shadow : Enabled External Cache : Enabled C8000 - CBFFF Shadow: Disabled Quick Power On Self Test: Disabled CC000 - CFFFF Shadow: Disabled **Boot Sequence** : A, C D0000 - D3FFF Shadow: Disabled Swap Floppy Drive : Disabled D4000 - D7FFF Shadow: Disabled Boot Up Floppy Seek : Enabled D8000 - DBFFF Shadow: Disabled Boot Up NumLock Status: On DC000 - DFFFF Shadow: Disabled IDE HDD Block Mode : Disabled **Security Option** : Setup  $\uparrow \downarrow \rightarrow \leftarrow$  : Select Item ESC: Quit F1: Help : Modify F5 : Old Values (Shift)F2 : Color F6: Load BIOS Defaults F7: Load Setup Defaults

#### Virus Warning

This category flashes on the screen. During and after the system boots up, any attempt to write to the boot sector or partition table of the hard disk drive will halt the system and the following error message will appear, in the mean time, you run can anti-virus program to locate the problem. Default value is Enabled.

Enabled	Activate automatically when the system boots up causing a	
	warning message to appear when anything attempts to access	
	the boot sector or hard disk partition table	
Disabled	No warning message to appear when anything attempts to	
	access the boot sector or hard disk partition table	

#### • CPU Internal Cache / External Cache

These two categories speed up memory access. However, it depends on CPU / chipset design. The default value is Enabled.

Enabled	Enable cache
Disabled	Disable cache

#### • Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power on the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST. The default value is Disabled.

Enabled	Enable quick POST
Disabled	Normal POST

#### • Boot Sequence

This category determines which drive computer searches first for the disk operating system (i.e., DOS). Default value is A,C.

A,C	System will first search for floppy disk drive then hard disk drive
C,A	System will first search for hard disk drive then floppy disk drive

#### Swap Floppy Drive

The default value is Disabled.

Enabled	Floppy A & B will be swapped under DOS
Disabled	Floppy A & B will be normal definition

#### Boot Up Floppy Seek

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360 K type is 40 tracks while 720 K, 1.2 M and 1.44 M

\_\_\_\_\_

are all 80 tracks. The default value is Enabled.

Enabled	BIOS searches for floppy disk drive to determine if it is
	40 or 80 tracks, Note that BIOS can not tell from 720 K,
	1.2 M or 1.44 M drive type as they are all 80 tracks
Disabled	BIOS will not search for the type of floppy disk drive by
	track number. Note that there will not be any warning
	message if the drive installed is 360 K

• Boot Up NumLock Status

The default value is On

On	Keypad is number keys
Off	Keypad is arrow keys

• IDE HDD Block Mode

The default value is Disabled.

Enabled	Enable IDE HDD Block Mode
Disabled	Disable IDE HDD Block Mode

Security Option

This category allows you to limit access to the system and Setup, or just to Setup. The default value is Setup.

System	The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt
Setup	The system will boot, but access to Setup will be denied if the correct password is not entered at the prompt

- To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.
- Video BIOS Shadow

It determines whether video BIOS will be copied to RAM, however, it is

# GA-586IS

optional from chipset design. Video Shadow will increase the video speed.

The default value is Enabled.

Enabled	Video shadow is enabled
Disabled	Video shadow is disabled

# C8000 - CFFFF Shadow / D0000 - DFFFF Shadow

These categories determine whether optional ROM will be copied to RAM by 16 K byte. The default value are Disabled.

Enabled	Optional shadow is enabled
Disabled	Optional shadow is disabled

## 4.7. CHIPSET FEATURES SETUP

#### ROM ISA BIOS CHIPSET FEATURES SETUP AWARD SOFTWARE, INC.

**DRAM Wait State** : 0 Wait Cache Wait (0=60, 1=66 MHz) : 0 Wait CPU To PCI write buffer : Enabled PCI Memory Burst Write : Enabled System BIOS Cacheable : Enabled Video BIOS Cacheable : Enabled Using ET4000/W32P PCI VGA: NO Memory Hole Start Address : 15 MB Memory Hole Size : Disabled NCR 810 SCSI Controller : Enabled Delay For SCSI/HDD (secs) : 5

ESC: Quit  $\uparrow \downarrow \rightarrow \leftarrow$  : Select Item F1 : Help PU/PD/+/- : Modify F5 : Old Values (Shift)F2 : Color

F6 : Load BIOS DefaultsF7 : Load Setup Defaults

• DRAM Wait State
The default value is 0 Wait.

0 Wait	For 70 ns DRAM (for 60 MHz).
1 Wait	For 70 - 80 ns DRAM (for 66 MHz).

• Cache Wait State
The default value is 0 Wait.

0 Wait	For	60MHz.
1 Wait	For	66MHz.

• CPU To PCI write buffer The default value is Enabled.

Enabled	Enhanced PCI performance.
Disabled	For some slower PCI device.

## **GA-586IS**

PCI Memory Burst Write

The default value is Enabled.

Enabled	Enhanced Master PCI device performance.
Disabled	For some slower PCI device.

• System BIOS Cacheable

The default value is Enabled.

Enabled	Enable BIOS shadowed cacheable.
Disabled	BIOS shadowed only.

• Video BIOS Cacheable

The default value is Enabled.

Enabled	Enable Video BIOS shadowed cacheable.
Disabled	Video BIOS shadowed only.

Using ET4000/W32P PCI VGA

The default value is NO.

YES	Using ET4000/W32P PCI VGA enhanced mode.
NO	Using other VGA.

• Memory Hole Start Address

A memory space reserved for ISA bus, it starting address is from 1 MB to 15 MB and ending address is 16 MB. The default value is 15 MB.

Memory Hole Size

For some linear address VGA / Video card on ISA bus can run a large frame port (not like standard VGA only access A0000 - BFFFF). The size = 16 MB - [starting address]. The default value is Disabled.

\_\_\_\_

• NCR 810 SCSI Controller

The default value is Enabled.

Enabled	Enable NCR 810 PCI SCSI device.
Disabled	Disable NCR 810 PCI SCSI device.

• Delay For SCSI / HDD (secs)

Set start timing for some slower speed SCSI hard disk. The start timing can set from 0 to 30 seconds. The default value is 5.

# 4.8. PCI SLOT CONFIGURATION

#### ROM ISA BIOS PCI SLOT CONFIGURATION AWARD SOFTWARE, INC.

SLOT 1 Location	: C3XX	
Using IRQ	: NA	
SLOT 2 Location	: C4XX	
Using IRQ	: NA	
SLOT 3 Location	: C5XX	
Using IRQ	: NA	
SLOT 3 Location	: C6XX	
Using IRQ	: NA	ESC: Quit $\uparrow \downarrow \rightarrow \leftarrow$ : Select Item F1 : Help PU/PD/+/- : Modify
NCR Using IRQ	: 9	F5 : Old Values (Shift)F2 : Color F6 : Load BIOS Defaults F7 : Load Setup Defaults

• SLOT 1 / SLOT 2 / SLOT 3 / SLOT 4 Location

These indicate the slot address. The address of slot 1 is C3XX, slot 2 is C4XX, slot 3 is C5XX and slot 4 is C6XX.

#### Using IRQ

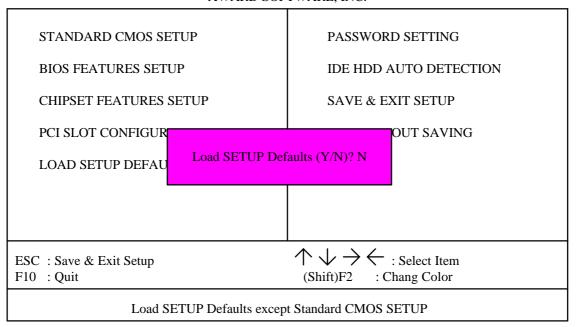
This category select a INT for slot. There are four INT (9, 10, 11, 14 and NA) can be selected for each PCI slot. This function is completed by Jumper on PCI device card or card's booting BIOS setup, but user must re-setup here. The default value is NA.

#### NCR SCSI Using IRQ

This category select a IRQ for NCR 810 PCI SCSI controller (GA-410). There are four IRQ selections (9, 10, 11, 14 and NA) for GA-410. The default value is 9.

# 4.9. LOAD SETUP DEFAULTS

#### **ROM ISA BIOS** CMOS SETUP UTILITY AWARD SOFTWARE, INC.



#### Load SETUP Defaults

To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

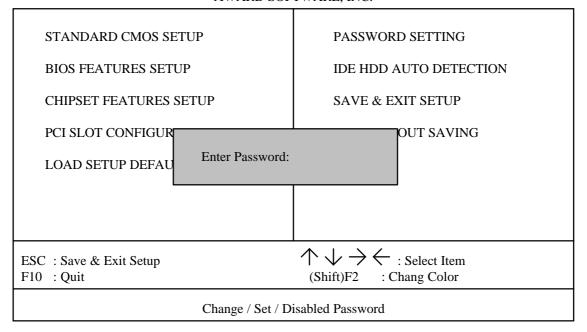
● If there is any problem occurred, loading SETUP DEFAULTS step is recommended.

#### 4.10. PASSWORD SETTING

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

#### **ENTER PASSWORD**

#### ROM ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Type the password, up to eight characters, and press <Enter>. The password typed now will clear and previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

#### PASSWORD DISABLED

If you select System at Security Option of BIOS Features Setup Menu, you will be prompted for the password every time the system is rebooted or any time you try to enter Setup. If you select Setup at Security Option of BIOS Features Setup Menu, you will be prompted only when you try to enter Setup.

#### 4.11. IDE HDD AUTO DETECTION

ROM ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

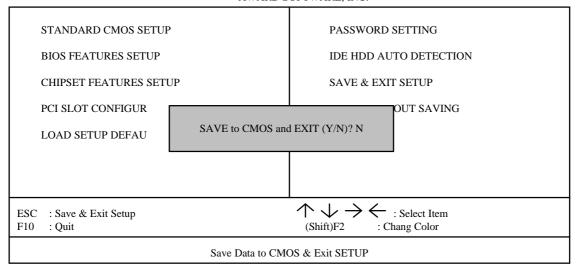
Drive C:	(202 Mb)	CYLS. 989	HEAD 12	PRECOMP 65535	LANDZONE 989	SECTORS 35		
	Do you accept this drive C (Y/N)? N							
			ESC : Skip					

Type "Y" will accept the H.D.D. parameter reported by BIOS. Type "N" will keep the old H.D.D. parameter setup.

----

#### 4.12. SAVE & EXIT SETUP

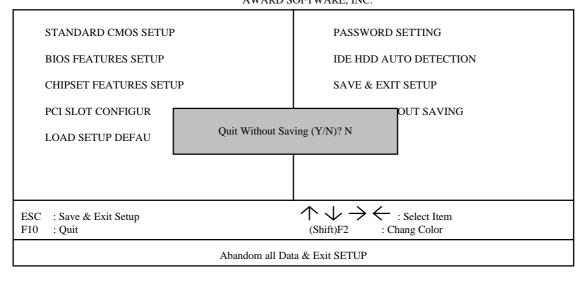
ROM ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Type "Y" will quit the Setup Utility and save the user setup value to RTC CMOS SRAM. Type "N" will return to Setup Utility.

#### 4.13. EXIT WITHOUT SAVING

ROM ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Type "Y" will quit the Setup Utility without saving to RTC CMOS SRAM. Type

------

"N" will return to Setup Utility.

### **4.14. KEYBOARD SETTING FUNCTION**

After booting the O.S., there are some special functions used by keyboard as follows:

"CTRL_ALT_DEL"	_	Pressing these keys simultaneously will cause system
		to Warm Start (Software Reset).
"CTRL_ALT_[+]"	_	Pressing these keys simultaneously will change the system speed to high speed (Turbo, all cache memory enable).
"CTRL_ALT_[-]"	_	Pressing these keys simultaneously will change the system speed to low speed (Normal, disable cache memory).

# 5. AT TECHNICAL INFORMATION

### **5.1. I/O BUS CONNECTOR PIN OUT**

### 5.1.1.ISA SLOT PIN OUT

			-				
GND	B01	A01	I/O CH CHK	-MEMCS16	D01	C01	SBHI
RESET	B02	A02	SD07	-I/OCS16	D02	C02	LA23
+5V	B03	A03	SD06	IRQ10	D03	C03	LA22
IRQ9	B04	A04	SD05	IRQ11	D04	C04	LA21
-5V	B05	A05	SD04	IRQ12	D05	C05	LA20
DRQ2	B06	A06	SD03	IRQ15	D06	C06	LA19
-12V	B07	A07	SD02	IRQ14	D07	C07	LA18
0WS	B08	A08	SD01	-DACK0	D08	C08	LA17
+12V	B09	A09	SD00	DRQ0	D09	C09	MEN
GND	B10	A10	I/O CH RDY	-DACK5	D10	C10	MEN
-SMEMW	B11	A11	AEN	DRQ5	D11	C11	SD08
-SMEMR	B12	A12	SA19	-DACK6	D12	C12	SD09
-IOW	B13	A13	SA18	DRQ6	D13	C13	SD10
-IOR	B14	A14	SA17	-DACK7	D14	C14	SD11
-DACK3	B15	A15	SA16	DRQ7	D15	C15	SD12
-DRQ3	B16	A16	SA15	+5V	D16	C16	SD13
-DACK1	B17	A17	SA14	-MASTER	D17	C17	SD14
-DRQ1	B18	A18	SA13	GND	D18	C18	SD15
-REFRESH	B19	A19	SA12				I
BCLK	B20	A20	SA11				
IRQ7	B21	A21	SA10				
IRQ6	B22	A22	SA09				
IRQ5	B23	A23	SA08				
IRQ4	B24	A24	SA07				
IRQ3	B25	A25	SA06				
-DACK2	B26	A26	SA05				
T/C	B27	A27	SA04				
BALE	B28	A28	SA03				
+5V	B29	A29	SA02				
OSC	B30	A30	SA01				
GND	B31	A31	SA00				
			J				

### 5.1.2. PCI - BUS SLOT PIN OUT

			-
-12V	B01	A01	NC
NC	B02	A02	+12V
GND	B03	A03	NC
NC	B04	A04	NC
VCC	B05	A05	VCC
VCC	B06	A06	INTA#
INTB#	B07	A07	INTC#
INTD#	B08	A08	VCC
PST#1	B09	A09	NC
NC	B10	A10	vcc
PST#2	B11 B12	A11	NC
GND		A12	GND
GND NC	B13 B14	A13 A14	GND NC
GND_	B15	A15	RST#
CLK_	B15	A16	KS1# VCC
GND_	B17	A17	VCC GNT#
REQ#	B18	A18	GNT#
VCC_	B19	A19	OND
AD_31	B20	A20	AD_30
AD_29	B21	A21	NC_NC
GND	B22	A22	AD 28
AD 27	B23	A23	AD_26
AD_25	B24	A24	GND
NC	B25	A25	AD_24
CBE#3	B26	A26	IDSEL
AD_23	B27	A27	NC
GND	B28	A28	AD_22
AD_21	B29	A29	AD_20
AD_19	B30	A30	GND
NC	B31	A31	AD_18
AD_17	B32	A32	AD_16
CEB#2	B33	A33	NC
GND	B34	A34	FRAME#
IRDY#	B35	A35	GND
NC	B36	A36	TRDY#
DEVSEL#	B37	A37	GND
GND	B38	A38	STOP#
LOCK#	B39	A39	NC
PERR#	B40	A40	SDONE
NC	B41	A41 A42	SBO#
SERR# NC	B42 B43	A42 A43	GND PAR
CBE#1	B44	A44	PAR AD_15
AD 14	B45	A45	AD_13
GND	B46	A46	AD_13
AD 12	B47	A47	AD_11
AD 10	B48	A48	GND
GND_	B49	A49	AD_09
AD_08	B52	A52	CBE#0
AD_07	B53	A53	NC
NC	B54	A54	AD_06
AD_05	B55	A55	AD_04
AD_03 GND	B56 B57	A56 A57	GND AD_02
AD 01	B58	A57 A58	AD_02 AD_00
VCC	B59	A59	VCC
NC	B60	A59 A60	VCC NC
VCC_	B61	A61	vcc
VCC_	B62	A62	vcc
	202	. 102	

42

#### **5.2. I/O & MEMORY MAP**

MEMORY MAP: [0000000-009FFFF] System memory used by DOS and application program.

[00A0000-00BFFFF] Display buffer memory for VGA/ EGA/CGA/MONOCHROME

adapter.

[00C0000-00DFFFF] Reserved for I/O device BIOS ROM or RAM buffer.

[00E0000-00EFFFF] Reserved for PCI device ROM.

[00F0000-00FFFFF] System BIOS ROM. [0100000-BFFFFFF] System extension memory.

I/O MAP: [000-01F] DMA controller.(Master)

[020-021] INTERRUPT controller.(Master) [022-023] CHIPSET control registers I/O ports.

[040-05F] TIMER control registers.

[060-06F] KEYBOARD interface controller.(8042)

[070-07F] RTC ports & CMOS I/O ports.

[080-09F] DMA register.

[0A0-0BF] INTERRUPT controller.(Slave)
[0C0-0DF] DMA controller.(Slave)
[0F0-0FF] MATH COPROCESSOR
[1F0-1F8] HARD DISK controller.
[278-27F] PARALLEL port-2.

[2B0-2DF] GRAPHICS adapter controller.

[2F8-2FF] SERIAL port-2. [360-36F] NETWORK ports. [378-37F] PARALLEL port-1

[3B0-3BF] MONOCHROME & PRINTER adapter.

[3C0-3CF] EGA adapter. [3D0-3DF] CGA adapter.

[3F0-3F7] FLOPPY DISK controller.

[3F8-3FF] SERIAL port-1.

#### **5.3. TIMER & DMA CHANNELS MAP**

TIMER MAP: TIMER Channel-0 System timer interrupt

TIMER Channel-1 DRAM REFRESH request TIMER Channel-2 SPEAKER tone generator

DMA CHANNELS: DMA Channel-0 Available

DMA Channel-1 IBM SDLC

DMA Channel-2 FLOPPY DISK adapter

DMA Channel-3 Available

DMA Channel-4 Cascade for DMA controller 1

DMA Channel-5 Available DMA Channel-6 Available DMA Channel-7 Available

### **5.4. INTERRUPT MAP**

NMI: Parity check error

IRQ (H/W): 0 System TIMER interrupt from TIMER-0

1 KEYBOARD output buffer full

2 Cascade for IRQ 8-15 3 SERIAL port 2 4 SERIAL port 1 5 PARALLEL port 2 6 FLOPPY DISK adapter 7 PARALLEL port 1

8 RTC clock 9 Available 10 Available 11 Available 12 Available

13 MATH coprocessor 14 HARD DISK adapter

15 Available

### 5.5. RTC & CMOS RAM MAP

RTC & CMOS:	00	Seconds
	01	Second alarm
	02	Minutes
	03	Minutes alarm
	04	Hours
	05	Hours alarm
	06	Day of week
	07	Day of month
	08	Month
	09	Year
	0A	Status register A
	0B	Status register B
	0C	Status register C
	0D	Status register D
	0E	Diagnostic status byte
	0F	Shutdown byte
	10	FLOPPY DISK drive type byte
	11	Reserve
	12	HARD DISK type byte
	13	Reserve
	14	Equipment byte
	15	Base memory low byte
	16	Base memory high byte
	17	Extension memory low byte
	18	Extension memory high byte
	19-2d	, , ,
	2E-2F	
	30	Reserved for extension memory low byte

### AT Technical Information

31	Reserved for extension memory high byte
32	DATE CENTURY byte
33	INFORMATION FLAG
34-3F	Reserve
40-7f	Reserved for CHIPSET SETTING DATA

### **APPENDIX A: POST MESSAGE**

When the BIOS encounters an error that requires the user to correct something, either a beep code will sound or a message will be displayed in a box in the middle of the screen and the message PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP will be shown in the information box at the bottom.

#### POST BEEP

Currently there is only one beep code in BIOS. This code indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps.

#### ERROR MESSAGE

Once or more of the following messages may be displayed if the BIOS detects an error during the POST. This list includes message for both the ISA and the EISA BIOS.

#### **☒** CMOS BATTERY HAS FAILED

CMOS battery is no longer functional. It should be replaced.

#### **⋈** CMOS CHECKSUM ERROR

Checksum of CMOS is incorrect. This can indicate that CMOS has become corrupt. This error may have been caused by a weak battery. Check the battery and replace if necessary.

#### ☑ DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER

No boot device was found. Insert a system disk into Drive A: and press <Enter>. If you assumed the system would boot from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also be sure the disk is formatted as a boot device. Then reboot the system.

#### ☑ DISKETTE DRIVES OR TYPES MISMATCH ERROR - RUN SETUP

Type of diskette drive installed in the system is different from the CMOS definition. Run Setup to re-configure the drive type correctly.

#### DISPLAY SWITCH IS SET INCORRECTLY

Display switch on the motherboard can be set to either monochrome or color. This indicates the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper, or enter Setup and change the VIDEO selection.

#### **☒** DISPLAY TYPE HAS CHANGED SINCE LAST BOOT

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.

#### **☒** EISAConfigurationChecksumError

#### PLEASE RUN EISA CONFIGURATION UTILITY

The EISA non-volatile RAM checksum is incorrect or cannot correctly read the EISA slot. This can indicate either the EISA non-volatile memory has become corrupt or the slot has configured incorrectly. Also be sure the card is installed firmly in the slot.

#### **☒** EISAConfigurationIsNotComplete

#### PLEASE RUN EISA CONFIGURATION UTILITY

The slot configuration information stored in the EISA non-volatile memory is incomplete.

When either of these errors appear, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

#### 

Hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also be sure the correct hard drive type is selected in Setup.

#### **⋈** ERROR INITIALIZING HARD DISK CONTROLLER

Cannot initialize controller. Make sure the cord is correctly and firmly installed in the bus. Be sure the correct hard drive type is selected in Setup. Also check to see if any jumper needs to be set correctly in the hard drive.

#### 

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there are no floppy drives installed, be sure the Diskette Drive selection in Setup is set to NONE.

#### **☒** InvalidEISAConfiguration

#### PLEASE RUN EISA CONFIGURATION UTILITY

The non-volatile memory containing EISA configuration information was programmed incorrectly or has become corrupt. Re-run EISA configuration utility to correctly program the memory.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

#### X KEYBOARD ERROR OR NO KEYBOARD PRESENT

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during the boot.

If you are purposely configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

#### Memory Address Error at ...

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

#### ➤ Memory parity Error at ...

Indicates a memory parity error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

#### MEMORY SIZE HAS CHANGED SINCE LAST BOOT

Memory has been added or removed since the last boot. In EISA mode use Configuration Utility to re-configure the memory configuration. In ISA mode enter Setup and enter the new memory size in the memory fields.

#### ➤ Memory Verify Error at ...

Indicates an error verifying a value already written to memory. Use the location along with your system's memory map to locate the bad chip.

#### **☒** OFFENDING ADDRESS NOT FOUND

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

#### **☒** OFFENDING SEGMENT:

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem has been isolated.

#### ▶ PRESS A KEY TO REBOOT

This will be displayed at the bottom screen when an error occurs that requires you to reboot. Press any key and the system will reboot.

#### PRESS F1 TO DISABLE NMI, F2 TO REBOOT

When BIOS detects a Non-maskable Interrupt condition during boot, this will allow you to disable the NMI and continue to boot, or you can reboot the system will the NMI enabled.

#### ☒ RAM PARITY ERROR - CHECKING FOR SEGMENT ...

Indicates a parity error in Random Access Memory.

# ➤ ShouldBeEmptyButEISABoardFound PLEASE RUN EISA CONFIGURATION UTILITY

A valid board ID was found in a slot that was configured as having no board ID.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

#### ShouldHaveEISABoardButNotFound

#### PLEASE RUN EISA CONFIGURATION UTILITY

The board installed is not responding to the ID request, or no board ID has been found in the indicated slot.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

#### **☒** Slot Not Empty

Indicates that a slot designated as empty by the EISA Configuration Utility actually contains a board.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

#### ☒ SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT ...

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

### $\boxtimes$ WrongBoardInSlot

#### PLEASE RUN EISA CONFIGURATION UTILITY

The board ID does not match the ID stored in the EISA non-volatile memory.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

# **APPENDIX B: POST CODES**

Fig. EISA POST codes are typically output to port address 300h. ISA POST codes are typically output to port address 80h.

he control. FLAGS) Verification.
FLAGS) Verification.
processor status flags
verflow,
each of these flags, verify they are set, then turn each flag
off.
all CPU registers except SS, SP, and BP with data
AIE, UEI, SQWV.
ty checking, DMA.
essor.
sters, CMOS shutdown byte.
, and 2, including set EISA timer to a known state.
atrollers 0 and 1.
controllers 0 and 1.
ended registers.
odically refreshed in order to keep the memory from
ction assures that the memory refresh function is working
* *** ***
r initialization.
11 11 11 11 11
working correctly, detects bad battery.
'
gisters with power on BIOS defaults.
to size on-board memory.
y Shadow enable for fast boot.
detection.
alization.
est. nes.
nemory.
nory.
ration.
i.
nterrupt vectors with SPURIOUS_INT-HDLR and
Fh according to INT_TBL.
Checksum, if bad, or insert key pressed, load defaults.
chocksum, if out, of insort key pressed, tout defaults.
oard controller (optional).
tatus.
on 14h to find out type of video in use.
e Video Adapter.
, write sign-on message to screen.
I - Enable shadow according to Setup.

F	Test DMA Controller 0	DIOC sheelroum test
F	Test DMA Controller 0	BIOS checksum test.  Keyboard detect and initialization.
10	Test DMA Controller 1	Reyboard detect and mitianization.
11	Test DMA Page registers	Test DMA Page Registers.
12-13	Reserved	Tool 21 m T ugo Nogavetsi
14	Test Timer Counter 2	Test 8254 Timer 0 Counter 2.
15	Test 8259-1 Mask Bits	Verify 8259 Channel 1 masked interrupts by alternately turning off and on
		the interrupt lines.
16	Test 8259-2 Mask Bits	Verify 8259 Channel 2 masked interrupts by alternately turning off and on
		the interrupt lines.
17	Test Stuck 8259's	Turn off interrupts then verify no interrupt mask register is on.
18	Interrupt Bits Test 8259 Interrupt	Force an interrupt and verify the interrupt occurred.
10	Functionality	Force an interrupt and verify the interrupt occurred.
19	Test Stuck NMI Bits	Verify NMI can be cleared.
	(Parity/IO Check)	
1A		Display CPU clock.
1B-1E	Reserved	
1F	Set EISA Mode	If EISA non-volatile memory checksum is good, execute EISA initialization.
		If not, execute ISA tests an clear EISA mode flag.
		Test EISA Configuration Memory Integrity (checksum & communication
		interface).
20	Enable Slot 0	Initialize slot 0 (System Board).
21-2F	Enable Slots 1-15	Initialize slot 1 through 15.
30	Size Base and Extended Memory	Size base memory from 256 K to 640 K extended memory above 1 MB.
31	Test Base and Extended	Test base memory from 256 K to 640 K and extended memory above 1 MB
	Memory	using various patterns.
		This will be skipped in EISA mode and can be "skipped" with ESC key
22	Test FIGA E des 1-1	in ISA mode.
32	Test EISA Extended Memory	If EISA Mode flag is set then test EISA memory found in slots initialization.  This will be skipped in ISA mode and can be "skipped" with ESC key
	Wellory	in EISA mode.
33-3B	Reserved	III Zioi i Inoto.
3C	Setup Enabled	
3D	Initialize & Install Mouse	Detect if mouse is present, initialize mouse, install interrupt vectors.
3E	Setup Cache Controller	Initialize cache controller.
3F	Reserved	
BF	Chipset Initialization	Program chipset registers with Setup values.
40		Display virus protest disable or enable.
41	Initialize Floppy Drive &	Initialize floppy disk drive controller and any drives.
	Controller	
42	Initialize Hard Drive &	Initialize hard drive controller and any drives.
L	Controller	
43	Detect & Initialize	Initialize any serial and parallel ports (also game port).
	Serial/Parallel Ports	
44	Reserved	T SC T
45	Detect & Initialize Math	Initialize math coprocessor.
46	Coprocessor	
46	Reserved Reserved	
48-4D	Reserved	
48-4D 4E	Manufacturing POST	Reboot if Manufacturing POST Loop pin is set. Otherwise display any
7.5	Loop or Display	messages (i.e., any non-fatal errors that were detected during POST) and
1	Messages	enter Setup.
4F	Security Check	Ask password security (optional).
50	Write CMOS	Write all CMOS values back to RAM and clear screen.
	1	

### **GA-586IS**

51	Pre-boot Enable	Enable parity checker.
		Enable NMI, Enable cache before boot.
52	Initialize Option ROMs	Initialize any option ROMs present from C8000h to EFFFFh.
		When FSCAN option is enabled, will initialize from C8000h to
		F7FFFh.
53	Initialize Time Value	Initialize time value in 40h: BIOS area.
60	Setup Virus Protect	Setup virus protect according to Setup
61	Set Boot Speed	Set system speed for boot
62	Setup NumLock	Setup NumLock status according to Setup
63	Boot Attempt	Set low stack.
	_	Boot via INT 19h.
В0	Spurious	If interrupt occurs in protected mode.
B1	Unclaimed NMI	If unmasked NMI occurs, display
		Press F1 to disable NMI, F2 reboot.
E1-EF	Setup Pages	E1 - Page 1, E2 - Page 2, etc.
FF	Boot	

# **APPENDIX C: BIOS DEFAULT DRIVE TABLE**

Type	Size (MB)	Cylinders	Heads	Sectors	Write / Precomp	Land Zone	Example Model
1	10 MB	306	4	17	128	305	TEAC SD510 MMI 112, 5412
2	20 MB	615	4	17	300	615	Seagate ST225, ST4026
3	31 MB	615	6	17	300	615	
4	62 MB	940	8	17	512	940	
5	47 MB	940	6	17	512	940	
6	20 MB	615	4	17	65535	615	Seagate ST125 Tandon TM262
7	31 MB	462	8	17	256	511	
8	30 MB	733	5	17	65535	733	Tandon TM703
9	112 MB	900	15	17	65535	901	
10	20 MB	820	3	17	65535	820	
11	35 MB	855	5	17	65535	855	
12	50 MB	855	7	17	65535	855	
13	20 MB	306	8	17	128	319	Disctron526, MMI M125
14	43 MB	733	7	17	65535	733	
16	20 MB	612	4	17	0	663	Microscience HH725 Syquest3250, 3425
17	41 MB	977	5	17	300	977	
18	57 MB	977	7	17	65535	977	
19	60 MB	1024	7	17	512	1023	
20	30 MB	733	5	17	300	732	
21	43 MB	733	7	17	300	732	
22	30 MB	733	5	17	300	733	Seagate ST4038
23	10 MB	306	4	17	0	336	
24	54 MB	925	7	17	0	925	Seagate ST4051
25	69 MB	925	9	17	65535	925	Seagate ST4096
26	44 MB	754	7	17	754	754	Maxtor2085
27	69 MB	754	11	17	65535	754	Maxtor2140, Priam S14
28	41 MB	699	7	17	256	699	Maxtor2190, Priam S19
29	68 MB	823	10	17	65535	823	Maxtor1085 Micropolis1325
30	53 MB	918	7	17	918	918	Maxtor1105, 1120, 4780
31	94 MB	1024	11	17	65535	1024	Maxtor1170
32	128 MB	1024	15	17	65535	1024	CDC9415
33	43 MB	1024	5	17	1024	1024	
34	10 MB	612	2	17	128	612	
35	77 MB	1024	9	17	65535	1024	
36	68 MB	1024	8	17	512	1024	
37	41 MB	615	8	17	128	615	
38	25 MB	987	3	17	987	987	
39	57 MB	987	7	17	987	987	Maxtor1140, 4380
40	41 MB	820	6	17	820	820	Seagate ST251
41	41 MB	977	5	17	977	977	Seagate ST4053 Miniscribe3053/6053
42	41 MB	981	5	17	981	981	Miniscribe3053/6053 RLL

### **GA-586IS**

43	48 MB	830	7	17	512	830	Miniscribe 3650
44	69 MB	830	10	17	65535	830	Miniscribe 3650 RLL
45	114 MB	917	15	17	65535	918	Conner CP3104
46	152 MB	1224	15	17	65535	1223	Conner CP3204
User							

# APPENDIX D: PROBLEM SHEET

1. (	Customer	Data							
Name				Tel. No	_				
Addr					Fax. No.				
				Purchas	e Date				
2. N	Mainboar	d Data							
Mode	el No.	GA-		Rev. No	).				
	Serial No.								
3. 8	System Co	onfiguration							
	Type:								
	Brand:								
	Speed:					D 16	□ 22.MD		
	M Type:	<b>1</b>	□ 2 □ <b>7</b> 0	<b>4</b>	<b>□</b> 8	<b>□</b> 16	□ 32 MB		
	M Speed:	□ 80	<b>1</b> 70	□ 60 ns					
DRA	M Total S	ize:	MB						
	M Brand:								
	M Size:	□ 64 KB	☐ 128 KB	□ 256 KB	□ 512 KB				
	M Part No	o. TAG:			DATA:				
	o Card:								
Video	o Chip or	Brand:							
Flopp	by Drive A	Capacity & Br	and:						
		Capacity & Br	and:						
Stora	ge Contro	ller Type	<b>M</b> FM	□ RLL	☐ IDE	☐ ESDI	☐ SCSI		
Hard	Drive C I	Brand & Type:							
Hard	Drive D I	Brand & Type:							
LAN	Controlle	r Type							
LAN	Card Bra	nd & Model:							
Seria	l / Paralle	Chip Brand &	Model:						
	se Brand &	_							
O. S.		□ DOS	□ OS/2	☐ NETWAI	RE 🔲 UI	NIX / XENIX V	er.:		
<b>4.</b> A	AUTOEX	EC.BAT & CO	ONFIG.SYS F	ile:					
5. I	Problem I	Description:							