GA - 586AL/S

USER'S MANUAL

PCI-ISA SOLUTION

PENTIUMA PCI - ISA BUS MAINBOARD

REV. 2S First Edition

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1. INTRODUCTION

1.1. PREFACE

Welcome to use the **GA - 586AL** motherboard. The motherboard is a 256 KB / 512 KB / 1 MB CACHE PENTIUM™ CPU based PC / AT compatible system with ISA bus and PCI Local Bus, and has been designed to be the fastest PC / AT system. There are some new features allow you to operate the system with just the performance you want.

This manual also explains how to install the motherboard for operation, and how to set up your CMOS CONFIGURATION with BIOS SETUP program.

1.2. KEY FEATURES

Pentium™ based PC / AT compatible mainboard with PCI - ISA Bus.
3 Master / Slave PCI Bus slots, 4 ISA Bus slots.
Supports Pentium / P5T running at 60 / 66 MHz.
Supports true 64 bits CACHE and DRAM access mode.
Supports 273 Pins (Socket 4) ZIF white socket on board.
Supports 256 KB / 512 KB / 1 MB second cache memory.
CPU L1 / L2 Write-Back cache operation.
Supports 2 - 160 MB DRAM memory on board.
Supports 2 channels Enhance PCI IDE ports for 4 IDE Device.
Supports 2xCOM, 1xLPT, 1x1.44MB Floppy port.
Supports Hardware and Software speed change function.
Licensed AWARD BIOS, FLASH EEPROM for BIOS update.
BENCHMARQ / ODIN 3287 RTC on board.
3/4BABY AT size, 4 layers PCB.

1.3. PERFORMANCE LIST

The following performance data list is the testing results of some popular benchmark testing programs. These data are just referred by users, and there is no responsibility for different testing data values gotten by users.(The different Hardware & Software configuration will result in different benchmark testing results.)

● CPU Pentium[™] 60 / 66 MHz

DRAM
 16 MB - 70 ns

• CACHE SIZE 256 KB

DISPLAY
 ATi Mach64 PCI VGA

STORAGE on board IDE + Conner 540MB H.D.

• O.S. MS DOS V6.2

<MAINBOARD>

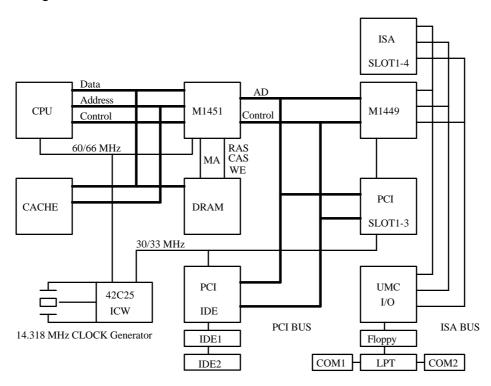
Testing Program	Item	Unit	Pentium 66	Pentium 60
LandMark V1.14	CPU	MHz	200+	200+
LandMark V2.0	CPU	MHz	385.88	347.29
	FPU	MHz	1135.31	1021.707
SI V7.0	CPU		211.7	190.6
Byte V2.1	CPU	AT Class	25.13	16.43
		386 Class	9.22	6.23
	FPU	AT Class	135.17	121.65
		386 Class	18.91	17.02
BenchMark V8.0	Processor Score		77.43	69.61
PM V1.7	MIPS	Mips	46.4	41.9
	DhryStone	K-Dstone/s	59.5	53.2
	WhetStone	K-Wstone/s	13148.9	11896.6

∢IDE≻

Program	Ver.	Item	Unit	Pentium 66	Pentium 60
Core	2.92	Data Transfer Rate	KB/s	10296.1	9307.1
		Average Seek Time	ms	10.5	10.5
		Track-Track Seek	ms	2.9	2.9
		Performance Index		66.517	60.661
Winbench	4.0	Disk Winmark	KB/s	2500	2350
Benchmark	8.0	Disk Score		1546	1500
PM	1.7	Data Transfer Rate	KB/s	32233.8	32233.8
		Mean Seek	ms/seek	10.7	10.9
		Track-Track Seek	ms/seek	2.8	2.9
		Aggregate	PUM's	4883.5	4881.9
Byte	2.1	Disk	AT Class	37.35	35.25
			386 Class	21.73	20.51
SI		Data Transfer Rate	KB/s	1958.7	1957.7
		Average Seek	ms	2.71	2.68
		Track-Track Seek	ms	10.29	10.23
		HDD Index		15.5	15.5
Winstone94	1.0	Winstone		106.5	96.7

1.4. BLOCK DIAGRAM

≺Figure 1.1≻



1.5. INTRODUCE THE PCI - BUS

Connecting devices to a CPU local bus can dramatically increase the speed of I/O-bound peripherals with only a slight increase in cost over traditional systems. This price / performance point has created a vast market potential for local bus products. The main barrier to this market has been the lack of an accepted standard for local bus peripherals. Many mainboard and chipset manufactures developed their own local bus implementations, but they are incompatible with each other. The VL (Video Electronics Standards Association) local bus and PCI (Peripheral Component Interconnect) bus specification was created to end this confusion.

The PCI - bus standard, under development since Jun. 1992, which is designed to bring workstation-level performance to standard PC platform. The PCI - bus removes many of the bottlenecks that have hampered PC for several years. On the PCI - bus, peripherals operate at the native speed of the computer system,

thus enabling data transfer between peripherals and the system at maximum speed. This performance is critical for bandwidth-constrained devices such as video, multimedia, mass storage, and networking adapters.

PCI - bus standard provides end-users with a low-cost, extendible and portable local bus design, which will allow system and peripherals from different manufactures to work together.

1.6. FEATURES

32 bits bus transfer mode.
Bus Master or Slave access.
Memory burst transfer to 132 MB/sec.
33 MHz operation speed.
10 device loading ability.
CPU independent.

2. SPECIFICATION

2.1. HARDWARE

• CPU	_	Pentium™ 60 / 66 MHz, P5T.
	_	273 pins (socket 4) ZIF white socket on board.
 COPROCESSOR 	_	Included in Pentium.
SPEED	_	60 / 66 MHz system and 30 / 33 PCI-Bus speed.
-	_	7.5 / 8 MHz AT bus speed.
	_	Hardware and Software speed switchable
		function (half speed).
 DRAM MEMORY 	_	3 banks 72 pins SIMM module socket on board
		(2 for Double banks, 1 for Single bank).
	_	Use 1 / 2 / 4 / 8 / 16 / 32 MB 70 ns SIMM module
		DRAM.
	_	2 ~ 160 MB DRAM size.
	_	Support Fast Page DRAM access mode.
 CACHE MEMORY 	_	40.45
	_	256 KB / 512 KB / 1 MB second cache memory
		on board.
	_	Support Write Back cache function for both CPU
		& on board cache.
 SHADOW RAM 	_	Main BIOS shadow function.
	_	Video BIOS shadow function programmable.
	_	
 I/O BUS SLOTS 	_	3 Master / Slave PCI-Bus.
	_	4 16 bits ISA Bus.
IDE PORTS	_	2 Enhanced IDE channels on board.
		(Using IRQ14,15)
	_	Support Mode 3, 4 IDE & ATAPI CD - ROM.
	_	Driver Support DOS, WINDOWS, OS/2, NT,
		UNIX, Netware.
I/O PORTS	_	Supports 2 16450 COM ports. (Using IRQ4,3)
	_	Supports 1 LPT port.(Using IRQ7)
	_	Supports 1 1.44MB Floppy port.
		(Using DMA2 & IRQ6)
• BIOS	_	128KB FLASH EEPROM.
DIMENSION	_	3/4Baby AT size / 4 layers.
		,

2.2. SOFTWARE

BIOS – Licensed AWARD BIOS.

- AT CMOS Setup, BIOS / Chipset Setup and

Hard Disk Utility included.

• O.S. & IDE DRIVER - Operation with MS-DOS, WINDOWS NT, OS/2,

NOVELL and SCO UNIX.

2.3. ENVIRONMENT

Ambient Temp.
 O°C to +50°C (Operating).

Relative Hum. – 0 to +85% (Operating).
Altitude – 0 to 10,000 feet (Operating).

● Vibration — 0 to 1,000 Hz.

● Electricity – 4.9 V to 5.2 V.

5 A to 7 A current.

3. HARDWARE INSTALLATION

3.1. UNPACKING

The mainboard package should contain the following:

- The GA 586AL mainboard.
- Diskette for Enhanced IDE Device Driver (1 x 1.44MB Diskette).
- USER'S MANUAL.
- Cable set for I/O Device.

The mainboard contains sensitive electric components which can be easily damaged by static electricity, so the mainboard should be left in its original packing until it is installed.

Unpacking and installation should be done on a grounded anti-static mat. The operator should be wearing an anti static wristband, grounded at the same point as the anti-static mat.

Inspect the mainboard carton for obvious damage. Shipping and handling may cause damage to your board. Be sure there are no shipping and handling damages on the board before proceeding.

After opening the mainboard carton, extract the system board and place it only on a grounded anti-static surface component side up. Again inspect the board for damage. Press down on all of the socket IC's to make sure that they are properly seated. Do this only on with the board placed on a firm flat surface.

● DO NOT APPLY POWER TO THE BOARD IF IT HAS BEEN DAMAGED.

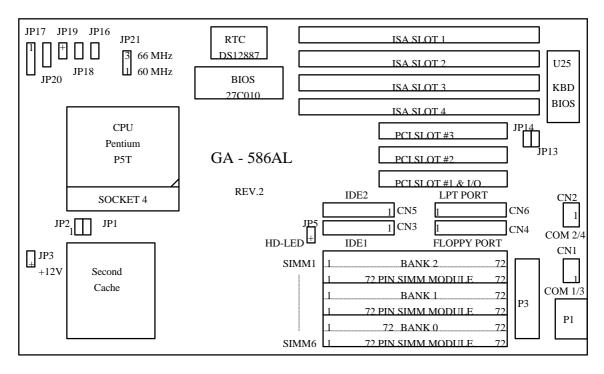
You are now ready to install your maniboard. The mounting hole pattern on the mainboard matches the IBM-AT system board. It is assumed that the chassis is designed for a standard IBM XT/AT mainboard mounting.

Place the chassis on the anti-static mat and remove the cover. Take the plastic clips, Nylon stand-off and screws for mounting the system board, and keep them

separate.

3.2. MAINBOARD LAYOUT

≺Figure 3.1≻



3.3. QUICK REFERENCE FOR JUMPERS & CONNECTORS

♦ P1: Ke	yboard Connector
Pin No.	Function
1	Key Clock.
2	Key Data.
3	NC.
4	VCC (+5V).
5	GND.

♦ P2: Powe	r Connector
Pin No.	Function
1	Power Good signal
2,10,11,12	VCC (+5V)
3	+12V
4	-12V
5,6,7,8	GND
9	-5V

♦ JP16: Res	set Switch
Open	For normal operation.
Close	For hardware reset system.

♦ JP17: F	♦ JP17: Power LED and Key-Lock Connector	
Pin No.	Function	
1	LED anode (+).	
2	NC.	
3	LED cathode (-).	
4	Key lock.	
5	GND.	

♦ JP18: Turbo Switch		
Pin No.	Function	
Close	For low speed (Non-cache).	
Open	For high speed.	

♦ JP19: T	urbo LED Connector
Pin No.	Function
1	LED cathode (+).
2	LED anode (-).

♦ JP20: \$	Speaker Connector
Pin No.	Function
1	VCC.
2	NC.
3	GND.
4	Data.

♦ JP1	JP2	Second Cache Size Selection	
Pin No.		Function	
1-2	1-2	Close for 256 KB cache size (using 32K x 8 SRAM).	
1-2	2-3	Close for 512 KB cache size (using 64K x 8 SRAM).	
2-3	2-3	Close for 1 MB cache size (using 128K x 8	
		SRAM).	

♦ JP21: System Speed Selection	
Pin No.	Function
1-2	Close for 60 MHz system speed.
2-3	Close for 66 MHz system speed.

♦ JP3: CPU Cooling Fan Power Connector		
Pin No.	Function	
1	+12V	
2	GND	

♦ CN1-6	♦ CN1-6 I/O Ports Connector	
CN1	For COM 1/3 (Serial port1)	
CN2	For COM 2/4 (Serial port2)	
CN4	For Floppy port	
CN6	For LPT port	
CN3	For Primary IDE port	
CN5	For Secondary IDE port	

3.4. DRAM INSTALLATION

The mainboard can be installed with 1 / 2 / 4 / 8 / 16 / 32 MB 72 pins SIMM module DRAM, and the DRAM speed must be 70 ns. The DRAM memory system on mainboard consists of bank 0, 1 & bank 2. The DRAM of any bank can be installed first. Each bank consist of 2 PCs, 72 pins SIMM module DRAM. Because the 72 pins SIMM module is 32 bits width, using 2 PCs which can match a 64 bits system. The total memory size is 2 - 160 MB, and various configuration of DRAM types in the following TABLE are available:

Bank 0	Bank 1	Bank 2
Single	None	None
Single	Single	
Single	Single	Single
Double	None	None
Double	Single	None
Double	Single	Single
Double	Single	Double

Single: Single bank SIMM Module

Ex. 1MB, 4MB, 16MB

Double: Double banks SImm Module

Ex. 2MB, 8MB, 32MB

The DRAM installation position refer to Figure 3.1, and notice the Pin 1 of SIMM module must match with the Pin 1 of SIMM socket when the DRAM SIMM module is installed.

Insert the DRAM SIMM module into the SIMM socket at 45 degree angle. If there is a wrong direction of Pin 1, the DRAM SIMM module couldn't be inserted into socket completely. After completely insert SIMM module into socket, then press the SIMM module in vertical direction until the left and right metal holders can keep the SIMM module standing up con-firmly.

3.5. SRAM INSTALLATION AND JUMPERS SETUP

The cache memory system consists of Data SRAM & TAG SRAM (U12), the Data SRAM type is 32Kx8-15/20 ns, 64Kx8-15/20 ns or 128Kx8-15/20 ns.

The mainboard can be installed with 256 KB, 512 KB or 1 MB cache memory when using 32Kx8, 64Kx8, 128Kx8 type DATA SRAM separately. Please refer to the following table to install cache memory system:

SIZE	Data SRAM	JP2	JP1	TAG SRAM (U12)
256 KB	32 KB x 8	1 - 2	1 - 2	8 KB x 8 - 32 K x 8
512 KB	64 KB x 8	2 - 3	1 - 2	16 KB x 8,32 K x 8
1	128 KB x 8	2 - 3	2 - 3	32 KB x 8
MB				

Because there are not standard IC's coding number of SRAM, refer to the following table to identify the SRAM component:

16 K x 8	32 K x 8	64 K x 8	128 K x 8
AE88128AK-15	W24257AK-15/20	W24512AK-15/20	W241024AK-15/20
	UM61256-15/20	AS7C512-15/20	AS7C1024-15/20
	AS7C256-15/20	UM61512AK-15/20	UM611024AK-15/20
	CY7C199-20		
	MCM6206-20		
	KM68257BP-20		
	TC555328P-20		

3.6. CPU INSTALLATION AND JUMPERS SETUP

The system's speed depends on the frequency of CLOCK GENERATOR. The user can change the JUMPER (JP21) selection to set up the system speed to 60 MHz or 66 MHz for 60 or 66 MHz PENTIUM™ CPU.

The mainboard can use PENTIUM™ or P5T CPU, and the CPU speed must match with the frequency of CLOCK GEN. It will cause system hanging up if the CLOCK GEN.'S frequency is higher than CPU's.

JP21	CPU Type	CLK. GEN. Speed	CPU Speed
2 - 3	PENTIUM™ - 66	66.667 MHz	66.667 MHz
1 - 2	PENTIUM™ - 60	60 MHz	60 MHz

- The CPU is a sensitive electric component and it can be easily damaged by static electricity, so users must keep it away from metal surface when the CPU is installed onto mainboard.
- When the user installs the CPU on socket, please notice the PIN 1 of CPU is in the same corner as the PIN 1 of socket!
- Before the CPU is installed, the mainboard must be placed on a flat plane in order to avoid being broken by the pressure of CPU installation.

3.7. CMOS RTC & ISA CFG CMOS SRAM

There're RTC & CMOS SRAM on board, they have a power supply from internal battery to keep the DATA inviolate & effective. The RTC is a REAL-TIME CLOCK device which provides the DATE & TIME to system. The CMOS SRAM is used for keeping the information of ISA device system configuration, so the system can automatically boot OS. every time.

Due to the life-time of RTC internal battery is 10 years, the user can change a new RTC to replace old one after it can not work. The new one's brand and type must be same with old one.

3.8. SPEAKER CONNECTOR INSTALLATION

There is always a speaker in AT system for sound purpose. The 4 - Pins connector **JP20** is used to connect speaker. The speaker can work well in both direction of connector when it is installed to the connector **JP20** on mainboard.

3.9. POWER LED & KEY LOCK CONNECTOR INSTALLATION

There are a system power LED lamp and a key on the panel of case. The power LED will light on when system is powered-on, and the key can lock the keyboard input or unlock it, both of them are connected to a 5 PIN connector. The connector should be installed to **JP17** of mainboard in correct direction.

3.10. TURBO SWITCH CONNECTOR INSTALLATION

The TURBO switch on the panel is used for controlling the system speed. Some

program developed on XT should be executed with a low speed system, so a high speed system needs the speed switching function to change its running speed.

The mainboard uses helf speed method to implement TURBO switching function. The **JP18** on mainboard should be connected to the TURBO switch on panel, and user can push in or pop out the TURBO switch to enable or disable the turbo function of system.

• If not necessary, please don't use De-turbo mode. (always keep system in Turbo mode)

3.11. TURBO LED CONNECTOR INSTALLATION

The TURBO LED on panel can indicate the current speed status of system. The TURBO LED connector should be installed to **JP19** in correct direction.

3.12. HARDWARE RESET SWITCH CONNECTOR INSTALLATION

The RESET switch on panel provides users with HARDWARE RESET function which is almost the same as power-on/off. The system will do a cold start after the RESET switch is pushed and released by user. The RESET switch is a 2 PIN connector and should be installed to **JP16** on mainboard.

3.13. PERIPHERAL DEVICE INSTALLATION

After the device installation and jumpers setup, the mainboard can be mounted into the case and fixed by screw. To complete the mainboard installation, the peripheral device could be installed now. The basic system needs a display interface card and a disk device.

If the PCI - Bus device is to be installed in the system, any one of three PCI - Bus slots can be used no matter Slave or Master PCI - Bus device being installed.

After installing the peripheral device, the user should check everything again, and prepare to power-on the system.

4. BIOS CONFIGURATION

Award's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS SRAM so that it retains the Setup information when the power is turned off.

4.1. ENTERING SETUP

Power ON the computer and press immediately will allow you to enter Setup. The other way to enter Setup is to power on the computer, when the below message appears briefly at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press <Ctrl>, <Alt>, and <Esc> keys.

- TO ENTER SETUP BEFORE BOOT PRESS CTRL-ALT-ESC OR DEL KEY If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" bottom on the system case. You may also restart by simultaneously press <Ctrl>,<Alt>, and keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to,
- PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP

4.2. CONTROL KEYS

Up arrow	Move to previous item	
Down arrow	Move to next item	
Left arrow	Move to the item in the left hand	
Right arrow	Move to the item in the right hand	
Esc key	Main Menu - Quit and not save changes into CMOS	
	Status Page Setup Menu and Option Page Setup Menu - Exit	
	current page and return to Main Menu	
PgUp key	Increase the numeric value or make changes	
PgDn key	Decrease the numeric value or make changes	
F1 key	General help, only for Status Page Setup Menu and Option Page	
	Setup Menu	
F2 key	Change color from total 16 colors	
F3 key	Calendar, only for Status Page Setup Menu	
F4 key	Reserved	
F5 key	Restore the previous CMOS value from CMOS, only for Option	
-	Page Setup Menu	
F6 key	Load the default CMOS value from BIOS default table, only for	
	Option Page Setup Menu	

F7 key	Load the default
F8 key	Reserved
F9 key	Reserved
F10 key	Save all the CMOS changes, only for Main Menu

4.3. GETTING HELP

4.3.1. Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

4.3.2. Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc>.

4.4. THE MAIN MENU

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 4.1) will appear on the screen. The Main Menu allows you to select from seven setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

Figure 4.1: Main Menu

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	PASSWORD SETTING
BIOS FEATURES SETUP	IDE HDD AUTO DETECTION
CHIPSET FEATURES SETUP	HDD LOW LEVEL FORMAT
PCI CONFIGURATION SETUP	SAVE & EXIT SETUP
LOAD SETUP DEFAULTS	EXIT WITHOUT SAVING
ESC: Quit F10: Save & Exit Setup	
Time, Date, H	ard Disk Type,

Standard CMOS setup

This setup page includes all the items in a standard compatible BIOS.

BIOS features setup

This setup page includes all the items of Award special enhanced features.

Chipset features setup

This setup page includes all the items of chipset special features.

PCI configuration setup

This setup page includes all the configurations of PCI slots parameters

Load setup defaults

BIOS defaults indicates the most appropriate value of the system parameter which the system would be in maximum performance.

Password setting

Change, set, or disable password. It allows you to limit access to the system and Setup, or just to Setup.

IDE HDD auto detection

Automatically configure hard disk parameter.

HDD LOW level format

Low level format IDE Hard disk.

Save & exit setup

Save CMOS value changes to CMOS and exit setup.

Exit without save

Abandon all CMOS value changes and exit setup.

4.5. STANDARD CMOS SETUP MENU

The items in Standard CMOS Setup Menu (Figure 4.2) are divided into 9 categories. Each category includes no, one or more than one setup items. Use the arrows to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

Figure 4.2: Standard CMOS Setup Menu

ROM PCI / ISA BIOS STANDARD CMOS SETUP AWARD SOFTWARE, INC.

Date (mm:dd:yy) : I Time (hh:mm:ss):								
HARD DISKS	TYPE S	SIZE	CYLS.	HEADS	PRECOMP	LANDZONE	SECTORS	S MODE
Primary Master	: None	0	0	0	0	0	0	
Primary Slave	: None	0	0	0	0	0	0	
Secondary Master	: None	0	0	0	0	0	0	
Secondary Slave	: None	0	0	0	0	0	0	
Driver A: 1.2M, 5	5.25 in.							
Driver B: None						Base Memory	: 640 K	
					Ext	ended Memory	: 7168 K	
Video : EGA/VG	Α				Exp	anded Memory	: 0 K	
Halt On: All Error						Other Memory	: 384 K	
Hait Oil . All Ellor	5					Total Memory	7: 8192 K	
ESC: Quit			$\uparrow \downarrow$	→ ← : ;	Select Item	Pi	U/ PD /+/- :	Modify
F1: Help			(Shift)F	72 : C	hang Color	F	3 : Taggle	Calender

Date

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

day	The day, from Sun to Sat, determined by the BIOS and is display-only
date	The date, from 1 to 31 (or the maximum allowed in the month)
month	The month, Jan. through Dec.
year	The year, from 1900 through 2099

Time

The time format in <nour> <minute> <second>. The time is calculated base on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

Primary HDDs / Secondary HDDs

The category identify the types of hard disk drive C drive F 4 devices that has been installed

in the computer. There are 46 pre-defined types and a user definable type. Type 1 to Type 46 are pre-defined. Type User is user-definable.

Press PgUp or PgDn to select a numbered hard disk type or type the number and press <Enter>. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.

If you select Type User, related information is asked to be entered to the following items. Enter the information directly from the keyboard and press <Enter>. Those information should be provided in the documentation form your hard disk vendor or the system manufacturer.

CYLS.	number of cylinders
HEADS	number of heads
PRECOMP	write precom
LANDZONE	landing zone
SECTORS	number of sectors

If a hard disk has not been installed select NONE and press <Enter>.

Drive A type / Drive B type

The category identify the types of floppy disk drive A or drive B that has been installed in the computer.

None	No floppy drive installed
360K, 5.25 in.	5-1/4 inch PC-type standard drive; 360 kilobyte capacity
1.2M, 5.25 in.	5-1/4 inch AT-type high-density drive; 1.2 megabyte capacity
720K, 3.5 in.	3-1/2 inch double-sided drive; 720 kilobyte capacity
1.44M, 3.5 in.	3-1/2 inch double-sided drive; 1.44 megabyte capacity
2.88M, 3.5 in.	3-1/2 inch double-sided drive; 2.88 megabyte capacity

Video

The category detects the type of adapter used for the primary system monitor that must matches your video display card and monitor. Although secondary monitors are supported,

you do not have to select the type in setup.

EGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA,
	SVGA, or PGA monitor adapters
CGA 40	Color Graphics Adapter, power up in 40 column mode
CGA 80	Color Graphics Adapter, power up in 80 column mode
MONO	Monochrome adapter, includes high resolution monochrome adapters

Halt on

The category determines whether the computer will stop if an error is detected during power

up.

NO errors	Whenever the BIOS detects a non-fatal error the system will be stopped and you will be prompted
All errors	The system boot will not be stopped for any error that may be detected
All, But Keyboard	The system boot will not stop for a keyboard error; it will stop for all other errors
All, But Diskette	The system boot will not stop for a disk error; it will stop for all other errors
All, But Disk/Key	The system boot will not stop for a keyboard or disk error; it will stop for all other errors

Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

Base Memory

The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system. The value of the base memory is typically 512 K for systems with 512 K memory installed on the motherboard, or 640 K for systems with 640 K or more memory installed on the motherboard.

Extended Memory

The BIOS determines how much extended memory is present during the POST. This is the amount of memory located above 1 MB in the CPU's memory address map.

Expanded Memory

Expanded Memory in memory defined by the Lotus/Intel/Microsoft (LIM) standard as EMS. Many standard DOS applications can not utilize memory above 640K, the Expanded Memory Specification (EMS) swaps memory which not utilized by DOS with a section, or frame, so these applications can access all of the system memory. Memory can be swapped by EMS is usually 64K within 1 MB or memory above 1 MB, depends on the chipset design.

Expanded memory device driver is required to use memory as Expanded Memory.

Other Memory

This refers to the memory located in the 640K to 1024K address space. This is memory that can be used for different applications. DOS uses this area to load device drivers to keep as much base memory free for application programs. Most use for this area is Shadow RAM.

4.6. BIOS FEATURES SETUP

ROM PCI / ISA BIOS BIOS FEATURES SETUP AWARD SOFTWARE, INC.

Virus Warning : Enabled Video BIOS Shadow : Enabled C8000 - CFFFF Shadow: Disabled **CPU Internal Cache** : Enabled External Cache : Enabled Quick Power On Self Test : Disabled **Boot Sequence** : A, C : Disabled Swap Floppy Drive Boot Up Floppy Seek : Enabled Boot Up NumLock Status : On IDE HDD Block Mode : Disabled **Security Option** : Setup IDE Second Channel Control : Disabled : Select Item ESC: Quit F1: Help : Modify F5 : Old Values (Shift)F2 : Color F6: Load BIOS Defaults F7: Load Setup Defaults

Virus Warning

This category flashes on the screen. During and after the system boots up, any attempt to write to the boot sector or partition table of the hard disk drive will halt the system and the following error message will appear, in the mean time, you run can anti-virus program to locate the problem. Default value is Enabled.

Enabled	Activate automatically when the system boots up causing a warning message to appear when anything attempts to access the boot sector or hard disk partition table
Disabled	No warning message to appear when anything attempts to access the boot sector or hard disk partition table

CPU Internal Cache / External Cache

These two categories speed up memory access. However, it depends on CPU / chipset design. The default value is Enabled.

Enabled	Enable cache
Disabled	Disable cache

Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power on the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST. The default value is Disabled.

Enabled	Enable quick POST
Disabled	Normal POST

Boot Sequence

This category determines which drive computer searches first for the disk operating system (i.e., DOS). Default value

is A,C.

A,C	System will first search for floppy disk drive then hard disk drive
C,A	System will first search for hard disk drive then floppy disk drive

Swap Floppy Drive

The default value is Disabled.

Enabled	Floppy A & B will be swapped under DOS
Disabled	Floppy A & B will be normal definition

Boot Up Floppy Seek

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360K type is 40 tracks while 720K, 1.2M and 1.44M are all 80 tracks. The default value is Enabled.

Enabled	BIOS searches for floppy disk drive to determine if it is 40 or 80 tracks, Note that BIOS can
	not tell from 720K, 1.2M or 1.44M drive type as they are all 80 tracks
Disabled	BIOS will not search for the type of floppy disk drive by track number. Note that there will not
	be any warning message if the drive installed is 360K

Boot Up NumLock Status

The default value is On

On	Keypad is number keys
Off	Keypad is arrow keys

IDE HDD Block Mode

The default value is Disabled.

Enabled	Enable IDE HDD Block Mode
Disabled	Disable IDE HDD Block Mode

Security Option

This category allows you to limit access to the system and Setup, or just to Setup. The default value is Setup.

System	The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt
Setup	The system will boot, but access to Setup will be denied if the correct password is not entered at the prompt

- To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.
- IDE Second Channel Control

The default value is Enabled.

Enabled	Enable 2nd IDE Channel
Disabled	Disable 2nd IDE Channel

Video BIOS Shadow

It determines whether video BIOS will be copied to RAM, however, it is optional from chipset design. Video Shadow will increase the video speed. The default value is Enabled.

Enabled	Video shadow is enabled
Disabled	Video shadow is disabled

C8000 - CFFFF Shadow / D0000 - DFFFF Shadow

These categories determine whether optional ROM will be copied to Ro

These categories determine whether optional ROM will be copied to RAM by 32 K byte. The default value are Disabled.

Enabled	Optional shadow is enabled
Disabled	Optional shadow is disabled

4.7. CHIPSET FEATURES SETUP

ROM PCI / ISA BIOS CHIPSET FEATURES SETUP AWARD SOFTWARE, INC.

L2 Cache Update Policy : Write Back Onboard PCI / IDE chip Cache Wait / 1 = 66, 0 = 60 MHz : 0 Wait: Normal **DRAM Timing Option** DRAM Refresh Peroid : Long Hidden Refresh Control : Enable 15 - 16 M Memory Location : Local

: Enabled Onboard FDD Controller : Enabled : Enabled Onboard Parallel Mode Onboard Parallel Port : 378H Onboard Serial Port1 : COM1 Onboard Serial Port2 : COM2

 $\uparrow \downarrow \rightarrow \leftarrow$: Select Item ESC: Quit F1: Help PU/PD/+/- : Modify F5 : Old Values (Shift)F2 : Color

F6: Load BIOS Defaults F7: Load Setup Defaults

L2 Cache Update Policy

The default value is Write Back.

Write Back	L2 Cache Write Back Operation.
Write Through	L2 Cache Write Through Operation.

NOTE: The cacheable DRAM Region is different for above two Cache Update

Policy.

L2 CACHE	Write Back	Write Through.
256 KB	32 MB	64 MB
512 KB	64 MB	128 MB
1 MB	128 MB	160 MB

Cache Wait / 1 = 66, 0=60 MHz

The default value is 0 Wait.

0 Wait	For 60 MHz CPU speed.
1 Wait	For 66 MHz CPU speed.

DRAM Timing Option

The default value is Normal.

Fast	For 60 ~ 70 ns DRAM.
Normal	For 70 ns DRAM.
Slow	For 70 ~ 80 ns DRAM.

DRAM Refresh Peroid

The default value is Long.

Long	For DRAM Refresh Peroid = 60 us. (better Performance)
Normal	For DRAM Refresh Peroid = 15 us. (standard)

Hidden Refresh Control

The default value is Enable.

Enable	For better Performance DRAM Refresh Operation.	
Disable	For standard DRAM Refresh Option.	

• 15 - 16 M Memory Location:

The default value is Local.

Local	Normal Operation	
Non-Local	A memory space reserved for ISA bus, it's address is from	
	15 MB to 16 MB and the space is 1 MB.	

Onboard PCI / IDE chip

The default value is Enable.

Enabled	Enable onboard PCI IDE Function (CMD 640B chip).	
Disabled	Disable onboard PCI IDE Function (CMD 640B chip).	

Onboard FDD Controller

The default value is Enable.

Enabled	Enable onboard FDD port.
Disabled	Disable onboard FDD port.

Onboard Parallel Mode

The default value is SPP.

Disabled	Disable onboard LPT port	
Enabled	Using Parallel port as Standard Printer Port	

Onboard Parallel port

The default value is 378H.

378H	Enable onboard LPT port and address is 378H	
278H	Enable onboard LPT port and address is 278H	
None	Disable onboard LPT port	

Onboard Serial Port1

The default value is COM1.

COM1	Enable onboard Serial port1 and address is 3F8H	
COM3	Enable onboard Serial port1 and address is 3E8H	
None	Disable onboard Serial port1	

Onboard Serial Port2

The default value is COM2.

COM2	Enable onboard Serial port2 and address is 2F8H	
COM4	Enable onboard Serial port2 and address is 2E8H	
None	Disable onboard Serial port2	

4.8. PCI SLOT CONFIGURATION

ROM PCI / ISA BIOS PCI SLOT CONFIGURATION AWARD SOFTWARE, INC.

SLOT 1 INT# Map To SLOT 2 INT# Map To SLOT 3 INT# Map To GA-410 NCR PCI SCSI 1st Available IRQ 2nd Available IRQ 3rd Available IRQ 4th Available IRQ 5th Available IRQ PCI IDE IRQ Map To	: AUTO : AUTO : AUTO : SLOT1 : 9 : 10 : 11 : 12 : 5 :ISA	PCI VGA Fully 32bit Access : Off PCI-to-CPU Write Buffer : On PCI-to-CPU Read Buffer : Off
		ESC: Quit : Select Item F1: Help PU/PD/+/-: Modify F5: Old Values (Shift)F2: Color F6: Load BIOS Defaults F7: Load Setup Defaults

SLOT 1~3 INT# Map To

The default value is AUTO.

	AUTO	The BIOS auto detect the PCI device using INTA ~ D and auto set up a available IRQ to let device use.	
Ī	A ~ D	For some not PCI specification compliant device, the user must set up IRQ manually.	

GA-410 NCR PCI SCSI

The default value SLOT1.

The GA-410 NCR 810 PCI SCSI card has a Jumper to select the card to be Primary or Secondary card. If the card is been set up to Primary then the user must set up the selection correctly (and the INT#A must be used). If the card is been set up to Secondary then the user only set up the above selections.

Available IRQ

The default value is shown on the above table.

These available IRQs are Mapped to be PCI INT# by BIOS for PCI device automaticly. If some one IRQ is used by

ISA device then the user must keep the IRQ out of the available table.

• PCI IDE IRQ Map To: ISA

ISA	For PCI IDE card using paddle card to connect ISA IRQ or using onboard IDE controller.	
PCI-Auto	For PCI specification compliant IDE device, the BIOS can Auto-detect the card using PCI	
	SLOT NO. & IRQ correctly.	
PCI-SLOTX	The user setup the SLOT NO. & the INT# used by PCI IDE device manually.	

• PCI VGA Fully 32bit Access (byte merge)

The default value is Off.

Off	Disable the function.	
On	Enable the system access the PCI	VGA card always in 32bit data format access (byte
	merged).	

• PCI VGA Fully 32bit Access (byte merge)

The default value is Off.

Off	Disable the function.	
On	Enable the system access the PCI	VGA card always in 32bit data format (byte merged).

NOTE: For some PCI VGA, the function can not be enabled. (Ex. IIT AGX016)

• PCI-to-CPU Write Buffer

The default value is On.

On	Enable PCI to CPU Write Buffer (better performance for PCI Master Device).
Off	Disable PCI to CPU Write Buffer (for some PCI Master Device).

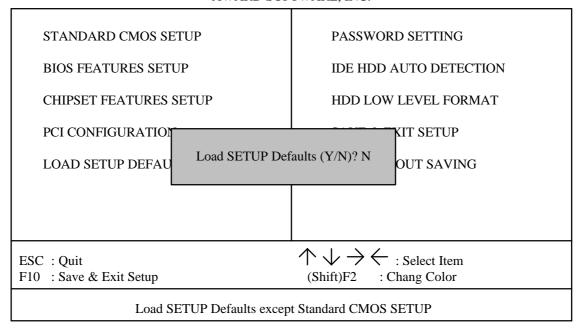
PCI-to-CPU Read Buffer

The default value is Off.

On	Enable PCI to CPU Read Buffer (better performance for PCI Master Device in multi-task									
	environment).									
Off	Disable PCI to CPU Read Buffer .									

4.9. LOAD SETUP DEFAULTS

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Load SETUP Defaults

To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

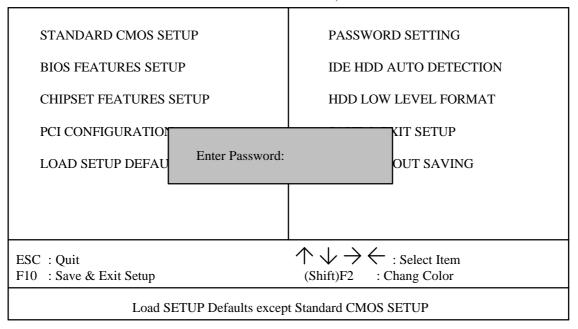
SETUP DEFAULTS step is recommended.

4.10. PASSWORD SETTING

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

ENTER PASSWORD

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Type the password, up to eight characters, and press <Enter>. The password typed now will clear and previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED

If you select System at Security Option of BIOS Features Setup Menu, you will be prompted for the password every time the system is rebooted or any time you try to enter Setup. If you select Setup at Security Option of BIOS Features Setup Menu, you will be prompted only when you try to enter Setup.

4.11. IDE HDD AUTO DETECTION

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

HARD DISKS	TYPE	SIZE	CYLS.	HEAD	PRECOMP	LANDZ	SECT	OR MODE
	·		·		·		·	
Select Primary Master Option (N=Skip): N								
OPTION	SIZE	CYLS.	HEAD	PRECO	MP LAI	NDZ	SECTOR	MODE
1 (Y)	521	1060	16	6553	35	1059	63	NORMAL
2	521	530	32		0	1059	63	LBA
3	521	530	32	6553	35	1059	63	LARGE
				ESC : Skip				

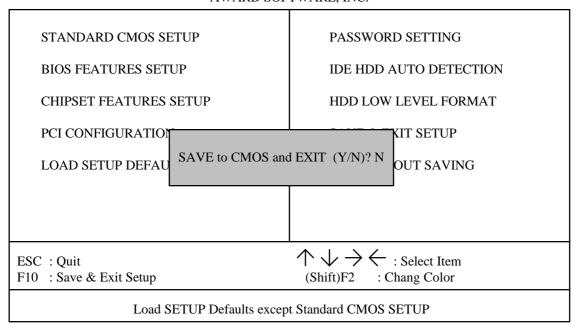
Type "Y" will accept the H.D.D. parameter reported by BIOS. Type "N" will keep the old H.D.D. parameter setup. If the hard disk cylinder NO. is over 1024, then the user can seclet LBA mode or LARGER mode for DOS partition LARGE than 528 MB.

4.12. HDD LOW LEVEL FORMAT

This selection support IDE HDD Low Level Format function.

4.13. SAVE & EXIT SETUP

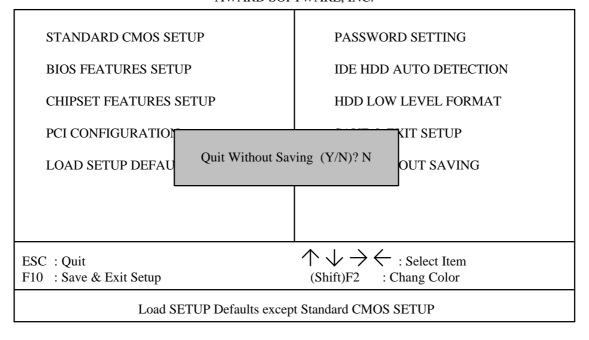
ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Type "Y" will quit the Setup Utility and save the user setup value to RTC CMOS SRAM. Type "N" will return to Setup Utility.

4.14. EXIT WITHOUT SAVING

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Type "Y" will quit the Setup Utility without saving to RTC CMOS SRAM. Type "N" will return to Setup Utility.

4.15. KEYBOARD SETTING FUNCTION

After booting the O.S., there are some special functions used by keyboard as follows:

"CTRL_ALT_DEL"	-	Pressing these keys simultaneously will cause system to Warm Start (Software Reset).
"CTRL_ALT_[+]"	_	Pressing these keys simultaneously will change the system speed to high speed (Turbo, all cache memory enable).
"CTRL_ALT_[-]"	_	Pressing these keys simultaneously will change the system speed to low speed (Normal, disable cache memory).

5. AT TECHNICAL INFORMATION

5.1. I/O BUS CONNECTOR PIN OUT

5.1.1.ISA SLOT PIN OUT

							-
GND	B01	A01	I/O CH CHK	-MEMCS16	D01	C01	SBHE
RESET	B02	A02	SD07	-I/OCS16	D02	C02	LA23
+5V	B03	A03	SD06	IRQ10	D03	C03	LA22
IRQ9	B04	A04	SD05	IRQ11	D04	C04	LA21
-5V	B05	A05	SD04	IRQ12	D05	C05	LA20
DRQ2	B06	A06	SD03	IRQ15	D06	C06	LA19
-12V	B07	A07	SD02	IRQ14	D07	C07	LA18
0WS	B08	A08	SD01	-DACK0	D08	C08	LA17
+12V	B09	A09	SD00	DRQ0	D09	C09	MEN
GND	B10	A10	I/O CH RDY	-DACK5	D10	C10	MEN
-SMEMW	B11	A11	AEN	DRQ5	D11	C11	SD08
-SMEMR	B12	A12	SA19	-DACK6	D12	C12	SD09
-IOW	B13	A13	SA18	DRQ6	D13	C13	SD10
-IOR	B14	A14	SA17	-DACK7	D14	C14	SD11
-DACK3	B15	A15	SA16	DRQ7	D15	C15	SD12
-DRQ3	B16	A16	SA15	+5V	D16	C16	SD13
-DACK1	B17	A17	SA14	-MASTER	D17	C17	SD14
-DRQ1	B18	A18	SA13	GND	D18	C18	SD15
-REFRESH	B19	A19	SA12				1
BCLK	B20	A20	SA11				
IRQ7	B21	A21	SA10				
IRQ6	B22	A22	SA09				
IRQ5	B23	A23	SA08				
IRQ4	B24	A24	SA07				
IRQ3	B25	A25	SA06				
-DACK2	B26	A26	SA05				
T/C	B27	A27	SA04				
BALE	B28	A28	SA03				
+5V	B29	A29	SA02				
OSC	B30	A30	SA01				
GND	B31	A31	SA00				
			ı				

5.1.2.PCI - BUS SLOT PIN OUT

			_
-12V	B01	A01	NC
NC	B02	A02	+12V
GND	B03	A03	NC
NC	B04	A04	NC
VCC	B05	A05	VCC
VCC	B06	A06	INTA#
INTB#	B07	A07	INTC#
INTD#	B08	A08	VCC
PST#1	B09	A09	NC
NC	B10	A10	VCC
PST#2	B11	A11	NC
GND	B12	A12	GND
GND	B13	A13	GND
NC	B14	A14	NC
GND	B15	A15	RST#
CLK	B16	A16	vcc
GND	B17	A17	GNT#
REQ#	B18	A18	GND
VCC	B19	A19	NC
AD_31	B20	A20	AD_30
AD_29	B21	A21	NC
GND	B22	A22	AD_28
AD_27	B23	A23	AD_26
AD_25	B24	A24	GND
NC	B25	A25	AD_24
CBE#3	B26	A26	IDSEL
AD_23	B27	A27	NC
GND	B28	A28	AD_22
AD_21	B29	A29	AD_20
AD_19	B30	A30	GND
NC	B31	A31	AD_18
AD_17	B32	A32	AD_16
CEB#2	B33	A33	NC
GND	B34	A34	FRAME#
IRDY#	B35	A35	GND
NC	B36	A36	TRDY#
DEVSEL#	B37	A37	GND
GND	B38	A38	STOP#
LOCK#	B39	A39	NC
PERR#	B40	A40	SDONE
NC	B41	A41	SBO#
SERR#	B42	A42	GND
NC	B43	A43	PAR
CBE#1	B44	A44	AD_15
AD_14	B45	A45	NC
GND	B46	A46	AD_13
AD_12	B47	A47	AD_11
AD_10	B48	A48	GND
GND	B49	A49	AD_09
AD_08	B52	A52	CBE#0
AD_07	B53	A53	NC
NC	B54	A54	AD_06
AD_05	B55	A55	AD_04
AD_03	B56	A56	GND
GND	B57	A57	AD_02
AD_01	B58	A58	AD_00
VCC	B59	A59	VCC
NC	B60	A60	NC
VCC	B61	A61	vcc
VCC	B62	A62	vcc
			•

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5.2. I/O & MEMORY MAP

MEMORY MAP: [000000-009FFFF] System memory used by DOS and application program.

[00A0000-00BFFFF] Display buffer memory for VGA/

EGA/CGA/MONOCHROME adapter.

[00C0000-00DFFFF] Reserved for I/O device BIOS ROM or RAM buffer.

[00E0000-00EFFFF] Reserved for PCI device ROM.

[00F0000-00FFFFF] System BIOS ROM. [0100000-BFFFFFF] System extension memory.

I/O MAP: [000-01F] DMA controller.(Master)

[020-021] INTERRUPT controller.(Master)
[022-023] CHIPSET control registers I/O ports.

[040-05F] TIMER control registers.

[060-06F] KEYBOARD interface controller.(8042)

[070-07F] RTC ports & CMOS I/O ports.

[080-09F] DMA register.

[0A0-0BF] INTERRUPT controller.(Slave)

[0C0-0DF] DMA controller.(Slave) [0F0-0FF] MATH COPROCESSOR [1F0-1F8] HARD DISK controller. [278-27F] PARALLEL port-2.

[2B0-2DF] GRAPHICS adapter controller.

[2F8-2FF] SERIAL port-2. [360-36F] NETWORK ports. [378-37F] PARALLEL port-1

[3B0-3BF] MONOCHROME & PRINTER adapter.

[3C0-3CF] EGA adapter. [3D0-3DF] CGA adapter.

[3F0-3F7] FLOPPY DISK controller.

[3F8-3FF] SERIAL port-1.

5.3. TIMER & DMA CHANNELS MAP

TIMER MAP: TIMER Channel-0 System timer interrupt

TIMER Channel-1 DRAM REFRESH request TIMER Channel-2 SPEAKER tone generator

DMA CHANNELS: DMA Channel-0 Available

DMA Channel-1 IBM SDLC

DMA Channel-2 FLOPPY DISK adapter

DMA Channel-3 Available

DMA Channel-4 Cascade for DMA controller 1

DMA Channel-5 Available DMA Channel-6 Available DMA Channel-7 Available

5.4. INTERRUPT MAP

NMI: Parity check error

IRQ (H/W): 0 System TIMER interrupt from TIMER-0

1 KEYBOARD output buffer full

2 Cascade for IRQ 8-15 3 SERIAL port 2 4 SERIAL port 1 5 PARALLEL port 2 6 FLOPPY DISK adapter 7 PARALLEL port 1

8 RTC clock 9 Available 10 Available 11 Available 12 Available

13 MATH coprocessor14 HARD DISK adapter

15 Available

5.5. RTC & CMOS RAM MAP

RTC & CMOS:	00	Seconds
	01	Second alarm
	02	Minutes
	03	Minutes alarm
	04	Hours
	05	Hours alarm
	06	Day of week
	07	Day of month
	80	Month
	09	Year
	0A	Status register A
	0B	Status register B
	0C	Status register C
	0D	Status register D
	0E	Diagnostic status byte
	0F	Shutdown byte
	10	FLOPPY DISK drive type byte
	11	Reserve
	12	HARD DISK type byte
	13	Reserve
	14	Equipment byte
	15	Base memory low byte
	16	Base memory high byte
	17	Extension memory low byte
	18	Extension memory high byte
	19-2d	
	2E-2F	
	30	Reserved for extension memory low byte
	31	Reserved for extension memory high byte
	32	DATE CENTURY byte
	33	INFORMATION FLAG
	34-3F	Reserve
	40-7f	Reserved for CHIPSET SETTING DATA

APPENDIX A: POST MESSAGE

When the BIOS encounters an error that requires the user to correct something, either a beep code will sound or a message will be displayed in a box in the middle of the screen and the message PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP will be shown in the information box at the bottom.

POST BEEP

Currently there is only one beep code in BIOS. This code indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps.

ERROR MESSAGE

Once or more of the following messages may be displayed if the BIOS detects an error during the POST. This list includes message for both the ISA and the EISA BIOS.

EXECUTE CMOS BATTERY HAS FAILED

CMOS battery is no longer functional. It should be replaced.

IDENTIFY AND SET OF STREET AND SET OF SET

Checksum of CMOS is incorrect. This can indicate that CMOS has become corrupt. This error may have been caused by a weak battery. Check the battery and replace if necessary.

DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER

No boot device was found. Insert a system disk into Drive A: and press <Enter>. If you assumed the system would boot from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also be sure the disk is formatted as a boot device. Then reboot the system.

IDENTIFY AND SET UP S

Type of diskette drive installed in the system is different from the CMOS definition. Run Setup to re-configure the drive type correctly.

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□ DISPLAY SWITCH IS SET INCORRECTLY

Display switch on the motherboard can be set to either monochrome or color. This indicates the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper, or enter Setup and change the VIDEO selection.

□ DISPLAY TYPE HAS CHANGED SINCE LAST BOOT

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.

The EISA non-volatile RAM checksum is incorrect or cannot correctly read the EISA slot. This can indicate either the EISA non-volatile memory has become corrupt or the slot has configured incorrectly. Also be sure the card is installed firmly in the slot.

The slot configuration information stored in the EISA non-volatile memory is incomplete.

When either of these errors appear, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

Hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also be sure the correct hard drive type is selected in Setup.

ERROR INITIALIZING HARD DISK CONTROLLER

Cannot initialize controller. Make sure the cord is correctly and firmly installed in the bus. Be sure the correct hard drive type is selected in Setup.

Also check to see if any jumper needs to be set correctly in the hard drive.

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there are no floppy drives installed, be sure the Diskette Drive selection in Setup is set to NONE.

The non-volatile memory containing EISA configuration information was programmed incorrectly or has become corrupt. Re-run EISA configuration utility to correctly program the memory.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

⋉ KEYBOARD ERROR OR NO KEYBOARD PRESENT

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during the boot.

If you are purposely configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

Memory Address Error at ...

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

Indicates a memory parity error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

Memory has been added or removed since the last boot. In EISA mode use Configuration Utility to re-configure the memory configuration. In ISA mode enter Setup and enter the new memory size in the memory fields.

Indicates an error verifying a value already written to memory. Use the location along with your system's memory map to locate the bad chip.

○ OFFENDING ADDRESS NOT FOUND

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

IX OFFENDING SEGMENT:

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem has been isolated.

This will be displayed at the bottom screen when an error occurs that requires you to reboot. Press any key and the system will reboot.

PRESS F1 TO DISABLE NMI, F2 TO REBOOT

When BIOS detects a Non-maskable Interrupt condition during boot, this will allow you to disable the NMI and continue to boot, or you can reboot the system will the NMI enabled.

Indicates a parity error in Random Access Memory.

Should Be Empty But EISA Board Found PLEASE RUN EISA CONFIGURATION UTILITY

A valid board ID was found in a slot that was configured as having no board ID.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

Should Have EISA Board But Not Found PLEASE RUN EISA CONFIGURATION UTILITY

The board installed is not responding to the ID request, or no board ID has been found in the indicated slot.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

Slot Not Empty

Indicates that a slot designated as empty by the EISA Configuration Utility actually contains a board.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT ...

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

PLEASE RUN EISA CONFIGURATION UTILITY

The board ID does not match the ID stored in the EISA non-volatile memory.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

APPENDIX B: POST CODES

FISA POST codes are typically output to port address 300h. ISA POST codes are typically output to port address 80h.

POST	Name	Description
C0	Turn Off Chipset Cache	OEM Specific-Cache control.
1	Processor Test 1	Processor Status (1 FLAGS) Verification.
		Test the following processor status flags
		carry, zero, sign, overflow,
		The BIOS will set each of these flags, verify they are set, then
		turn each flag off and verify it is off.
2	Processor Test 2	Read/Write/Verify all CPU registers except SS, SP, and BP with data pattern FF and 00.
3	Initialize Chips	Disable NMI, PIE, AIE, UEI, SQWV.
		Disable video, parity checking, DMA.
		Reset math coprocessor.
		Clear all page registers, CMOS shutdown byte. Initialize timer 0, 1, and 2, including set EISA timer to a known
		state.
		Initialize DMA controllers 0 and 1.
		Initialize interrupt controllers 0 and 1.
		Initialize EISA extended registers.
4	Test Memory Refresh	RAM must be periodically refreshed in order to keep the
	Toggle	memory from decaying. This function assures that the memory
		refresh function is working properly.
5	Blank video, Initialize	Keyboard controller initialization.
	keyboard	
6	Reserved	Varifica CMCC is warding correctly detects had bettery
7	Test CMOS Interface and Battery Status	Verifies CMOS is working correctly, detects bad battery.
BE	Chipset Default Initialization	Program chipset registers with power on BIOS defaults.
C1	Memory presence test	OEM Specific-Test to size on-board memory.
C5	Early Shadow	OEM Specific-Early Shadow enable for fast boot.
C6	Cache presence test	External cache size detection.
8	Setup low memory	Early chip set initialization.
		Memory presence test.
		OEM chip set routines. Clear low 64 K of memory.
		Test first 64 K memory.
9	Early Cache	Cyrix CPU initialization.
	Initialization	Cache initialization.
Α	Setup Interrupt Vector	Initialize first 120 interrupt vectors with SPURIOUS_INT-HDLR
	Table	and initialize INT 00h-1Fh according to INT_TBL.
В	Test CMOS RAM Checksum	Test CMOS RAM Checksum, if bad, or insert key pressed, load defaults.
С	Initialize keyboard	Detect type of keyboard controller (optional).
	·	Set NUM_LOCK status.
D	Initialize Video	Detect CPU clock.
	Interface	Read CMOS location 14h to find out type of video in use.
_	Toot Video Marray	Detect and Initialize Video Adapter.
Е	Test Video Memory	Test video memory, write sign-on message to screen.

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		Setup shadow RAM - Enable shadow according to Setup.
F	Test DMA Controller 0	BIOS checksum test.
		Keyboard detect and initialization.
10	Test DMA Controller 1	,
11	Test DMA Page	Test DMA Page Registers.
	registers	
12-13	Reserved	
14	Test Timer Counter 2	Test 8254 Timer 0 Counter 2.
15	Test 8259-1 Mask Bits	Verify 8259 Channel 1 masked interrupts by alternately turning
		off and on the interrupt lines.
16	Test 8259-2 Mask Bits	Verify 8259 Channel 2 masked interrupts by alternately turning off and on the interrupt lines.
17	Test Stuck 8259's Interrupt Bits	Turn off interrupts then verify no interrupt mask register is on.
18	Test 8259 Interrupt Functionality	Force an interrupt and verify the interrupt occurred.
19	Test Stuck NMI Bits (Parity/IO Check)	Verify NMI can be cleared.
1A	(i diny/10 offoot)	Display CPU clock.
1B-1E	Reserved	Diopidy of O Glook.
1F	Set EISA Mode	If EISA non-volatile memory checksum is good, execute EISA
11		initialization. If not, execute ISA tests an clear EISA mode flag. Test EISA Configuration Memory Integrity (checksum & communication interface).
20	Enable Slot 0	Initialize slot 0 (System Board).
21-2F	Enable Slots 1-15	Initialize slot 1 through 15.
30	Size Base and Extended Memory	Size base memory from 256 K to 640 K extended memory above 1 MB.
31	Test Base and Extended Memory	Test base memory from 256 K to 640 K and extended memory above 1 MB using various patterns. This will be skipped in EISA mode and can be "skipped" with ESC key in ISA mode.
32	Test EISA Extended Memory	If EISA Mode flag is set then test EISA memory found in slots initialization. This will be skipped in ISA mode and can be "skipped" with ESC key in EISA mode.
33-3B	Reserved	
3C	Setup Enabled	
3D	Initialize & Install Mouse	Detect if mouse is present, initialize mouse, install interrupt vectors.
3E	Setup Cache Controller	Initialize cache controller.
3F	Reserved	
BF	Chipset Initialization	Program chipset registers with Setup values.
40		Display virus protest disable or enable.
41	Initialize Floppy Drive & Controller	Initialize floppy disk drive controller and any drives.
42	Initialize Hard Drive & Controller	Initialize hard drive controller and any drives.
43	Detect & Initialize Serial/Parallel Ports	Initialize any serial and parallel ports (also game port).
44	Reserved	
45	Detect & Initialize Math	Initialize math coprocessor.
.5	Coprocessor	ao main ooprooodon
46	Reserved	
47	Reserved	
48-4D	Reserved	
4E	Manufacturing POST	Reboot if Manufacturing POST Loop pin is set. Otherwise
	Loop or Display	display any messages (i.e., any non-fatal errors that were

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Appendix B: Post Codes

	Messages	detected during POST) and enter Setup.
4F	Security Check	Ask password security (optional).
50	Write CMOS	Write all CMOS values back to RAM and clear screen.
51	Pre-boot Enable	Enable parity checker.
		Enable NMI, Enable cache before boot.
52	Initialize Option ROMs	Initialize any option ROMs present from C8000h to EFFFFh.
		When FSCAN option is enabled, will initialize from C8000h
		to F7FFFh.
53	Initialize Time Value	Initialize time value in 40h: BIOS area.
60	Setup Virus Protect	Setup virus protect according to Setup
61	Set Boot Speed	Set system speed for boot
62	Setup NumLock	Setup NumLock status according to Setup
63	Boot Attempt	Set low stack.
		Boot via INT 19h.
B0	Spurious	If interrupt occurs in protected mode.
B1	Unclaimed NMI	If unmasked NMI occurs, display
		Press F1 to disable NMI, F2 reboot.
E1-EF	Setup Pages	E1 - Page 1, E2 - Page 2, etc.
FF	Boot	

APPENDIX C: BIOS DEFAULT DRIVE TABLE

Type	Size (MB)	Cylinders	Heads	Sectors	Write / Precomp	Land Zone	Example Model	
1	10 MB	306	4	17	128	305	TEAC SD510 MMI 112, 5412	
2	20 MB	615	4	17	300	615	Seagate ST225, ST4026	
3	31 MB	615	6	17	300	615		
4	62 MB	940	8	17	512	940		
5	47 MB	940	6	17	512	940		
6	20 MB	615	4	17	65535	615	Seagate ST125 Tandon TM262	
7	31 MB	462	8	17	256	511		
8	30 MB	733	5	17	65535	733	Tandon TM703	
9	112 MB	900	15	17	65535	901		
10	20 MB	820	3	17	65535	820		
11	35 MB	855	5	17	65535	855		
12	50 MB	855	7	17	65535	855		
13	20 MB	306	8	17	128	319	Disctron526, MMI M125	
14	43 MB	733	7	17	65535	733		
16	20 MB	612	4	17	0	663	Microscience HH725 Syquest3250, 3425	
17	41 MB	977	5	17	300	977		
18	57 MB	977	7	17	65535	977		
19	60 MB	1024	7	17	512	1023		
20	30 MB	733	5	17	300	732		
21	43 MB	733	7	17	300	732		
22	30 MB	733	5	17	300	733	Seagate ST4038	
23	10 MB	306	4	17	0	336		
24	54 MB	925	7	17	0	925	Seagate ST4051	
25	69 MB	925	9	17	65535	925	Seagate ST4096	
26	44 MB	754	7	17	754	754	Maxtor2085	
27	69 MB	754	11	17	65535	754	Maxtor2140, Priam S14	
28	41 MB	699	7	17	256	699	Maxtor2190, Priam S19	
29	68 MB	823	10	17	65535	823	Maxtor1085 Micropolis1325	
30	53 MB	918	7	17	918	918	Maxtor1105, 1120, 4780	
31	94 MB	1024	11	17	65535	1024	Maxtor1170	
32	128 MB	1024	15	17	65535	1024	CDC9415	
33	43 MB	1024	5	17	1024	1024		
34	10 MB	612	2	17	128	612		
35	77 MB	1024	9	17	65535	1024		
36	68 MB	1024	8	17	512	1024		
37	41 MB	615	8	17	128	615		
38	25 MB	987	3	17	987	987		
39	57 MB	987	7	17	987	987	Maxtor1140, 4380	
40	41 MB	820	6	17	820	820	Seagate ST251	
41	41 MB	977	5	17	977	977	Seagate ST4053 Miniscribe3053/6053	
42	41 MB	981	5	17	981	981	Miniscribe3053/6053 RLL	
43	48 MB	830	7	17	512	830	Miniscribe 3650	

Appendix D: PROBLEM SHEET

44	69 MB	830	10	17	65535	830	Miniscribe 3650 RLL
45	114 MB	917	15	17	65535	918	Conner CP3104
46	152 MB	1224	15	17	65535	1223	Conner CP3204
User							

APPENDIX D: PROBLEM SHEET

 Customer Data Name Address Mainboard Data Model No. GA- Serial No. System Configue CPU Type: 	ration	Tel. No Fax. No Purcha Rev. No	o. se Date		
CPU Brand: CPU Speed: DRAM Type: 1	□ 2	4	□ 8	□ 16	□ 32 MB
DRAM Speed: ☐ 8 DRAM Total Size:	0 □ 70 MB	□ 60 ns	_ `		
DRAM Brand: SRAM Size: ☐ 64 SRAM Part No. TA Video Card:		□ 256 KB	☐ 512 KB DATA:		
Video Chip or Brand:					
Floppy Drive A Capac Floppy Drive B Capac Storage Controller Ty Hard Drive C Brand 8	city & Brand: pe □ MFM Type:	□ RLL	□ IDE	□ ESDI	□ SCSI
Hard Drive D Brand & LAN Controller Type LAN Card Brand & Mo					
Serial / Parallel Chip Mouse Brand & Mode O. S. DOS A. AUTOEXEC.BAT	Brand & Model: el: G	□ NETWAF	RE 🗆 UI	NIX / XENIX Ve	ər.:
5. Problem Descrip	otion:				