

## Addendum

### CPU Update Settings

This manual has 5 updated items listed below.

#### 1) Flash EPROM Type Selection: EP1, EP2

1M F-ROM	EP1	EP2	2M F-ROM	EP1	EP2	2M F-ROM	EP1	EP2
Intel 28F001			AMD AM29F002T			ATMEL AT29C020		
SST 29EE010			SST 29EE020			MXIC MX28F2000P		

These two jumpers allow you to set the flash EPROM chip. (table right side)

#### 2) CPU to Bus Frequency Ratio: FREQ1, 2, 3 (table below)

FREQ1	FREQ2	FREQ3	Ratio			
			P54C	P55C/ M2/K6	K5	M1
			---	4 x	---	---
			---	4.5 x	---	---

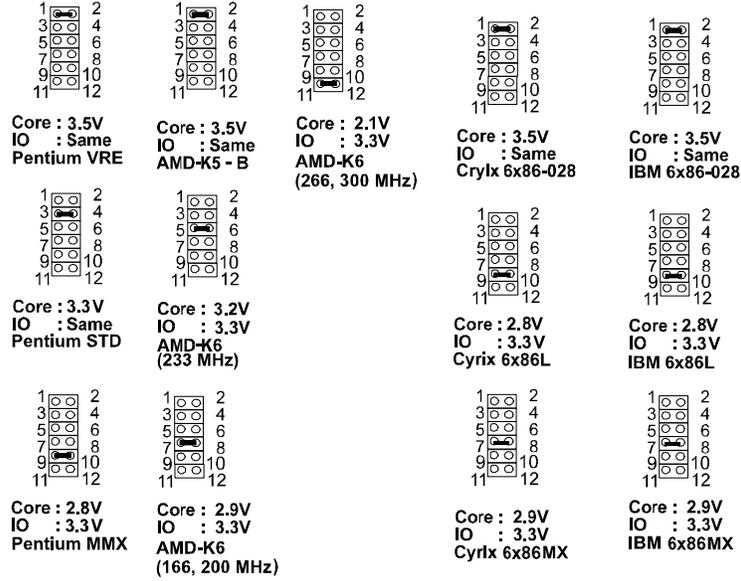
#### 3) Newly added AMD-K6 CPU supported by PA-2007 mainboard

Model	CPU Speed (MHz)	External (CPU/CLK) (MHz)	CLK1	CLK2	CLK3	Internal	CPU Clock Rate		
							FREQ1	FREQ2	FREQ3
K6-300*	300	66				4.5x			
K6-266*	266	66				4x			

Note : \* this type cpu has not been tested yet when this paper pressed.

#### 4) The jumper settings for voltages are listed below

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5) The jumper settings for Cyrix and IBM's 6x86MX-PR200/PR166 are listed below.

Model	CPU Speed (MHz)	External (CPU/CLK) (MHz)	CLK1	CLK2	CLK3	Internal	CPU Clock Rate		
							FREQ1	FREQ2	FREQ3
6x86MX-PR200	180	60				3 x			
	166	66				2.5 x			
	165	55				3 x			
6x86MX-PR166	150	60				2.5 x			
	138	55				2.5 x			
	133	60				2.5 x			

The items about "75MHz" in Note of page 27, 29 are removed.