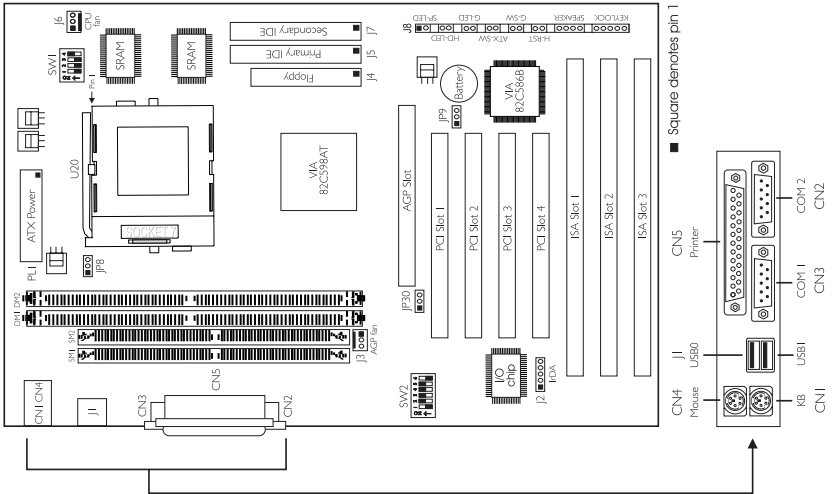


P5XV3+

rev. A+



Intel - SW2: I-6	
MMX166MHz - 66MHz - 2.5x	
MMX200MHz - 66MHz - 3x	
MMX233MHz - 66MHz - 3.5x	
AMD - SW2: I-6	
K6-200 - 66MHz - 3x	
K6-233 - 66MHz - 3.5x	
K6-266 - 66MHz - 4x	
K6-300 - 66MHz - 4.5x	
K6-2/250 - 100MHz - 2.5x	
K6-2/266 - 66MHz - 4x	
K6-2/300 - 100MHz - 3x	
K6-2/333 - 95MHz - 3.5x	
K6-2/350 - 100MHz - 3.5x	

K6-2/380 - 95MHz - 4x	
K6-2/400 - 100MHz - 4x	
IBM - SW2: I-6	
6x86MX-PR200 - 66MHz - 2.5x	
6x86MX-PR233 - 66MHz - 2.5x	
6x86MX-PR266 - 83MHz - 2.5x	
6x86MX-PR300 - 66MHz - 3.5x	
6x86MX-PR300 - 75MHz - 3x	
6x86MX-PR333 - 83MHz - 3x	
Cyrix - SW2: I-6	
6x86MX-PR200 - 66MHz - 2.5x	
6x86MX-PR233 - 75MHz - 2.5x	
6x86MX-PR266 - 83MHz - 2.5x	
M II-300 - 66MHz - 3.5x	

M II-300 - 75MHz - 3x	
M II-333 - 83MHz - 3x	
M II-350 - 100MHz - 3x	
M II-400 - 100MHz - 3.5x	
IDT - SW2: I-6	
C6-180 - 60MHz - 3x	
C6-200 - 66MHz - 3x	
C6-225 - 75MHz - 3x	
C6-240 - 60MHz - 4x	
WINCHIP2-200 - 66MHz - 3x	
WINCHIP2-225 - 75MHz - 3x	
WINCHIP2-240 - 60MHz - 4x	
WINCHIP2-266 - 83MHz - 3x	

WINCHIP2-300 - 100MHz - 3x		
Voltage	JP8	SW1
2.1V		
2.2V		
2.5V		
2.8V		
2.9V		
3.2V		
3.3V		
3.5V		

Jumper JP9 (CMOS Clear) - 1-2 On: Clear CMOS Data; 2-3 On: Normal (default)
 Before clearing the CMOS data, make sure to power-off your system and unplug the power cord.

Jumper JP30 (SDRAM Clock)
 1-2 On: SDRAM CLK = AGP CLK (default); 2-3 On: SDRAM CLK = CPU CLK