Chapter 1 Hardware Configuration

Your computer system is a high-performance computer system board that supports a Pentium TM CPU running at 75, 90, 100, 120, 133, 150, 166MHz and future Pentium Processor upgradable. You can install 256K or 512K of external cache memory on the motherboard using on board cache sockets or cache module. The motherboard offers floppy drive interface, IDE interface for HDD and CD-ROM Drive, two serial ports and an ECP/EPP capable parallel port. In addition to the hardware features, Windows 95TM ready Plug and Play and Advanced Power Management (APM) are supported.

Features:-

CPU

 PentiumTM Processor at bus speed of 50, 60, 66Mhz, that means Processor speed of 75, 90, 100, 120, 133, 150, 166Mhz and future Pentium Processor upgradable.

DRAM

 2 banks of 64 bit wide memory with each bank consists of two 72 pin SIMMs. (total 4 SIMM sockets.)

 Both SIMMs in same bank must be of the same memory size and type, however two banks may have different types and size and populated in any order.

 Memory configuration from 8M to 128M using combinations of 512K, 1M, 2M, 4M and 8M SIMM modules.

 70ns Fast Page Mode DRAM or 60ns Extended Data Out (EDO) DRAM Type supported.

Cache

- Support 256/512K bytes direct mapped write back L2 cache.
- 3.3V and mixed mode standard SRAM are supported.

Cache Module

 Onboard cache module slot accepts Pipelined burst, Asynchrous or Synchrous burst SRAM.

On-board I/O

- On board PCI fast IDE supports up to mode 4 HDD, with transfer rate up to 22M Bytes /s.
- Twin headers for 4 IDE devices including IDE Hard disk and CD-ROMs.
- · Support bus-mastering IDE, enhancing for multitasking application.
- One ECP/EPP parallel port.
- Two 16550 compatible UART serial ports.

- Support 2 FDD of 360K, 720K, 1.2M, 1.44M and 2.88M capacity.
- 4 PCI slots supporting bus masters.
- 3 ISA slots.

Power Management

- · Support System Memory Management (SMM) and APM.
- · Comply to Energy Star "Green" PC program.

Plug and Play

Support PnP for DOS and Windows 3.1 as well as Windows 95TM.

BIOS

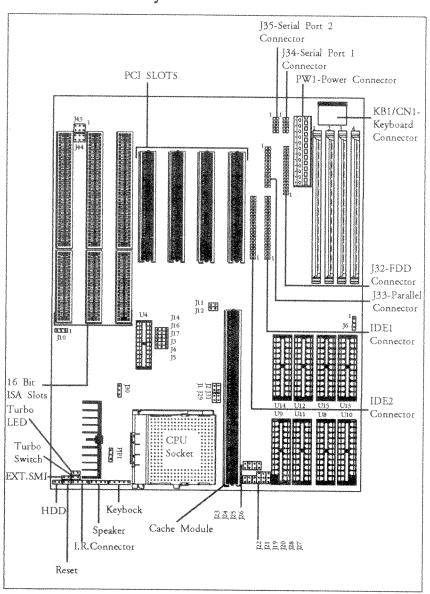
 Flash EPROM supported and the BIOS can be updated by Flash memory programming utility.

Static Electricity Precaution

Static electricity can easily damage the components on the motherboard. Obseving a few basic precautions can help you safeguard against damage that could result in expensive repairs. Follow the measures below to protect your equipment from static discharge:

- Keep the motherboard and other system components in their anti-static packaging until you are ready to install them.
- Touch a grounded surface before you remove any system component from its protective anti-static packaging. A grounded surface within easy reach is the expansion slot covers at the rear of the computer case, or any other unpainted portion of the computer chassis.
- During configuration and installation, touch a grounded surface frequently to discharge any static electric charge that may build up in your body. Another option is to wear a grounding wrist strap.
- When handling a motherboard or an adapter card, avoid touching its components. Handle the motherboard and adapter cards either by the edges or by the mounting bracket that attaches to the slot opening in the case.

Motherboard Layout



Hardware Setup

This chapter explains how to configure the motherboard's hardware. After you install the motherboard, you can set jumpers, install memory on the motherboard, and make case connections. Refer to this chapter whenever you upgrade or reconfigure your system.

Jumper Settings J1 : CPU Pipeline Enable / Disable

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And deposit to the Person	pipeline enabled	closed (default)	
and other designation of	pipeline disabled	open	

127, 128 : CPU Host Bus Frequency Select-

july, july, Or O 110st pus 11equency sciect.		· NAW	riost bus riequency select:	
	J28	J27	CPU Speed	,
	2-3	2-3	150Mhz/166Mhz	-
	2-3	1-2	Reserved	OCCUSION PROPERTY
	1-2	2-3	120Mhz/133Mhz	-
	1-2	1-2	75Mhz/90Mhz/100Mhz (default)	and the special party of the second

CPU Type Selection:

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	CPU Type	M1	INTEL
	J19	open	closed
	J20	2-3	1-2
-	J21	open	closed
	J22	open	closed
-	J23	open	closed
	J24	closed	open
	J25	closed	open
	J26	open	closed
	J30	open	closed
	J31	open	closed

III, II2, II4: CPU Clock Speed Option

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	CPU CLK	J11	J12	J14
-	50 MHz	closed	closed	1-2
-	60 MHz	closed(default)	open(default)	2-3(default)
-	66 MHz	open	closed	2-3
3		*		

PJP1: Intel CPU Voltage Select

		PJP1	
VR(3.3	8V)	2-3	
	ΓD(3.53/3.5V)	1-2	(default)

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DMA Channel Select:

DMA	JP43	JP44	
0	open	open	
1	open	open	
3	closed	closed (default)	

J2: L1 Cache Protocol

write back	open (default)
write through	closed

J3, J4, J5: L2 Cache Size

Cache Size	J3	J4	J5
256K	2-3(default)	2-3(default)	1-2(default)
 512K	1-2	1-2	2-3

J6: SRAM Type Select

3.3V cache 1-2 5V Mix mode cache 2-3	•			
5V Mix mode cache 2-3		3.3V cache	1-2	Manager Park

J8: CMOS Data Clear (Available if component RTC with this function pin)

(
Normal	open (default)	
Clear	closed	

J10: BIOS Option

Flash Type	J10	
5V Flash	1-2(default)	
12V Flash	2-3	

J16, J17: Onboard IDE IRQ Routing Option

Onboard IDE	J16	J17
IRQ 14, 15	1-2(default)	1-2 (default)
MIRQ 0, 1	2-3	2-3

I15: Ext. SMI Switch

,	1 A J . AME. CITIL CHYLLCIL	
-	Normal	open
- Landanian	SMI Mode	closed

J13: RESET Switch

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1 × 7 1	
Normal	onen
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1	
1 July Lat Later L	i t v
I RESEL	closed
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Hardware Configuration

J40: Turbo Switch

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Datumba		
Detuido	Closed	
	1	

Connectors

Once you have fastened the motherboard into the system case, the next step is to connect the internal cables. The motherboard connectors have varying numbers of pins and are the points of contact between the motherboard and other parts of the computer.

KB1 - Keyboard Connector

A standard five-pin female DIN keyboard connector is located at the rear of the

board. Plug the keyboard jack into this connector.

Pin	Description	Pin	Description
1	Keyboard Clock	4	Ground
2	Keyboard Data	5	+5V DC
3	NČ		
		I	1

CN1 - PS/2 Keyboard Connector (optional)

Pin	Description	Pin	Description	
1	Keyboard Data	4	+5V DC	
2	NĊ	5	Keyboard Clock	
3	Ground	6	NĆ	
			i i	

PW1 - Power Supply Connector

The motherboard requires a power supply with at least 200 Watts and a "power good" signal. PW1 has two six-pin male header connectors. Plug the dual connectors from the power directly onto the board connector while making sure the black

leads are in the centre.

Pin	Description	Pin	Description
12	+5V DC	6	Ground
11	+5V DC	5	Ground
10	+5V DC	4	-12V DC
9	-5V DC	3	+12V DC
8	Ground	2	+5V DC
7	Ground	1	Power Good

J9 - Speaker Connector

Attach the system speaker to connector IP7.

Attach the system	in speaker to connector	P/.		
Pin	Description	Pin	Description	1
1	Data out	3	Ground	
2	NC	4	+5V	-

J7 - Keylock & Power LED Connector

The state of the s				
Pin	Description	Pin	Description	
1	LED power	4	Keyboard Inhibiter	
2	NC .	5	Ground	
3	Ground		Ground	

J13 - Reset Switch Connector

Attach the Reset switch cable to this connector. The Reset switch restarts the system

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Pin	Description	Pin	Description	
1	Reset	2	Ground	

J18 - IDE LED

The indicator lights up during IDE data flow.

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Pin	Description	Pin	Description
1	- Cathode	2	+ Anode

J41 - Turbo LED

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Pin	Description	Pin	Description	Distance of the last
1	- Cathode	2	+ Anode	-

P34, 35 - Serial Ports Connectors

LIN	Description	Pin	Description
1	RLSD	6	DSR
2	RX	7	RTS
3	TX	8	CTS
4	DTR	9	RI
5	GND	10	N.C.

J33 - Parallel Port Connector

Pin	Description	Pin	Description
1	STROBE-	14	Ground
2	AUTO FEED-	15	Data Bit 6
3	Data Bit 0	16	Ground
4	ERROR-	17	Data Bit 7
5	Data Bit 1	18	Ground
6	INIT-	19	ACJ-
7	Data Bit 2	. 20	Ground
8	SLCT IN-	21	BUSY
9	Data Bit 3	22	Ground
10	Ground	23	PE(PaperEnd)
11	Data Bit 4	24	Ground
12	Ground	25	SLCT
13	Data Bit 5	26	N.C.

Cache Configuration

The motherboard supports standard Asynchronous SRAMs in DIP package and Pipelined Burst SRAM/Async SRAM in Cache Module. It also support 3.3V and mixed mode standard SRAM. You can configure the motherboard's cache by installing cache chips in the sockets noted below, and then set jumpers J3, J4, J5.

Cache Size	Cache RAM	Tag RAM	Cacheable Range
256KB	32K x 8,	8K x 8 or	64MB
and the second	(U8-U15)	32K x 8	
- Indonesia de la companya de la com		(U4)	
512KB	64K x 8,	16K x 8 or	64MB
· ·	(U8-U15)	32K x 8	
annual contraction of the contra		(U4)	

Cache RAM access time requirement:

Cache 10 th access time requirement.						
Cache Type / Speed	Data RAM Speed Tag	RAM Speed				
Async 50 MHz	20ns	20ns				
Async 60 MHz	17ns	20ns				
Async 66 MHz	15ns	15ns				
Burst 50 MHz	13.5ns	20ns				
Burst 60 MHz	10ns	15ns				
Burst 66 MHz	8.5ns	15ns				

Memory Configuration

Table 1 shows the possible memory combination. The motherboard will support both Fast Page DRAM or EDO DRAM SIMMs, but they cannot be mixed within the same memory bank. If Fast Page DRAM and EDO DRAM SIMMs are installed in separate banks, each bank will be optimized for maximum performance. Parity generation and detection is NOT supported.

SIMM 1 (Bank 0) SIMM Type (Size)	SIMM 2 (Bank 0) SIMM Type (Size)	SIMM 3 (Bank 1) SIMM Type (Size)	SIMM 4 (Bank 1) SIMM Type (Size)	Total System Memory
Empry	Empty	4 MB	4 MB	8 MB
Empty	Empty	8 MB	8 MB	16 MB
Empty	Empty	16 MB	16 MB	32 MB
Empty	Empty	32 MB	32 MB	64 MB
4 MB	4 MB	Empty	Empty	8 MB
4 MB	4 MB	4 MB	4 MB	16 MB
4 MB	4 MB	8 MB	8 MB	24 MB
4 MB	4 MB	16 MB	16 MB	40 MB
4 MB	4 MB	32 MB	32 MB	72 MB
8 MB	8 MB	Empty	Empty	16 MB
8 MB	8 MB	4 MB	4 MB	24 MB
8 MB	8 MB	8 MB	8 MB	32 MB
8 MB	8 MB	16 MB	16 MB	48 MB
8 MB	8 MB	32 MB	32 MB	80 MB
16 MB	16 MB	Empty	Empty	32-MB
16 MB	16 MB	4 MB	4 MB	40 MB
16 MB	16 MB	8 MB	8 MB	48 MB
16 MB	16 MB	16 MB	16 MB	64 MB
16 MB	16 MB	32 MB	32 MB	96 MB
32 MB	32 MB	Empty	Empty	64 MB
32 MB	32 MB	4 MB	4 MB	72 MB
32 MB	32 MB	8 MB	8 MB	80 MB
32 MB	32 MB	16 MB	16 MB	96 MB
32 MB	32 MB	32 MB	32 MB	128 MB

Hardware Configuration