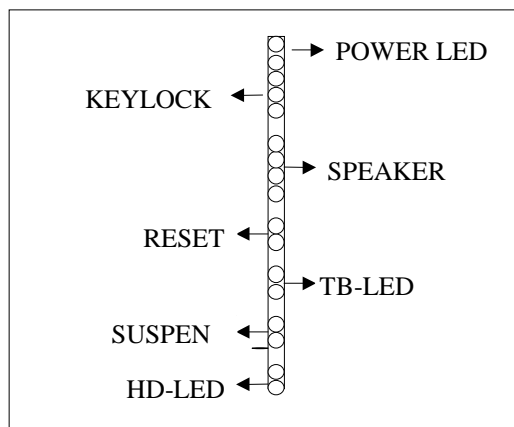
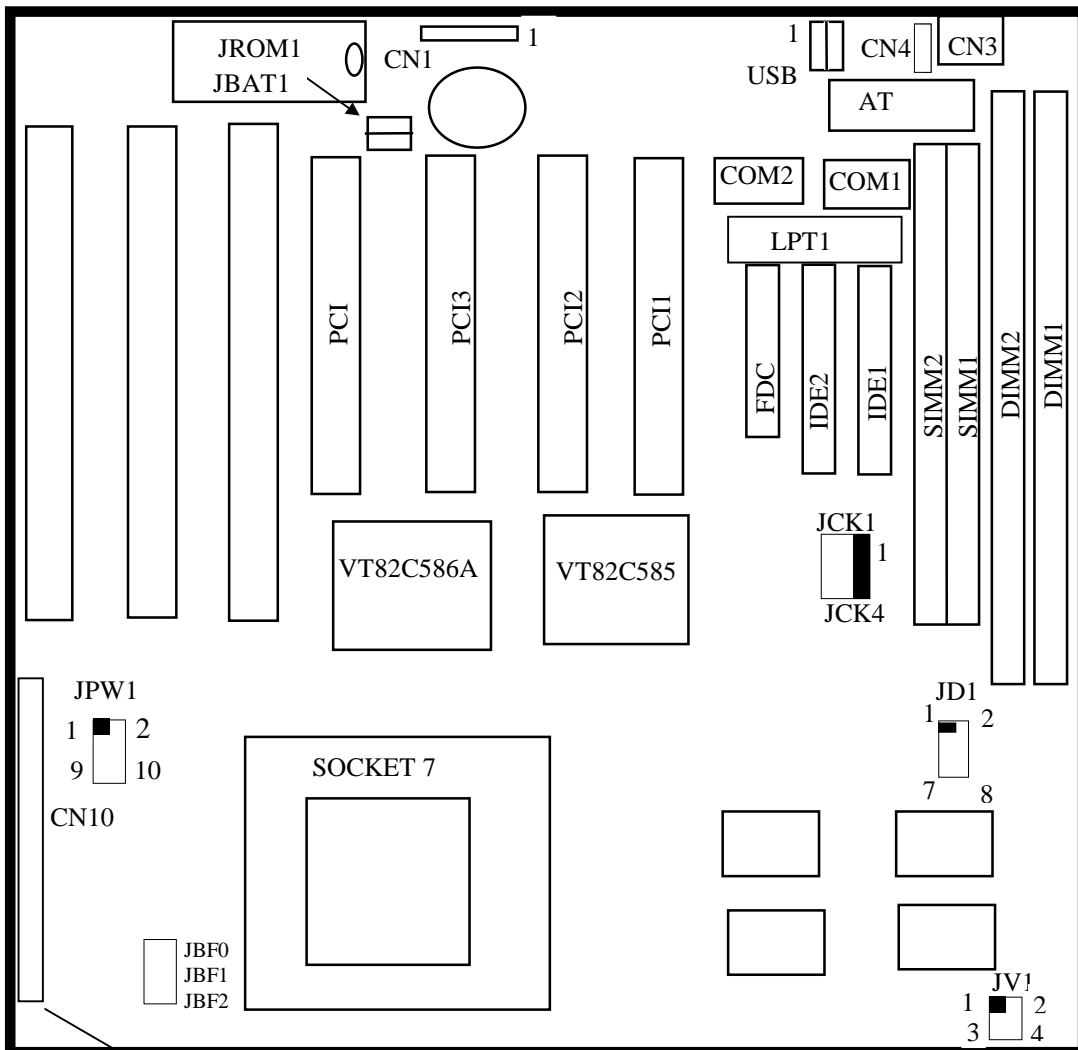


**MAIN BOARD
USER'S GUIDE
(VER : 5V-1A)**

I. MOTHERBOARD LAYOUT



II JUMPER SETTING(5V-1A)

INTEL® PENTIUM® PROCESSOR INSTALLATION

CPU CLOCK	SYS. CLOCK	JCK1	JCK2	JCK3	JCK4	JBF0	JBF1	JBF2	JPW1	JV1	JV2	RATIO
P54C 75 MHZ 3.3V	50 MHZ	2-3	2-3	2-3	1-2	OPEN	OPEN	OPEN	1-2,5-6,7-8	OPEN	1-2	1.5X
P54C 90 MHZ 3.3V	60 MHZ	1-2	2-3	2-3	2-3	OPEN	OPEN	OPEN	1-2,5-6,7-8	OPEN	1-2	1.5X
P54C 100 MHZ 3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	OPEN	OPEN	1-2,5-6,7-8	OPEN	1-2	1.5X
P54C 120 MHZ 3.3V	60 MHZ	1-2	2-3	2-3	2-3	CLOSE	OPEN	OPEN	1-2,5-6,7-8	OPEN	1-2	2X
P54C 133 MHZ 3.3V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	OPEN	OPEN	1-2,5-6,7-8	OPEN	1-2	2X
P54C 150 MHZ 3.3V	60 MHZ	1-2	2-3	2-3	2-3	CLOSE	CLOSE	OPEN	1-2,5-6,7-8	OPEN	1-2	2.5X
P54C 166 MHZ 3.3V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	CLOSE	OPEN	1-2,5-6,7-8	OPEN	1-2	2.5X
P55C 166 MHZ 2.8V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	CLOSE	OPEN	7-8	1-2,3-4	1-2	2.5X
P54C 180 MHZ 3.3V	60 MHZ	1-2	2-3	2-3	2-3	OPEN	CLOSE	OPEN	1-2,5-6,7-8	OPEN	1-2	3X
P54C 200 MHZ 3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	CLOSE	OPEN	1-2,5-6,7-8	OPEN	1-2	3X
P55C 200 MHZ 2.8V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	CLOSE	OPEN	7-8	1-2,3-4	1-2	3X
P55C 233 MHZ 2.8V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	OPEN	OPEN	7-8	1-2,3-4	1-2	3.5X

AMD® K5/K6 PROCESSOR INSTALLATION

CPU CLOCK	SYS. CLOCK	JCK1	JCK2	JCK3	JCK4	JBF0	JBF1	JBF2	JPW1	JV1	JV2	RATIO
K5-PR 75 3.52V	50 MHZ	2-3	2-3	2-3	1-2	OPEN	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	1.5X
K5-PR 90/PR120 3.52V	60 MHZ	1-2	2-3	2-3	2-3	OPEN	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	1.5X
K5-PR100/PR133 3.52V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	1.5X
K5-PR166 3.52V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	CLOSE	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	2.5X
K6-PR166 2.9V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	CLOSE	OPEN	1-2,7-8	1-2,3-4	1-2	2.5X
K6-PR200 2.9V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	CLOSE	OPEN	1-2,7-8	1-2,3-4	1-2	3X
K6-PR233 3.2V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	OPEN	OPEN	5-6,7-8	1-2,3-4	1-2	3.5X
K6-PR233 3.3V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	OPEN	OPEN	1-2,5-6,7-8	1-2,3-4	1-2	3.5X
K6-PR266 2.2V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	OPEN	CLOSE	3-4	1-2,3-4	1-2	4X
K6-PR300 2.2V/3.45V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	CLOSE	CLOSE	3-4	1-2,3-4	2-3	4.5X

CYRIX® 6X86 PROCESSOR INSTALLATION

CPU CLOCK	SYS. CLOCK	JCK1	JCK2	JCK3	JCK4	JBF0	JBF1	JBF2	JPW1	JV1	JV2	RATIO
P120+ M1 3.52V	50 MHZ	2-3	2-3	2-3	1-2	CLOSE	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	2X
P133+ M1 3.52V	55 MHZ	2-3	2-3	1-2	2-3	CLOSE	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	2X
P150+ M1 3.52V	60 MHZ	1-2	2-3	2-3	2-3	CLOSE	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	2X
P166+ M1 3.52V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	OPEN	OPEN	1-2,3-4,5-6,7-8	OPEN	1-2	2X
P133+ 2.8V/3.3V 6X68L	55 MHZ	2-3	2-3	1-2	2-3	CLOSE	OPEN	OPEN	7-8	1-2,3-4	1-2	2X
P150+ 2.8V/3.3V 6X68L	60 MHZ	1-2	2-3	2-3	2-3	CLOSE	OPEN	OPEN	7-8	1-2,3-4	1-2	2X
P166+ 2.8V/3.3V 6X68L	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	OPEN	OPEN	7-8	1-2,3-4	1-2	2X
P200+ 2.8V/3.3V 6X68L	75 MHZ	1-2	2-3	1-2	2-3	CLOSE	OPEN	OPEN	7-8	1-2,3-4	1-2	2X
MX PR 166 2.9V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	CLOSE	OPEN	OPEN	1-2,7-8	1-2,3-4	1-2	2X
MX PR 200 2.9V/3.3V	75 MHZ	1-2	2-3	1-2	2-3	CLOSE	OPEN	OPEN	1-2,7-8	1-2,3-4	1-2	2X
MX PR 233 2.9V/3.3V	75 MHZ	1-2	2-3	1-2	2-3	CLOSE	CLOSE	OPEN	1-2,7-8	1-2,3-4	1-2	2.5X
MII PR 300 2.9V/3.3V	66 MHZ	2-3	1-2	2-3	2-3	OPEN	OPEN	OPEN	1-2,7-8	1-2,3-4	1-2	3.5X

JCK1-4 - CPU SPEED SELECTOR

JUMPER	50MHZ	55MHZ	60MHZ	66MHZ	75MHZ
JCK1	2-3	2-3	1-2	2-3	1-2
JCK2	2-3	2-3	2-3	1-2	2-3
JCK3	2-3	1-2	2-3	2-3	1-2
JCK4	1-2/2-3	1-2/2-3	1-2/2-3	1-2/2-3	1-2/2-3
PCICLK	25/32	7.5/32	30/32	33.3/32	37.5/32

NOTE: JCK4 IS SYNCHRONOUS/ASYNCHRONOUS SELECTION.

JPW1 , JV1, JV2- CPU VOLTAGE SELECTOR (FOR ONE REGULATOR)

DESCRIPTION	CPU VOLTAGE	JPW1	JV1	JV2
1. P54C-STD,VR 2. AMD® K5-C,F 3. CYRIX® 6X86-016	CORE: 3.3V IO: 3.3	1-2,5-6,7-8	OPEN	1-2
1. P54C-VRE 2. AMD® K5-B 3. CYRIX®6X86-028	CORE: 3.4 ~ 3.6V IO: 3.4 ~ 3.6V	1-2,3-4,5-6,7-8	OPEN	1-2

(FOR TWO REGULATOR)

DESCRIPTION	CPU VOLTAGE	JPW1	JV1	JV2
1.INTEL® P55C 2.AMD® K5-H,J 3.CYRIX® 6X86L	CORE: 2.8V IO: 3.3V	7-8	1-2,3-4	1-2
1. AMD® K6 2. CYRIX® 6X86MX	CORE: 2.9V IO: 3.3V	1-2,7-8	1-2,3-4	1-2
AMD® K6 233	CORE: 3.2V IO: 3.3V	5-6,7-8	1-2,3-4	1-2
AMD® K6 300	CORE: 2.2V IO: 3.45V	1-2, 7-8	1-2, 3-4	2-3

NOTE : INTEL® P55C MMX, AMD® K6 AND CYRIX® 6X86L ARE “DUAL VOLTAGE CPUS” , “6X86L” IS CYRIX® NEW LOW POWER CPU.

JBF0-JBF2 - CPU/BUS RATIO

RATIO	JBF0	JBF1	JBF2
1.5X	OPEN	OPEN	OPEN
2X	CLOSE	OPEN	OPEN
2.5X	CLOSE	CLOSE	OPEN
3X	OPEN	CLOSE	OPEN
3.5X	OPEN	OPEN	OPEN
4.0X	CLOSE	OPEN	CLOSE
4.5X	CLOSE	CLOSE	CLOSE

JBAT1 - CMOS SELECTOR

DESCRIPTION	JBAT1
NORMAL (DEFAULT)	1-2
CLEAR CMOS (CLEAR PASSWORD)	2-3

JROM1 - FLASH ROM VOLTAGE SELECTOR

DESCRIPTION	JROM1
5V (SST,WINBOND)	1-2
12V (INTEL,MXIC)	2-3

JD1 - DIMM VOLTAGE SELECTOR

DESCRIPTION	JD1
5V DIMM	1-3,2-4
3.3V DIMM	5-7,6-8

CONNECTORS**CN3 - KEYBOARD CONNECTOR**

PIN	DESCRIPTION
1	KEYBOARD CLOCK
2	KEYBOARD DATA
3	N.C.
4	GROUND
5	+5 VCC

CN4 - EXTERNAL PS/2 MOUSE CONNECTOR**CN5,CN6 - USB CONNECTOR (OPTIONAL)****CN7 - POWER SUPPLY CONNECTOR**

PIN	DESCRIPTION	PIN	DESCRIPTION
1	POWER GOOD	7	GROUND
2	+5V DC	8	GROUND
3	+12V DC	9	-5V DC
4	-12V DC	10	+5V DC
5	GROUND	11	+5V DC
6	GROUND	12	+5V DC

IDE1 - PRIMARY IDE CONNECTOR
IDE2 - SECONDARY IDE CONNECTOR
CN9 - FLOPPY DISK CONNECTOR
COM1,2 - SERIAL PORTS CONNECTOR
CN8 - PRINTER PORT CONNECTOR
CN1 - INFRARED CONNECTOR : IR

PIN	SIGNAL NAME
1	IRRX
2	GROUND
3	IRTX
4	VCC
5	IRRXH
6	VCC
7	GND