



BA810 Desktop Board Specification Update

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The BA810 Desktop Board may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The BA810 Desktop Board may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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REVISION HISTORY

Date of Revision	Version	Description
April 2000	-001	This document is the first Specification Update for the Intel® BA810 Desktop Board.
June 2000	-002	Added erratum 1.

PREFACE

This document is an update to the specifications contained in the BA810 Desktop Board Technical Product Specification (Order number A15858). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Celeron™ Processor Specification Update* (Order number 243748) for specification updates concerning the Intel Celeron processor. Items contained in the *Intel Celeron Processor Specification Update* that either do not apply to the BA810 Desktop Board or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the PBA revision(s) associated with that stepping.

Refer to the *Intel® Pentium® III Processor Specification Update* (Order number 244453) for specification updates concerning the Intel Pentium III processor. Items contained in the *Intel Pentium III Processor Specification Update* that either do not apply to the BA810 motherboard or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the PBA revision(s) associated with that stepping.

Refer to the *Intel® 82810 Chipset: 82810/82810-DC100 Graphics and Memory Controller Hub (GMCH) Specification Update* (Order Number 290659) for specification updates concerning the 82810 GMCH Controller. Items contained in the *82810 GMCH Specification Update* that either do not apply to the BA810 Desktop Board or have been worked around are noted in this document. Otherwise, it should be assumed that any GMCH errata for a given stepping are applicable to the PBA revision(s) associated with that stepping.

Refer to the *Intel® 82801 I/O Controller Hub (ICH) Specification Update* (Order Number 290677) for specification updates concerning the 82801 I/O Controller Hub. Items contained in the *Intel 82801 ICH Specification Update* that either do not apply to the BA810 Desktop Board or have been worked around are noted in this document. Otherwise, it should be assumed that any ICH errata for a given stepping are applicable to the Printed Board Assembly (PBA) revision(s) associated with that stepping.

Refer to the *Intel® 82802 Firmware Hub (FWH) Specification Update* (Order Number TBD) for specification updates concerning the 82802 Firmware Hub. Items contained in the *Intel 82802 FWH Specification Update* that either do not apply to the SU810 desktop board or have been worked around are noted in this document. Otherwise, it should be assumed that any ICH errata for a given stepping are applicable to the Printed Board Assembly (PBA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the BA810 Desktop Board's behavior to deviate from published specifications. Hardware and software designed to be used with any given Printed Board Assembly (PBA) and BIOS revision level must assume that all errata documented for that PBA and BIOS revision level are present on all Desktop Boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for
BA810 Desktop Boards**

GENERAL INFORMATION

Basic BA810 Desktop Board Identification Information

AA Revision	PBA Revision	BIOS Revision	Notes
A05559-303	A05562-303	BA81010A.86A.0004.P01	1-6

NOTES:

1. The PBA number or AA number is found on a small label on the component side of the board.
2. The 440ZX AGPset kit used on this PBA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82810 DC100 GMCH	A3	SLP6
82801AA ICH	B1	SL3MA
82802AB FWH	A1	SB48

3. The following errata are contained in the *Intel® Celeron™ Processor Specification Update* (Order Number 243748) for the Celeron processor and either do not apply to the BA810 Desktop Board or have been worked-around in this PBA and/or BIOS revision: All other errata associated with the processor apply to this PBA revision.
4. The following errata are contained in the *Intel® Pentium III Processor Specification Update* (Order Number 243748) for the Pentium III processor and either do not apply to the SU810 motherboard or have been worked-around in this PBA and/or BIOS revision: All other errata associated with the processor apply to this PBA revision.
5. The following items are contained in the *82810/82810-DC100 Graphics and Memory Controller Hub (GMCH) Specification Update* (Order Number TBD) and either do not apply to the BA810 Desktop Board or have been worked around in this PBA and/or BIOS revision: All other errata associated with the GMCH apply to this PBA revision.
6. The following items are contained in the *Intel® 82801 I/O Controller Hub Specification Update* (Order Number TBD) and either do not apply to the BA810 Desktop Board or have been worked around in this PBA and/or BIOS revision: None. All other errata associated with the ICH apply to this PBA revision.

Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes which apply to the BA810 Desktop Board. Intel intends to fix some of the errata in a future revision of the Desktop Board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
Fix:	This erratum is intended to be fixed in a future revision of the Desktop Board or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
1	Fix	System BIOS will not report an error during power on self test when the diskette drive is disconnected.

The errata described in this specification update apply to combinations of PBA revision and BIOS revision as shown in the table below. Descriptions of the individual errata referred to by number in the table below are found in the ERRATA section of this document.

PBA Revision	BIOS Revision	Errata That Apply
A05562-303	BA81010A.86A.0004.P01	1

[†] Note: This combination of BIOS revision and PBA revision has not undergone regression testing. Use of a PBA with down-revision BIOS is an untested combination and is undertaken at the user's risk.



SPECIFICATION CHANGES

ERRATA**1. *System BIOS will Not Report an Error During Power On Self Test When The Diskette Drive Is Disconnected***

PROBLEM: If the diskette drive is disconnected from the Desktop Board, the system BIOS will not generate an error during POST and the system will continue to boot to the operating system.

IMPLICATION: The user will not be prompted to press F1 to continue the boot process during POST if the diskette drive is disconnected.

WORKAROUND: None.

STATUS: This erratum will be fixed in a future BIOS revision.



DOCUMENTATION CHANGES