1. Jumpers Location Diagram for "B51 V1.X " main board

RJ1 | CN14 | CN15 | CN9 | CN3 | CN13 | CN12 | JP14

CN10 — 33 34 — 1 2

CN17 — 1 8

CN2 — 39 40 — 1 2

JP5

JP11 | JP20 | JP9 | JP10

EISA SLOT

CN16

CN1 — 44 43 ... 2 1

SDIM   CN18

U21
NS
PC9317

JP19

JP21

CN11 — 25 26 — 1 2

EISA SLOT

U1
CPU

CYRIX
CX5530

DOC
U25

JP8

JP7

JP22

JP1

J1 | JP12

JP6

JP13

JP3

CN6 | CN8 | CN7 | CN4 | CN5
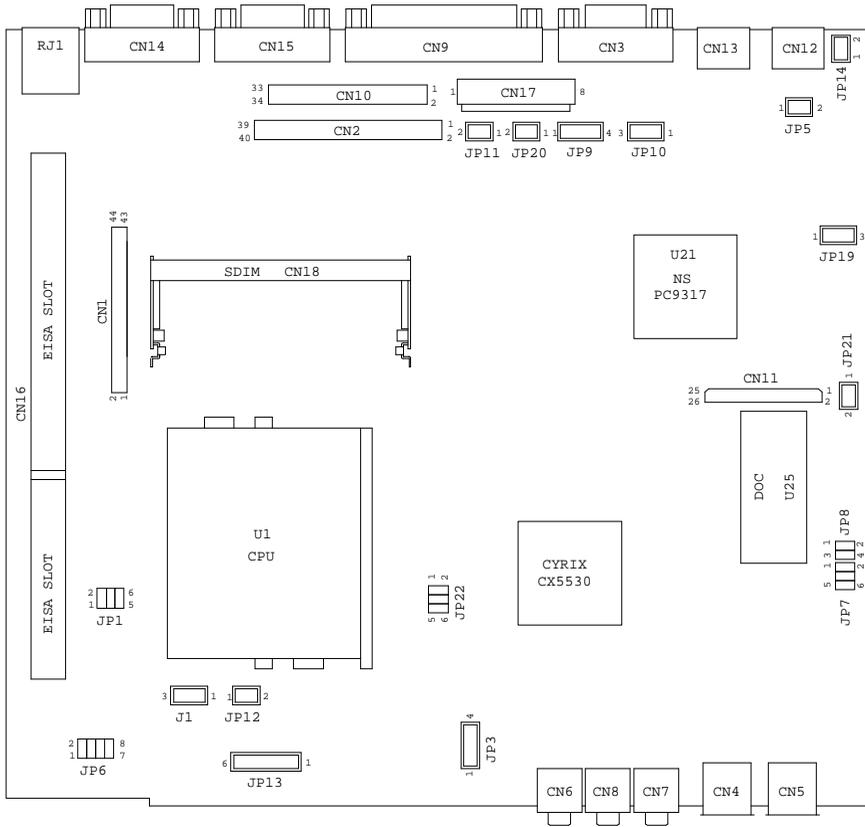
2.Memory configuration for "B51 V1.X" main board

The basic system memory configuration of this Net PC Series is
32MB SDRAM at jumper location U31, U32, U33 and U34.
It supports 4MB VGA share memory (UMA).   It is optional
for memory expansion by a 144-pin S.O. DIMM socket (CN18).
To use same type of SDRAM for expansion is important.

3. Jumper Setting

● **CPU type selection : JP1**

CPU	1-2	3-4	5-6
Gxm 200MHz	Open	Close	Open
Gxm 233MHz	Close	Open	Open
Gxm 266MHz	Open	Open	Open
Gxm 300MHz	Close	Open	Close

● **CMOS Data Clean : J1**

CMOS data	J1
Clean	2-3
Normal (default)	1-2

IMPORTANT !

To reset CMOS data,   please move power connector away from PC system after power off if ATX type power supply is used.   Otherwise the 5VSB (stand-by voltage) still exists and RTC will not be cleaned without removing power connector.

● **Reset Switch : JP21**

Mode	JP21
Reset	Close
Normal mode (default)	Open