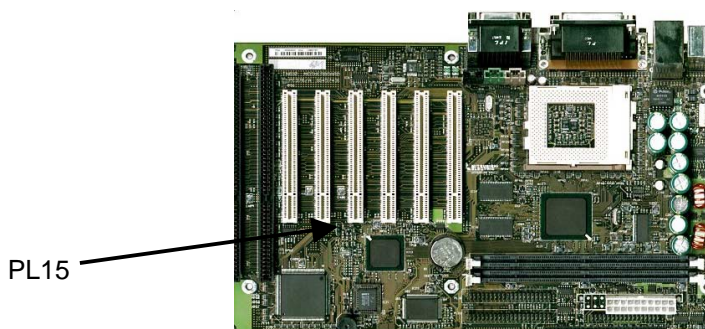


CPU

The Trimond TN810 ATX motherboard uses Intel's 810 chipset and supports 66MHz and 100MHz Front Side Bus 370-pin PGA Celeron® processors. Processor core multipliers are auto-detected for current releases of CPU. Older revisions may be configured using the "Frequency Strap" setting found in the "Advanced Chipset Control" element of the "Advanced BIOS" menu.

The processor core multiplier may be forced to its lowest value (X2) for diagnostic purposes by fitting a jumper to **PL15** across pins 1-2.



RAM

Two DIMM sockets accept 64-bit wide (72-bit with parity/ECC support) unbuffered **PC100 SDRAM** with **SPD** (serial presence detect). Please note that 66MHz SDRAM and EDO memory are **NOT** supported.

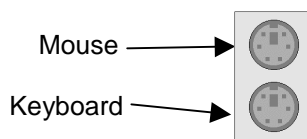
Populate DIMM sockets in the following order: **MM1**, MM2.

Supported sizes: **16MB**, **32MB**, **64MB**, **128MB**, **256MB**.

Approved vendors: Please see the current list at <http://www.trimond.com/shared/reference.asp>.

KEYBOARD & MOUSE

Orientation as shown.

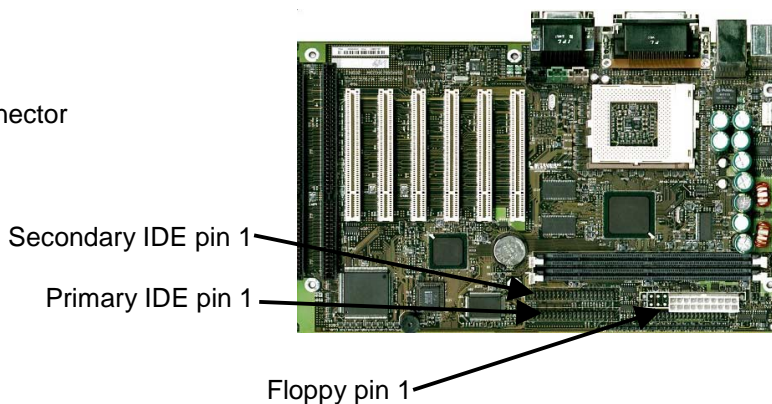


IDE & FLOPPY CONNECTORS & CABLES

IDE cables must not exceed 12".

Rev B boards will be fitted with keyed connector shrouding to ensure correct orientation.

Orientations for **Rev A** boards shown.



INTEL 810 CHIPSET

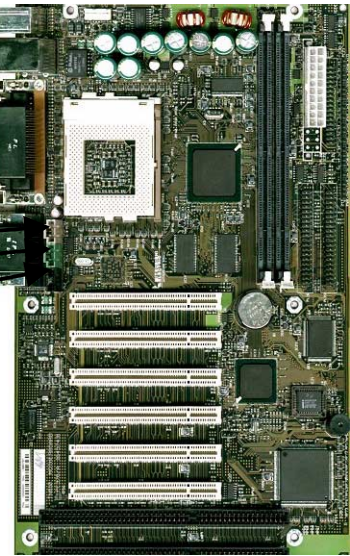
Windows 98 and, in future, Windows 2000 are the preferred operating systems for Intel's 810 chipset. If required, an INF file update for Windows 95 to correctly enumerate the 810 chipset is available from the evaluation support CD or from our web site (see below).

AUDIO CONNECTORS

Audio connectors as shown.

Line out
Line in
Microphone

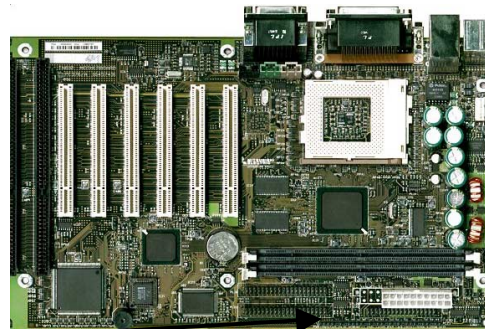
PL5: ATAPI/AUX
PL6: CD
PL7: Telephony



PSU

Trimond motherboards are designed to be connected to a **soft-switch PSU**, with 5V standby. If the 5V standby current is insufficient, the PSU may deregulate and possibly damage the motherboard. Ensure that at least **30mA** is available to the 5V standby output.

If you wish to use a **hard-switch PSU**, it is necessary to change a flag in the manufacturer settings of the BIOS and provide current to the motherboard 5V standby connector. Please contact the Motherboard Division for full instructions.



Connect the power on/off lead to "PWR ON" on **PL23**.

COOLING

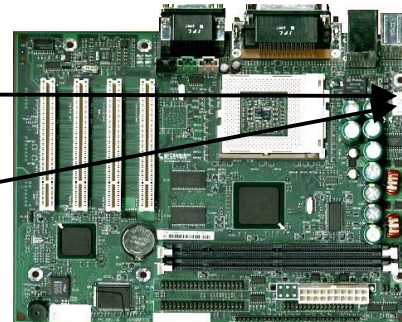
Two tacho fan connectors, **PL8** and **PL9**, are provided.

CPU Fan
Pin 3 (tacho)

Front Fan
Pin 3 (tacho)

PL8

PL9



PHOENIX BIOS & CMOS

BIOS updates are available from our web site. Please see *Documentation & Drivers* below for further details.

The port 80 codes for Phoenix BIOS 4.0 Rel 6.1 can be obtained from our web site:

<http://www.trimond.com/shared/reference.asp>

It should not be necessary to clear CMOS. A combination of "Reset Configuration Data" and <F9> (reset default values), will reset all information. It is possible to clear date and time settings only by removing the battery.

DOCUMENTATION & DRIVERS

Supporting documentation, drivers and BIOS updates are available from our web site as follows:

<http://www.trimond.com>

- Click on the "Support" left menu item.
- Click on "Private Pages".
- Login using the following user-id:

Name: **evaluation** (not case-sensitive)

Password: **motherboard** (not case-sensitive)

An online **problem report** form is available from the evaluation support page.