

Premiere/PCI II

Memory Map, I/O Map, Interrupts & DMA Channels

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Memory Map

Address Range (Decimal)	Address Range (hex)	Size	Description
1024K-131072K	100000-8000000	130048K	Extended Memory
960K-1023K	F0000-FFFFF	64K	AMI System BIOS
952K-959K	EE000-EFFFF	8K	FLASH Boot Block (Available as HIMEM)
948K-951K	ED000-EDFFF	4K	ECSD (Plug and Play Configuration area)
944K-947K	EC000-ECFFF	4K	OEM Logo Area (Available as UMB)
896K-943K	E0000-EBFFF	48K	BIOS Reserved (Available as UMB)
640K-895K	A0000-DFFFF	256K	Available High DOS Memory (open to the ISA & PCI bus)
639K	9FC00-9FFFF	1K	Extended BIOS Data (moveable by QEMM, 386MAX)
512K-638K	80000-9FBFF	127K	Extended conventional
0K-511K	00000-7FFFF	512K	Conventional

Table D-1. Premiere/PCI II memory map

I/O Map

Address (hex)	Size	Description
0000 - 000F	16	SIO - DMA 1
0020 - 0021	2	SIO - Interrupt Controller 1
0040 - 0043	4	SIO - Timer 1
0048 - 004B	4	SIO - Timer 2
0060	1	Keyboard Controller Data Byte
0061	1	SIO - NMI, speaker control
0064	1	Kbd Controller, CMD/STAT Byte
0070, bit 7	1 bit	SIO - Enable NMI
0070, bits 6:0	7 bits	SIO - Real Time Clock, Address
0071	1	SIO - Real Time Clock, Data
0073	1	Reserved - Brd. Config.
0075	1	Reserved - Brd. Config.,RD only
0078	1	SIO - BIOS Timer
0080 - 008F	16	SIO - DMA Page Register
00A0 - 00A1	2	SIO - Interrupt Controller 2
00C0 - 00DE	31	SIO - DMA 2
00F0	1	Reset Numeric Error
0170 - 0177	8	Secondary IDE Channel

01F0 - 01F7	8	Primary IDE Channel
0278 - 027B	4	Parallel Port 2
02F8 - 02FF	8	On-Board Serial Port 2
0376	1	Secondary IDE Chan Cmd Port
0377	1	Secondary IDE Chan Stat Port
0378 - 037F	8	Parallel Port 1
03BC - 03BF	4	Parallel Port x
03E8 - 03EF	8	Serial Port 3
03F0 - 03F5	6	Floppy Channel 1
03F6	1	Primary IDE Channel Cmnd Port
03F7 (Write)	1	Floppy Channel 1 Command
03F7, bit 7	1 bit	Floppy Disk Change Channel 1
03F7, bits 6:0	7	Primary IDE Channel Status Port
03F8 - 03FF	8	On-Board Serial Port 1
LPT + 400h	8	ECP port, LPT + 400h
0CF8	1	PCI Configuration Space Enable
0CF9	1	Deturbo Mode Enable
C000 - C0FF	256	82434NX Config. Registers *
C200 - C2FF	256	82378IB Config. Registers *

Table E-1. Premiere/PCI II I/O address map

* Only accessible after PCI configuration space is enabled.

Interrupts & DMA Channels

<i>IRQ</i>	<i>System Resource</i>
NMI	Parity Error
0	Reserved, Interval Timer
1	Reserved, Keyboard buffer full
2	Reserved, Cascade interrupt from slave PIC
3	Serial Port 2
4	Serial Port 1
5	Parallel Port 2
6	Floppy
7	Parallel Port 1
8	Real Time Clock
9	User available
10	User available
11	User available
12	On-board Mouse Port
13	Reserved, Math coprocessor
14	Primary IDE if enabled, else available to user
15	Secondary IDE if enabled, else available to user

Table F-1. Premiere/PCI II interrupts

<i>DMA</i>	<i>Data Width</i>	<i>System Resource</i>
0	8- or 16-bits	Open
1	8- or 16-bits	Open - Normally used for LAN
2	8- or 16-bits	Floppy
3	8- or 16-bits	Parallel Port
4		Reserved - Cascade channel
5	16-bits	Open
6	16-bits	Open
7	16-bits	ISA IDE

Table F-2. Premiere/PCI II DMA map