PINE TECHNOLOGY USA
PT-602A

Card Type	Multi-I/O
Chipset/Controller	Unidentified
I/O Options	Game port, parallel port, serial ports (2)
Maximum DRAM	N/A
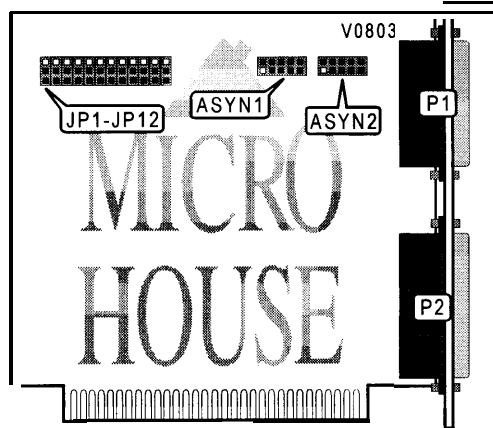
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Game port	P I	Serial port 1	ASYN1
Parallel port	P2	Serial port 2	ASYN2

SERIAL PORT 1 CONFIGURATION		
COM	**JP1**	**JP2**
⇨ COMI	Pins 1 & 2 closed	Pins 1 & 2 closed
COM3	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	N/A	Pins 2 & 3 closed

SERIAL PORT 2 CONFIGURATION		
COM	**JP3**	JP4
⇨ COM2	Pins 1 & 2 closed	Pins 1 & 2 closed
COM4	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	N/A	Pins 2 & 3 closed

SERIAL PORT 1 INTERRUPT/ADDRESS SELECT	
IRQ/Address	**JP8**
⇨ IRQ4/3F8h	Pins 1 & 2 closed
IRQ3/3E8h	Pins 2 & 3 closed

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SERIAL PORT 2 INTERRUPT/ADDRESS SELECT	
IRQ/Address	**JP9**
⇨ IRQ3/2F8h	Pins 1 & 2 closed
IRQ4/2E8h	Pins 2 & 3 closed

PARALLEL PORT CONFIGURATION		
LPT		
⇨ LPTI	Pins 1 & 2 closed	Pins 1 & 2 closed
LPT2	Pins 2 & 3 closed	Pins 1 & 2 closed
Disabled	N/A	Pins 2 & 3 closed

PARALLEL PORT INTERRUPT/ADDRESS SELECT	
IRQ/Address	**JP10**
⇨ IRQ7/378h	Pins 1 & 2 closed
IRO5/278h	Pins 2 & 3 closed

GAME PORT	
Setting	**JP7**
⇨ Enabled	Pins 1 & 2 closed
Disabled	Pins 2 & 3 closed

FACTORY CONFIGURED SETTINGS	
Jumper	**Setting**
JP11	Pins 1 & 2 closed
JP12	Pins 1 & 2 closed