# **Handling Precautions**

# 

High potential static charge can cause damage to the integrated circuits on the board. Before handling any mainboard outside of its protective packaging, ensure that there is no static electric charge on your body.

Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer.

- Keep the board in its anti-static bag until you are ready to install it.
- Protect your board form static electricity by well grounding of your body and the equipment during the installation, such as wearing a grounded wrist strap.
- Always handle the board by its edges.
- Do not touch with the components on the boards, add-on cards and modules, or the "gold finger" connectors going to be plugged into the expansion slot. It is best to handle system components by their mounting bracket.
- Ensure the system power is completely turn-off before doing any installation work.

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Page ii

# **Table of Contents**

FEATURES OF THE BOARD	
Overview	
Auto Jumper Yes	
FEATURES SUMMARY	1
MOTHERBOARD INSTALLATION	
INSTALLATION PRECAUTIONS	
A QUICK INTRODUCTION	
CPU INSTALLATION	4
INSTALLING SYSTEM MEMORY (RAM)	5
EXPANSION CARDS INSTALLATION	6
CONNECTING EXTERNAL CONNECTOR	6
AT Power Connector	6
ATX Power Connector	7
Floppy Drive Connector	7
IDE Connectors	7
Front Panel Function Connectors.	
REPLACING BATTERY	9
QUICK INSTALLATION GUIDE	
QUICK REFERENCE OF YOUR MOTHERBOARD	
AMIBIOS SETUP	14
STANDARD CMOS SETUP	
CPU SETUP	
ADVANCED CMOS SETUP	
ADVANCED CHIPSET SETUP	
PCI/PNP SETUP	
POWER MANAGEMENT SETUP	
PERIPHERAL SETUP	

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# **Features of the Board**

## Overview

Congratulations on purchasing the highest performance motherboard. With the advanced technology available today, this motherboard is designed to run Pentium<sup>®</sup>, AMD<sup>®</sup> and Cyrix<sup>®</sup> processors. The onboard system memory consists of DIMM. In addition, two unbuffered, 3.3 Volts, 168-pin SDRAM sockets are also included for 16 to 256MB synchronous Dynamic memory support. A 321-pins, Zero Insertion Force (ZIF) socket allows an easy upgrade path for the future processors.

The motherboard uses VIA VT82C585VPX PCI chipset. The VIA VT82C586B PCI/ISA Bridge provides an integrated Bus Mastering IDE controller with two high performance UltraDMA-33 EIDE interfaces for up to four IDE devices (such as hard drives or CD-ROM). The PCI/ISA Bridge also including an USB interface. The integrated I/O controller integrates the standard PC I/O functions: floppy interface, two FIFO serial ports, one EPP/ECP capable parallel port, and support for an IrDA and Consumer Infra Red compatible interface.

Four on-board 32-bit PCI local bus slots allow a higher bandwidth data path, which serves as a super highway for intensive data-movement such as video or networking. The BIOS support PCI bridge user configuration, which allows further expansion of the system with PCI peripherals. Up to two 16-bit ISA slots are provided so that the board is backward hardware compatible with the older expansion card. A total of five expansion slots can be populated with add-in cards as one PCI and ISA slots share the same chassis I/O panel. Two full lengths ISA add-in cards can be installed while the third can be half-length. All PCI cards can be full lengths.

## **Auto Jumper**

This motherboard is one of our "Auto Jumper" series, which eliminates the necessity for the user to be bored by jumper settings on the motherboard. It is capable of detecting the CPU brand, and setting the appropriate CPU core voltage and speed according to instructions from the user through CMOS setup menu. In contrast to "Jumperless", "Auto Jumper" allows switching to manual mode that enables physical jumpers on the mainboard, so as to prevents the potential impacts on jumperless boards from the future CPU specifications. On the other hand, pressing "F" to clear CMOS setting and "J" to re-detect CPU during power on the system, to avoid the difficulties to reset the CMOS data.

## **Features Summary**

This motherboard comes with following features:

- Support Intel Pentium/Pentium-MMX, AMD K5/K6/K6-2/K6-III, Cyrix/IBM 6x86/6x86MX/M-II, RiSE mP6, IDT-C6 and WinChip
- VIA Apollo VPX Chipset, including a 82C585VPX PCI/Memory controller, 82C587 data bus Accelerator and 82C586B PCI ISA IDE Accelerator.
- With an On-Board Crystal<sup>™</sup> 3D Sound Chip for Sound Version.
- WAKEUP-LINK interface header supporting Intel Wake-On-LAN
- Supports "Modems Ring On" (Requires ATX power supply and External COM2)
- Two DIMM slots Supporting up to 512MB Memory Capacity
- ✓ 4 x PCI slots, 2 x ISA slots



- 2 x USB ports, 1 x PS/2 mouse port, 1 x IrDA port
- 1 x FDD port, 1 x LPT port, 2 x COM ports
- 1 x Line-out, 1 x Line-in, 1 x Mic-in, 1 x CD-in, 1 x Game Port
- Dual IDE Channels Supporting Four Ultra-DMA33 IDE Devices
- Dual AT/ATX Power Supply Interface
- Slim Baby AT, 220mm x 170mm PCB
- AMI BIOS, PC98/ACPI/DMI Compliant
- Flash BIOS for easy upgrade
- Auto Jumper



# **Motherboard Installation**

## INSTALLATION PRECAUTIONS

During installation and initial test, use caution to avoid personal injury and damage to wiring due to sharp pins on connectors and printed circuit assemblies, rough chassis edges and corners, and hot components. Adhere to warnings and limitations regarding accessibility into areas designated only for authorized technical personnel.

## A QUICK INTRODUCTION

To Install and operate your the new motherboard, you must follow the steps below:

- 1. Install the CPU with cooling fan
- 2. Install RAM modules
- 3. Connect cables, wires and power connector
- 4. Install expansion cards



## **CPU Installation**

The motherboard provides a 321-pins, type-7, ZIP socket. The CPU should have a fan attached to it to prevent overheating. If this is not the case then purchase a fan before you turn-on the system.



**CPU Installation Diagram** 

## CAUTION

Without an effective cooling fan, the CPU can overheat and cause damage to both the CPU and the motherboard.



## **INSTALLING SYSTEM MEMORY (RAM)**

The motherboard supports two 168-pin DIMM.



168-Pin SDRAM Module Installation Diagram



## **EXPANSION CARDS INSTALLATION**

At the most of beginning, you must read your expansion card documentation on any hardware and software settings that may be required. The installation procedures are summarized as below:

- 1. Read the User's Guide/Manual of your expansion card.
- 2. If necessary, set any jumpers on your expansion card.
- 3. Power-off the system and then disconnected the power cord.
- 4. Remove your computer's cover.
- 5. Remove the metal bracket from one of the empty slot, ISA or PCI, corresponding to the type of expansion card.
- 6. Carefully align the card's connectors and press firmly, make sure that the connection is good.
- 7. Secure the card on the slot.
- 8. Replace the computer's cover.
- 9. Setup the BIOS configuration if necessary.
- 10. Install the required software drivers for your expansion card.

#### CAUTION

Before adding or removing any expansion card or other system components, make sure that you unplug your system power supply. Failure to do so may cause damage of your motherboard and expansion cards.

## **CONNECTING EXTERNAL CONNECTOR**

#### **AT Power Supply Connector**

A 12-Pin power supply provides two plugs incorporates standard  $\pm 5V$  and  $\pm 12V$ , each containing six wires, two of which are black. Orient the connectors so that the black wires are together.

Pin	Signal Name	Pin	Signal Name
1	Power Good Signal	7	Ground
2	+5V	8	Ground
3	+12V	9	-5V
4	-12V	10	+5V
5	Ground	11	+5V
6	Ground	12	+5V



#### **AT Power Connector Installation**

#### **ATX Power Supply Connector**

A single 20-pin connector incorporates standard  $\pm$ 5V and  $\pm$ 12V, optional 3.3V and soft-power signals. With a power supply supports remote power on/off, the motherboard can turn off the system power through software control, such as the shutdown in Windows 95 Start menu. The system BIOS will turn the system power off when it receives the proper APM command from the OS. APM must be enabled in the system BIOS and OS in order for the soft-off feature to work correctly.

Pin	Signal Name	Pin	Signal Name
1	+3.3V	11	+3.3V
2	+3.3V	12	-12V
3	Ground	13	Ground
4	+5V	14	PW_ON
5	Ground	15	Ground
6	+5V	16	Ground
7	Ground	17	Ground
8	PWRGOOD	18	-5V
9	+5VSB	19	+5V
10	+12V	20	+5V



ATX Power Connector Installation

#### **Floppy Drive Connector**

This 34-pin connector supports the provided floppy drive ribbon cable. After connecting the single end to the on-board "FLOPPY" connector, connect the remaining plugs on the other end to the floppy drives correspondingly.

#### **IDE Connectors**

The two on-board IDE connectors support the provided 40-pin IDE hard disk ribbon cable. After connecting the single end to the board, connect the two remaining plugs at the other end of your hard disk(s). If you install two hard disks, you must configure the two drives by setting its jumpers according to the documentation of your hard disk. Also, you may connect the two hard disks to be both Masters using one ribbon cable on the primary IDE connector and one on the secondary IDE connector.



Page 7



**Ribbon cable Installation Example** 

For the flat ribbon cable connection, please make sure that the pin 1 of the ribbon cable (the red wire side of the cable) is correctly connected to the on-board connector's pin 1 as shown on the "Map of the Motherboard".

#### **Front Panel Function Connectors**

All the front panel indicator, speaker, and switch functions are grouped into an on-board 26-pin connector, J5. Front panel features supported include:

- System Reset, RESET
- Power LED, form KEYLOCK
- Hard Drive activity LED, IDE LED
- System Speaker, SPEAKER
- Soft-touch button power on/off, SW ON
- External power saving control, EXTSMI (optional)

The connector pin out are described as the figure below:



The Onboard Function Connector Pin Out

#### Integrated USB, Infra-Red and PS/2 Mouse connector

The total of two USB device connectors, a PS/2 mouse, and Infrared devices are all allocated at this connector (JP15).

- Pin1 to Pin5 for USB1 connector
- Pin11 to Pin15 for USB2 connector
- Pin6 to Pin10 for PS/2 connector
- Pin16 to Pin20 InfraRed connector

The connector pin out signal definitions are described as the table below:

Pin	Signal Name	Pin	Signal Name
1 (USB1)	USB +5 Volt	11 (USB2)	USB +5 Volt
2	USB Port 1-	12	USB Port 2-
3	USB Port 1+	13	USB Port 2+
4	Ground	14	Ground
5	No Connect	15	No Connect
6 (PS/2 Mouse)	PS/2 Data	16 (IR)	+5V
7	PS/2 Clock	17	No Connect
8	Ground	18	Infrared Receive
9	No Connect	19	Ground
10	+5V	20	Infrared Transmit



#### **On-board Sound Connector (For Sound Version)**

The total of three connectors are using to support the On-board sound features. JP18 including the Line-In, Mic-In, Line-Out and MIDI/JOYSTICK. J6 and J7 are the CD-Audio input headers, and there pin out signal definitions are described as the table below:

Header	Pin1	Pin2	Pin3	Pin4
J6, J7	Ground	Right	Ground	Left

#### **REPLACING BATTERY**

A 3V, CR-2030, Lithium battery is installed on the on-board battery socket. This battery is used to supply the CMOS RAM backup power during system powered-off. Danger of explosion if battery is incorrectly replaced. Therefore, if you have any difficulties, please consult to the technical personnel.



Page 9

# **Quick Installation Guide**



# TX100-3D / TX100



# **TX100-3D** • Crystal 3D Audio Onboard

# **TX100** • No Crystal 3D Audio Onboard

A. AT Power Supply Connector	K. Audio CD Connectors
	- J7 ( Left, Ground, Ground, Right )
	- J6 ( Ground, Right, Ground, Left )
B. ATX Power Supply Connector	L. PCI Bus Connectors
C. AT Keyboard Connector	M. ISA Bus Connectors
D. Serial (COM1 and 2) Headers	
E. Parallel (Printer) Port Header	O. Front Panel Connector
F. Integrated Functions Connector	
G. 3.3v DIMM Sockets	Q. WAKEUP-LINK Header
H. CPU Fa Connector	
I. Floppy Drive Connector	S. IDE Connectors
J. Audio Connector	T. Socket 7

## **Quick Reference of Your Motherboard**

## [A] AT Power Supply Connector

Based on the AT specification, one 12-pin power connector covers all the required power sources,  $\pm 5V$  and  $\pm 12V$ .

## [B] ATX Power Supply Connector

Based on the ATX specifications, one 20-pin power connector covers all the required power sources,  $\pm 5V$ ,  $\pm 12V$ , and  $\pm 3.3V$ , with soft-touch button power on/off features. This connector reduces the installation time and minimizes the chance of defects caused by incorrect connection.

## [C] AT Keyboard Connector

This connection is used for a standard IBM-compatible keyboard, such as 101 enhanced keyboard.

#### [D] Serial (COM 1 and 2) Port Headers

This motherboard provides two high-speed UART compatible serial ports.

#### [E] Parallel (Printer) Port Connector

One Parallel port with SPP, EPP and ECP capabilities.

## [F] Integrated Functions Connector

- I) From Pin1 to Pin5 is the first USB device "USB1" and Pin11 to Pin15 is the second USB device "USB2" connector. Universal Serial Bus (USB) is new interface standard for any I/O device "Outside the Box". USB makes your peripherals have a real plug and play (PnP) capabilities with up to 12MB/sec data speed In the coming soon, any external device connected to your computer will be standardized into USB standard which are all have a special 4-pin rectangle shape connector. Therefore, with the Intel chipset, this motherboard builds two USB headers on-board for you future investment. If you are using an USB device, you must purchase an optional USB connector.
- II) From Pin6-10 is the PS/2 device, likes some PS/2 keyboard and mouse, are all have a standard 6pin round shape connector. However, it does not have a unique onboard standard. Therefore, please refer to the Chapter "Motherboard Installation", in order to purchase an suitable PS/2 Mouse Connector.
- III) From Pin16-20 is a 5-pin interface on the front panel I/O connector is provided to allow connection to a Hewlett Packard HSDSL-1000 compatible Infra-red (IrDA) transmitter/receiver. Once the module is connected to the front panel I/O header, Serial port 2 can be re-directed to the IrDA module. When configured for IrDA, the user can transfer files to or from portable devices such as laptops, PDA's and printers using application software such as LapLink. The IrDA specification provides for data transfers at 115kbps from a distance of 1 meter. Support for Consumer Infra Red (ASK-IR) is also included, please refer to your IR equipment for more detailed information.

#### [G] SDRAM Sockets

There are two unbuffered, 3.3 Volts, SDRAM socket on-board provides more flexibility for your system memory upgrade.

## [H] CPU FAN CONNECTOR

A 3-pin CPU fan connector.



Page 12

## [I] Floppy Drive Connector

A 34-pin connector on-board allows connection to two 360K, 720K, 1.2M, 1.44, 2.88M floppy disk drives.

#### [J] Audio Connector

All the Line-In, MIC In, Line-Out and MIDI/JOYSTICK for the On-Board Sound are located here.

#### [K] Audio CD Connectors

J6, J7 are the Audio CD input connector for the On-Board Sound.

#### [L] PCI Add-in Board Connectors

This motherboard provides four full-length 32-bits PCI slots with up to 133MB/sec burst data transfer rate.

#### [M] ISA Add-in Board Connectors

This motherboard provides two 16-bits ISA slot which allows backward hardware compatibility.

#### [O] Front Panel Function Connector

For you convenience, all the front panel functions are integrated into a single connector, which included power LED and keyboard lock, turbo switch, reset switch, SMI switch and speaker.

## [Q] WAKEUP-LINK Interface

This header is used to connect an add-in NIC (Network Interface Card) which has WOL capability to a motherboard.

## [S] IDE Connectors

This motherboard have two independent high performance bus-mastering PCI IDE interfaces capable of supporting up to UltraDMA-33 devices. The system BIOS supports automatic detection of the IDE device data transfer rate and translation between different kinds of device mode such as Logical Block Addressing (LBA) and Extended Cylinder Sector Head (ECSH) translation modes and ATAPI (e.g., CD-ROM) devices on both IDE interfaces.

In a true multi-tasking operating systems like Windows<sup>®</sup> 95 and Windows<sup>®</sup> NT, the IDE interface can operate as a PCI bus master capable of supporting Ultra DMA-33 devices with transfer rates of up to 33MB/sec.

#### [T] Pentium<sup>®</sup> Type-7 Socket

The type-7 socket is a 321-pins, zero insertion force (ZIF) socket. It provides users with a performance upgrade path to Pentium<sup>®</sup> OverDrive technology.

## PENTIUM<sup>®</sup> PROCESSOR

An approved Pentium heatsink is necessary for proper thermal dissipation in an AT compliant chassis. The processor/heatsink assembly must be securely fastened to the Socket 7 ZIF socket by two clips. These clips fit over the heatsink assembly and attach to the outer wide tabs of the Socket 7 assembly.



#### AMIBIOS HIFLEX SETUP UTILITY - VERSION 1.20 (C)1998 American Megatrends, Inc. All Rights Reserved

Standard CMOS Setup Advanced CMOS Setup Advanced Chipset Setup Power Management Setup PCI / Plug and Play Setup Peripheral Setup CPU Configuration Setup Auto-Detect Hard Disks Change User Password Change Supervisor Password Change Language Setting Auto Configuration with Optimal Settings Save Settings and Exit Exit Without Saving

# **AMIBIOS Setup**

Types of Setup	Description
Standard CMOS Setup	Sets time, date, hard disk type, types of floppy drives, monitor type, and if keyboard is installed.
Advanced CMOS Setup	Sets Typematic Rate and Delay, Above 1 MB Memory Test, Memory Test Tick Sound, Hit <del> Message Display, System Boot Up Sequence, and many others.</del>
Advanced Chipset Setup	Sets chipset-specific options and features.
Power Management Setup	Controls power conservation options.
PCI/PnP Setup	Sets options related to PCI bus and Plug and Play options.
Peripheral Setup	Controls I/O Controller-related options.
CPU Configuration Setup	This option selects the type of CPU install in the motherboard. The settings are <i>Auto</i> (AMIBIOS automatically determines the CPU type).



AMIBIOS SETUP - STANDARD CMOS SETUB (C)1998 American Megatrends, Inc. All Rights	s Reserved
Date (mm/dd/yyyy): Mon Jun 21,1999 Time (hh/mm/ss) : 16:46:08	Base Memory: 0 KB Extd Memory: 0 MB
Floppy Drive A:1.44 MB 3½Floppy Drive B:Not InstalledTypeSize Cyln Head WPcom SecPri Master : AutoPri Slave : AutoSec Master : AutoSec Slave : Auto	LBA B1k PIO 32Bit Mode Mode Mode Off Off Off Off Off
Boot Sector Virus Protection Disabled	
Available Options: Not Installed 360 KB 54 1.2 MB 54 720 KB 3½ ▶ 1.44 MB 3½	ESC:Exit ↑↓:Sel PgUp/PgDn:Modify F2/F3:Color

## Standard CMOS Setup

Select the AMIBIOS Setup options by choosing Standard Setup from the AMIBIOS Setup main menu. Standard Setup options are described below.

#### Floppy Drive A: and B:

Move the cursor to these fields via  $\uparrow$  and  $\downarrow$  and select the floppy type. The settings are 360 KB 5<sup>1</sup>/<sub>4</sub> inch, 1.2 MB 5<sup>1</sup>/<sub>4</sub> inch, 720 KB 3<sup>1</sup>/<sub>2</sub> inch, 1.44 MB 3<sup>1</sup>/<sub>2</sub> inch, or 2.88 MB 3<sup>1</sup>/<sub>2</sub> inch.

#### Primary Master Primary Slave Secondary Master Secondary Slave

Select these options to configure the drive named in the option. Select *Auto Detect IDE* to let AMIBIOS automatically configure the drive. A screen with a list of drive parameters appears. Click on *OK* to configure the drive.



Туре	How to Configure
SCSI	Select <i>Type</i> . Select <i>Not Installed</i> the drive parameter screen.
	The SCSI drivers provided by the SCSI manufacturer should
	allow you to configure the SCSI drive.
IDE	Select <i>Type</i> . Select <i>Auto</i> to let AMIBIOS determine the
	parameters. Click on OK when AMIBIOS displays the drive
	parameters. Select LBA Mode. Select On if the drive has a
	capacity greater than 540 MB.
	Select Block Mode. Select On to allow block mode data
	transfers. Select 32-Bit Mode. Select On to allow 32-bit data
	transfers. Select the PIO Mode. It is best to select Auto to allow
	AMIBIOS to determine the PIO mode. If you select a PIO mode
	that is not supported by the IDE drive, the drive will not work
	properly. If you are absolutely certain that you know the drive's
	PIO mode, select PIO mode 0 - 4, as appropriate.
CD-ROM	Select Type. Select CDROM. Click on OK when AMIBIOS
	displays the drive parameters.
Standard	Select <i>Type</i> . You must know the drive parameters. Select the
MFM	drive type that exactly matches your drive's parameters.
Non-	Select <i>Type</i> . If the drive parameters do not match the drive
Standard	parameters listed for drive types 1 - 46, select User and enter the
MFM	correct hard disk drive parameters.

#### **Entering Drive Parameters**

You can also enter the hard disk drive parameters. The drive parameters are:

Parameter	Description
Туре	The number for a drive with certain identification
	parameters.
Cylinders	The number of cylinders in the disk drive.
Heads	The number of heads.
Write Precompensation	The actual physical size of a sector gets progressively smaller as the track diameter diminishes. Yet each
	sector must still hold 512 bytes. Write precompensation circuitry on the hard disk
	by boosting the write current for sectors on inner tracks. This parameter is the track number on the disk surface where write precompensation begins.
Landing Zone	This number is the cylinder location where the heads normally park when the system is shut down.
Sectors	The number of sectors per track. MFM drives have 17 sectors per track. RLL drives have 26 sectors per track. ESDI drives have 34 sectors per track. SCSI and IDE drives have even more sectors per track.
Capacity	The formatted capacity of the drive is the number of heads times the number of cylinders times the number of sectors per track times 512 (bytes per sector).





# **CPU CONFIGURATION SETUP**

This Mainboard is among our "Auto Jumper" Series that eliminates the necessity for the user to be overwhelmed by jumper settings on the Mainboard. The system BIOS is capable to detect the CPU brand and core voltage setting the appropriate CPU speed according to the instructions from the user through the CMOS setup. In addition, *overclocking* option is provided for advanced users who prefer to run the CPU over the specified clock frequency.

Support Intel Pentium/Pentium-MMX, AMD K5/K6/K6-2/K6-III, Cyrix/IBM 6x86/6x86MX/M-II, RiSE mP6, IDT-C6 and WinChip



AMIBIOS SETUP - ADVANCED CMOS SETUP (C)1998 American Megatrends, Inc. All Rights Reserved		
1st Boot Device2nd Boot Device3rd Boot Device4th Boot DeviceTry Other Boot DevicesS.M.A.R.T. for Hard DisksQuick BootBootUp Num-LockFloppy Drive SwapFloppy Drive SeekFloppy Access ControlHDD Access ControlPS/2 Mouse SupportPrimary DisplayPassword CheckBoot To OS/2L2/L3 CacheSystem BIOS Cacheable	FLOPPY IDE-0 CDROM Disabled Yes Enabled Enabled On Disabled Enabled Normal Normal Enabled VGA/EGA Setup No Enabled Enabled Enabled	Available Options: Disabled IDE-0 IDE-1 IDE-2 IDE-3 FLOPPY LS-120 CDROM SCSI NETWORK ESC:Exit †4:Sel
COOD,16k Shadow	Enabled Enabled	FgUp/FgDn:Modify F2/F3:Color

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# ADVANCED CMOS SETUP

Quick Boot	Set this option to <i>Enabled</i> to instruct AMIBIOS to boot quickly when the computer is powered on. This option replaces the old <b>Above 1 MB Memory Test</b> Advanced Setup option. The settings are:
1st Boot Device	This option sets the type of device for the first boot drives that the AMIBIOS attempts to boot from after AMIBIOS POST completes. The settings are <i>Disabled</i> , <i>Network</i> , <i>Flopppy</i> , <i>SCSI</i> , <i>CDROM</i> , <i>IDE-0</i> , <i>IDE-1</i> , <i>IDE-2</i> , or <i>IDE-3</i> . The Optimal and Fail-Safe default settings are <i>IDE-0</i>
2nd Boot Device	This option sets the type of device for the second boot drives that the AMIBIOS attempts to boot from after AMIBIOS POST completes. The settings are <i>Disabled</i> , <i>Floppy</i> or <i>IDE-0</i> . The Optimal and Fail-Safe default settings are <i>Floppy</i> .
3rd Boot Device	This option sets the type of device for the third boot drives that the AMIBIOS attempts to boot from after AMIBIOS POST completes. The settings are <i>Disabled</i> , <i>CD-ROM</i> , or <i>IDE-0</i> . The Optimal and Fail-Safe default settings are <i>CD-ROM</i> .



Try Other Boot Devices	Set this option to <i>Yes</i> to instruct AMIBIOS to attempt to boot from any other drive in the system if it cannot find a boot drive among the drives specified in the <b>1</b> <sup>st</sup> <b>Boot Device</b> , <b>2</b> <sup>nd</sup> <b>Boot Device</b> , <b>3</b> <sup>rd</sup> <b>Boot Device</b> options.	
Floppy Access Control	This option specifies the read/write access that is set when booting from a floppy drive. The settings are <i>Read/Write</i> or <i>Read-Only</i> . The Optimal and Fail-Safe default settings are <i>Read/Write</i> .	
Hard Disk Access Contro	This option specifies the read/write access that is set when booting from a hard disk drive. The settings are <i>Read/Write</i> or <i>Read-Only</i> . The Optimal and Fail-Safe default settings are <i>Read/Write</i> .	
S.M.A.R.T. For Hard Dis	<b>sks</b> Set this option to <i>Enabled</i> to permit AMIBIOS to use the SMART (System Management and Reporting Technologies) protocol for reporting server system information over a network. The settings are <i>Enabled/Disabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .	
Boot Up Num Lock	Set this option to <i>Off</i> to turn the Num Lock key off when the computer is booted so you can use the arrow keys on both the numeric keypad and the keyboard. The settings are <i>On</i> or <i>Off</i> . The default settings are <i>On</i> .	
PS/2 Mouse Support	Set this option to <i>Enabled</i> to enable AMIBIOS support for a PS/2-type mouse. Pins 2-3 of the PS/2 Mouse Selector jumper on the motherboard must be shorted together to enable PS/2 mouse support. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Enabled</i> .	
Primary Display	This option configures the type of monitor attached to the computer. The settings are <i>Mono</i> , <i>CGA40x25</i> , <i>CGA80x25</i> , <i>VGA/EGA</i> , or <i>Absent</i> . The Optimal and Fail-Safe default settings are <i>VGA/EGA</i> .	
Password Check	This option enables password checking every time the system boots or when you run AMIBIOS Setup. If <i>Always</i> is chosen, a user password prompt appears every time the computer is turned on. If <i>Setup</i> is chosen, the password prompt appears if AMIBIOS is executed. See Advanced Setup chapter for instructions on changing a password. The Optimal and Fail-Safe defaults are <i>Setup</i> .	
Boot To OS/2	Set this option to <i>Enabled</i> if running OS/2 operating system and using more than 64 MB of system memory on the motherboard. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .	

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Page 19

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CPU Microcode Update	Set this option to <i>Enabled</i> to permit the CPU to be updated online at any time. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Enabled</i> .
System BIOS Cacheable	When set to <i>Enabled</i> , the contents of the F0000h system memory segment can be read from or written to cache memory. The contents of this memory segment are always copied from the BIOS ROM to system RAM for faster execution. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal default setting is <i>Enabled</i> . The Fail-Safe default setting is <i>Disabled</i> .

#### C000,16K Shadow C400,16K Shadow

These options specify how the 32 KB of video ROM at C0000h is treated. The settings are:

Setting	Description
Disabled	The contents of the video ROM are not copied to RAM.
Enabled	The contents of the video ROM area from C0000h - C7FFFh are copied (shadowed) from ROM to RAM for faster execution.
Cached	The contents of the video ROM area from C0000h - C7FFFh are copied from ROM to RAM and can be written to or read from cache memory.

The Optimal and Fail-Safe default settings are Cached.

C800,16K Shadow CC00,16K Shadow D000,16K Shadow D400,16K Shadow D800, 16K Shadow DC00,16K Shadow

These options enable shadowing of the contents of the ROM area named in the option. The ROM area not used by ISA adapter cards is allocated to PCI adapter cards. The settings are:

Setting	Description
Disabled	The contents of the video ROM are not copied to RAM.
Cached	The contents of the video ROM area from C0000h - C7FFFh are copied from ROM to RAM and can be written to or read from cache memory.
Enabled	The contents of the video ROM area from C0000h - C7FFFh are copied (shadowed) from ROM to RAM for faster execution.

The Optimal and Fail-Safe default settings are Cached.



AMIBIOS (C)1998 American	SETUP - ADVANCED CH Megatrends, Inc. Al	HIPSET SETUP 11 Rights Reserved
Memory Holes ECC Mode Sustained 3T Write Linear Burst CAS Latency (Clocks) Bank Interleave DRAM Timing Control DRAM Pipeline CPU To PCI Write Buffer PCI Dynamic Bursting PCI Burst. Quick Frame Generation PCI Master 1 WS Write PCI Peer Concurrency ISA Bus Clock	None Disabled Disabled 3 Enabled Normal Enabled Enabled Disabled Disabled Disabled Disabled PCICLK/5	Available Options: ► None 15M-16M(1M) 14M-16M(2M)
		ESC:Exit †↓:Sel PgUp/PgDn:Modify F2/F3:Color

#### ADVANCED Chipset Setup

The AMIBIOS Setup options described in this section are selected by choosing Advanced Chipset Setup from the AMIBIOS Setup main menu.

#### **Memory Holes**

This option enables or disables the memory hole. Memory holes are the region in memory forwarded to ISA/PCI bus instead of memory bus. The settings are *None*, 15M-16M(1M) and 14M-16M(2M). This feature will be useful for some operating systems like UNIX. The optimal and fail-safe default settings are *None*.

#### **Sustained 3T Write**

Set this option to *Enabled* to enable the sustained 3T write logic for write through cache. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Disabled*.

#### Linear Burst

Set this option to *Enabled* to enable linear burst cache mode for Cyrix CPUs. This option is only valid for Cyrix CPU. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Disabled*.



#### CAS Latency

This option is used to set the CAS latency timing for SDRAM DRAM. This setting is valid only for SDRAM DIMM. The settings are 2*CL* or 3*CL*. The optimal and fail-safe default settings are 3*CL*.

#### **Bank Interleave**

Set this option to *Enabled* to enable DRAM banks interleave logic. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Enabled*.

#### **DRAM Timing Control**

This option is used set DRAM timing of the chipset. For user convenience the options are specified as *Normal, Medium, Fast* and *Turbo*. The optimal and fail-safe default settings are *Normal*.

#### **DRAM** Pipeline

This option is used to enable or disable DRAM read and write pipeline logic. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Enabled* and *Disabled* respectively.

#### **CPU to PCI Write Buffer**

This option is used to enable or disable CPU to PCI write buffer logic. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Enabled* and *Disabled* respectively.

#### **PCI Dynamic Bursting**

This option is used to enable or disable PCI dynamic bursting logic in the chipset. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Enabled* and *Disabled* respectively.

#### **PCI Burst**

This option is used to enable or disable PCI burst logic in the chipset. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Enabled* and *Disabled* respectively.

#### **Quick Frame Generation**

This option is used to enable or disable quick frame generation logic. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Enabled* and *Disabled* respectively.

#### PCI Master 1 WS Write

This option is used to enable or disable PCI master 1 wait state write logic in the chipset. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Disabled*.

#### **PCI Peer Concurrency**

This option is used to enable or disable PCI peer concurrency logic in the chipset. The settings are *Enabled* or *Disabled*. The optimal and fail-safe default settings are *Disabled*.



Page 22

AMIBIUS SETUP - PCI / PLUG AND PLAY SETUP (C)1998 American Megatrends, Inc. All Rights Reserved		
Plug and Play Aware O/S Clear NURAM on Every Boot PCI Latency Timer (PCI Clocks) PCI UGA Palette Snoop Assign IRQ to PCI UGA PCI Slot1 IRQ Priority PCI Slot2 IRQ Priority PCI Slot3 IRQ Priority PCI Slot4 IRQ Priority DMA Channel 0 DMA Channel 1 DMA Channel 5 DMA Channel 5 DMA Channel 7 IRQ3 IRQ4 IRQ5 IRQ7	No No 64 Disabled Yes Auto Auto Auto Auto Auto PnP PnP PnP PnP PnP PnP PnP Pn	Available Options: No Yes ESC:Exit 11:Sel PgUp/PgDn:Modify
11002	101/111	12/13.00101

#### PCI/PnP Setup

Choose PCI/Plug and Play Setup from the AMIBIOS Setup screen to display the PCI and Plug and Play Setup options, described below.

#### Plug and Play Aware O/S

Set this option to *Yes* to inform AMIBIOS that the operating system can handle plug and Play (PnP) devices. The settings are *No* or *Yes*. The Optimal and Fail-Safe default settings are *No*.

#### PCI Latency Timer (PCI Clocks)

This option specifies the latency timings (in PCI clocks) for PCI devices installed in the PCI expansion slots. The settings are *32*, *64*, *96*, *128*, *160*, *192*, *224*, or *248*. The Optimal and Fail-Safe default settings are *64*.

PCI VGA Palette Snoop	When this option is set to Enabled, multiple VGA devices operating on
	different buses can handle data from the CPU on each set of palette registers on



every video device. Bit 5 of the command register in the PCI device configuration space is the VGA Palette Snoop bit (0 is disabled). For example: if there are two VGA devices in the computer (one PCI and one ISA) and:

VGA Palette	Action
Snoop Bit	
Disabled	Data read and written by the CPU is only directed to the
	PCI VGA device's palette registers.
Enabled	Data read and written by the CPU is directed to the both
	the PCI VGA device's palette registers and the ISA VGA
	device palette registers, permitting the palette registers of
	both devices to be identical.

This option must be set to *Enabled* if any ISA adapter card installed in the system requires VGA palette snooping. The Optimal and Fail-Safe default settings are *Disabled*.

Allocate IRQ To PCI VGA	Set this option to <i>Yes</i> to allocate an IRQ to the VGA device on the PCI bus. The settings are <i>Yes</i> or <i>No</i> . The Optimal and Fail-Safe default settings are <i>Yes</i> .
PCI IDE Bus Master	Set this option to <i>Enabled</i> to specify that the IDE controller on the PCI bus has bus mastering capability. The settings are <i>Disabled</i> or <i>Enabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .
Offboard PCI IDE Card	This option specifies if an offboard PCI IDE controller adapter card is used in the computer. You must also specify the PCI expansion slot on the motherboard where the offboard PCI IDE controller card is installed. If an offboard PCI IDE controller is used, the motherboard onboard IDE controller is automatically disabled. The settings are <i>Disabled</i> , <i>Auto</i> , <i>Slot1</i> , <i>Slot2</i> , <i>Slot3</i> , <i>Slot4</i> , <i>Slot5</i> , or <i>Slot6</i> . If <i>Auto</i> is selected, AMIBIOS automatically determines the correct setting. The Optimal and Fail-Safe default settings are <i>Auto</i> . This option forces IRQ 14 and 15 to a PCI slot on the PCI local bus. This is necessary to support non-compliant PCI IDE adapter cards.
Offboard PCI IDE Primary	<b>IRQ</b> This option specifies the PCI interrupt used by the primary IDE channel on the offboard PCI IDE controller. The settings are <i>Disabled</i> , <i>Hardwired</i> , <i>INTA</i> , <i>INTB</i> , <i>INTC</i> , or <i>INTD</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .
Offboard PCI IDE Secondar	<b>ry IRQ</b> This option specifies the PCI interrupt used by the secondary IDE channel on the offboard PCI IDE controller. The settings are <i>Disabled, Hardwired, INTA, INTB, INTC</i> , or <i>INTD</i> . The Optimal and Fail-Safe settings are <i>Disabled</i> .
PCI Slot1 IRQ Priority PCI Slot2 IRQ Priority PCI Slot3 IRQ Priority PCI Slot4 IRQ Priority EX PCI Slot4 IRQ Priority PCI Slot4 IRQ Priority PCI Slot4 IRQ Priority	hese options specify the IRQ priority for PCI devices installed in the PCI apansion slots. The settings are <i>Auto</i> , <i>(IRQ) 3, 4, 5, 7, 9, 10</i> , and <i>11</i> , in fority order. The Optimal and Fail-Safe default settings are <i>Auto</i> .

DMA Channel 0 DMA Channel 1 DMA Channel 3 DMA Channel 5 DMA Channel 6 DMA Channel 7	These options allow you to specify the bus type used by each DMA channel. The settings are $PnP$ or ISA/EISA. The Optimal and Fail-Safe default settings are $PnP$ .
IRQ3 IRQ4 IRQ5 IRQ7 IRQ9 IRQ10 IRQ11 IRQ12 IRQ14 IRQ15 Reserved Memo	These options specify the bus that the specified IRQ line is used on. These options allow you to reserve IRQs for legacy ISA adapter cards. These options determine if AMIBIOS should remove an IRQ from the pool of available IRQs passed to devices that are configurable by the system BIOS. The available IRQ pool is determined by reading the ESCD NVRAM. If more IRQs must be removed from the pool, the end user can use these options to reserve the IRQ by assigning an <i>ISA/EISA</i> setting to it. Onboard I/O is configured by AMIBIOS. All IRQs used by onboard I/O are configured as <i>PCI/PnP</i> . <b>IRQ12</b> only appears if the <b>Mouse Support</b> option in Advanced Setup is set to <i>Disabled</i> . IRQ14 and 15 will not be available if the onboard PCI IDE is enabled. If all IRQs are set to ISA/EISA and IRQ14 and 15 are allocated to the onboard PCI IDE, IRQ9 will still be available for PCI and PnP devices, because at least one IRQ must be available for PCI and PnP devices. The settings are <i>ISA/EISA</i> or <i>PCI/PnP</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> . <i>16K</i> , <i>32K</i> , or <i>64K</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .
Reserved Memo	<b>ry Address</b> This option specifies the beginning address (in hex) of the reserved memory area. The specified ROM memory area is reserved for use by legacy ISA adapter cards.
This option do	es not appear if the <b>Reserved Memory Size</b> option is set to <i>Disabled</i> . The settings are <i>C0000</i> , <i>C4000</i> , <i>C8000</i> , <i>CC000</i> , <i>D0000</i> , <i>D4000</i> , <i>D8000</i> , <i>DC000 or N/A</i> . The Optimal and Fail-Safe default settings are <i>N/A</i> .



AMIBIOS SETUP - POWER MANAGEMENT SETUP (C)1998 American Megatrends, Inc. All Rights Reserved		
Power Management/APM Wake-On-LAN Feature External Modem Wake-Up Green PC Monitor Power State Video Power Down Mode Hard Disk Time Out (Minute) Standby Time Out (Minute) Standby Time Out (Minute) Standby Clock Display Activity IRQ3 IRQ4 IRQ5 IRQ7 IRQ9 IRQ10 IRQ11 IRQ12 IRQ13	Disabled Disabled Disabled Stand By Suspend Stand By Disabled 3 3 CPUCLK/2 Ignore Monitor Monitor Ignore Ignore Ignore Ignore Ignore Ignore Ignore Ignore Ignore Ignore	Available Options: ► Disabled Enabled Enabled ESC:Exit 14:Sel PgUp/PgDn:Modify F2/F3:Color

## POWER MANAGEMENT SETUP

The AMIBIOS Setup options described in this section are selected by choosing Power Management Setup from the AMIBIOS Setup main menu.

Power Management/APM	Set this option to <i>Enabled</i> to enable the chipset power management and APM (Advanced Power Management) features. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .
Wake-On-LAN Feature	Set this option to <i>Enabled</i> to enable the Wake-On-LAN features. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i>
External Modem Wake-Up	Set this option to <i>Enabled</i> to enable External Modem Wake-Up. The settings are <i>Enabled</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> .



Green PC Monitor	Power State mon spec Stan are A	This option specifies the power state that the green PC-compliant video itor enters when AMIBIOS places it in a power saving state after the cified period of display inactivity has expired. The settings are <i>Off</i> , <i>adby</i> , <i>Suspend</i> , or <i>Disabled</i> . The Optimal and Fail-Safe default settings <i>Disabled</i> .		
Video Power Down Mode Hard Disk Power Down Mode		This option specifies the power state that the video subsystem enters when AMIBIOS places it in a power saving state after the specified period of display inactivity has expired. The settings are <i>Standby</i> , <i>Suspend</i> or <i>Disabled</i> . The Optimal and Fail-Safe default settings are <i>Disabled</i> . This option specifies the power conserving state that the hard disk drive enters after the specified period of hard drive inactivity has expired. The settings are <i>Disabled</i> , <i>Standby</i> , or <i>Suspend</i> . The Optimal and Fail-Safe		
Hard Disk Timeout This option specifies the length of a period of hard disk drive inactivity. When this length of time expires, the computer enters power-conserving state specified in the Hard Disk Power Down Mode option (see the previous page). The settings are Disabled, 1 min. (minute), 2 min, 3 min., 4 min., 5 min., 6 min, 7 min., 8 min., 9 min., 10 min., 11 min., 12 min., 13 min., 14 min, and 15 min. The Optimal and Fail-Safe default settings are Disabled.				
Standby/Suspend T	<b>Timer Unit</b>	This option specifies the unit of time used for the Standby and Suspend imeout periods. The settings are <i>4 msec</i> , <i>4 sec</i> , <i>32 sec</i> , or <i>4 min</i> . The Optimal and Fail-Safe default settings are <i>4 min</i> .		
Standby Timeout	This option specifies the length of a period of system inactivity while in Full power on state. When this length of time expires, the computer enters Standby power state. The settings are <i>Disabled</i> , 4 msec, 8 msec, 12 msec, 16 msec, up to 508 msec, in increments of 4 msec. The Optimal and Fail-Safe default settings are <i>Disabled</i> .			
Suspend Timeout	This option specifies the length of a period of system inactivity while in Standby state. When this length of time expires, the computer enters Suspend power state. The settings are <i>Disabled</i> , 4 msec, 8 msec, 12 msec, 16 msec, up to 508 msec, in increments of 4 msec. The Optimal and Fail-Safe default settings are <i>Disabled</i> .			
Slow Clock Ratio	This option s power saving CPU clock sp conserving st 62.5%, 62.5- 62.5%.	pecifies the speed at which the system clock runs in the Standby Mode state. The settings are expressed as a percentage between the normal peed and the CPU clock speed when the computer is in the power- ate. The settings are 0-12.5%, 12.5-25%, 25-37.5%, 37.5-50%, 50- 75%, or 75-87.5%. The Optimal and Fail-Safe default settings are 50-		

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#### **Display Activity**

When set to *Monitor*, this option enables event monitoring on the video display. If set to *Monitor* and the computer is in a power saving state, AMIBIOS watches for display activity. The computer enters the Full On state if any activity occurs. AMIBIOS reloads the Standby and Suspend timeout timers if display activity occurs. The settings are *Monitor* or *Ignore*. The Optimal and Fail-Safe default settings are *Ignore*.

Device 6 (Serial Port 1) Device 7 (Serial Port 2) Device 8 (Parallel Port) Device 5 (Floppy Disk) Device 0 (Primary Master IDE) Device 1 (Primary Salve IDE) Device 2 (Secondary Master IDE) Device 3 (Secondary Slave IDE)

When set to *Monitor*, these options enable event monitoring on the specified hardware interrupt request line. If set to *Monitor* and the computer is in a power saving state, AMIBIOS watches for activity on the specified IRQ line. The computer enters the Full On state if any activity occurs. AMIBIOS reloads the Standby and Suspend timeout timers if activity occurs on the specified IRQ line.

The settings for each of these options are *Monitor* or *Ignore*. The Optimal and Fail-Safe default settings are *Ignore*.



AMIBIOS SETUP - PERIPHERAL SETUP (C)1998 American Megatrends, Inc. All Rights Reserved					
Duboard Crystal 3D Audio Onboard USB Controller USB Legacy Support OnBoard FDC Serial Port1 Serial Port2 OnBoard Ir Port Ir Mode Ir Duplex OnBoard Parallel Port Parallel Port Mode Parallel Port IRQ Parallel Port DMA Channel VIA OnBoard PCI IDE	Enabled Enabled Enabled Auto Auto Disabled HPSIR Half 378 Normal 7 N/A Both	Available Options: Disabled ► Enabled			
		ESC:E×it ↑↓:Sel PgUp/PgDn:Modify F2/F3:Color			

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## **PERIPHERAL SETUP**

	Peripheral Setup options are displayed by choosing Peripheral Setup from the AMII Setup main menu. All Peripheral Setup options are described here.		
Onboard Crystal	3D Audio	Set this option to <i>Enabled</i> to enable the onboard Crystal 3D Audio for mainboard with Crystal 3D sound chip.	
USB Function	Set this option to <i>Enabled</i> to enable USB (Universal Serial Bus) support. The settings are <i>Enabled</i> or <i>Disabled</i> .		
USB Keyboard	Mouse Lega Set this opti devices if the or <i>Disabled</i> .	acy Support ion to <i>Enabled</i> to enable support for older keyboards and mouse e USB Function option is set to <i>Enabled</i> . The settings are <i>Enabled</i>	
Onboard IDE	This option specifies the IDE channel used by the onboard IDE controller. The settings are <i>Disabled, Primary</i> , or <i>Secondary</i> .		
Onboard Floppy	<sup>7</sup> Controller	Set this option to <i>Enabled</i> to enable the floppy drive controller on the motherboard. The settings are <i>Auto (AMIBIOS automatically determines if the floppy controller should be enabled), Enabled, or Disabled.</i>	



Onboard Serial Port1		This option specifies the base I/O port address of serial port 1. The settings are <i>Auto (AMIBIOS automatically determines the correct base I/O port address), Disabled, 3F8h, 2F8h, 2E8h,</i> or <i>3E8h.</i> The Optimal and Fail-Safe default settings are <i>Auto.</i>			
Onboard Serial Port2		This option specifies the base I/O port address of serial port 2. The settings are <i>Auto (AMIBIOS automatically determines the correct base I/O port address), Disabled, 3F8h, 2F8h, 2E8h,</i> or <i>3E8h.</i> The Optimal and Fail-Safe default settings are <i>Auto.</i>			
Serial Port2 Mode		This option specifies the operating mode for serial port 2. This option only appears if the <b>Onboard Serial Port2</b> option is not set to <b>Auto</b> or <b>Disabled</b> . The settings are <i>IR</i> (infrared) or <i>Normal</i> . The Optimal and Fail-Safe default settings are <i>Normal</i> .			
IR Mode		This option specifies type of infrared devices supported by the system. This option only appears if the <b>Onboard Serial Port2</b> option is not set to <b>Auto</b> or <b>Disabled.</b> The settings are <i>Encoded</i> or <i>Non-Encoded</i> . There are no default settings.			
IR Duplex Mode		This option specifies the type of duplexing used for infrared on serial port 2. This option only appears if the <b>Onboard Serial Port2</b> option is not set to <b>Auto</b> or <b>Disabled.</b> The settings are <i>Half</i> or <i>Full</i> . There are no default settings.			
IR Transmitter		This option specifies the type of transmission used by the infrared devices attached to serial port 2. This option only appears if the <b>Onboard Serial Port2</b> option is not set to <b>Auto</b> or <b>Disabled.</b> The settings are <i>1.6 uS</i> or <i>3/16 Baud.</i> There are no default settings.			
Onboard Parallel Port		This option specifies the base I/O port address of the parallel port on the motherboard. The settings are <i>Disabled</i> , <i>378h</i> , <i>278h</i> , or <i>3BCh</i> . The Optimal default setting is <i>378h</i> .			
Parallel Port Mode		This option specifies the parallel port mode. The Optimal default setting is <i>Normal</i> . The Fail-Safe default setting is <i>Disabled</i> . The settings are:			
	Setting	g Description			
	Norma	<i>l</i> The normal parallel port mode is used.			
	Bi-Dir	Use this setting to support bidirectional transfers on the parallel			
	EPP	The parallel port can be used with devices that adhere to the Enhanced Parallel Port (EPP) specification. EPP uses the existing parallel port signals to provide asymmetric bidirectional data transfer driven by the host device.			
	ECP	The parallel port can be used with devices that adhere to the Extended Capabilities Port (ECP) specification. ECP uses the DMA protocol to achieve data transfer rates up to 2.5 Megabits per second. ECP provides symmetric bidirectional			

Page 30

communication.

EPP Version	This option specifies the Enhanced Parallel Port specification version number that is used in the system. This option only appears if the <b>Parallel Port Mode</b> option is set to <i>EPP</i> .			
	The settings are 1.7 or 1.9. There are no Optimal and Fail-Safe default settings because the default setting for the <b>Parallel Port Mode</b> option is not <i>EPP</i> .			
Parallel Port DMA Channel		This option is only available if the setting for the <b>Parallel Port Mode</b> option is <i>ECP</i> . This option sets the DMA channel used by the parallel port. The settings are ( <i>DMA Channel0, 1,</i> or <i>3</i> . The Optimal and Fail-Safe default settings are not provided.		
Parallel Port II	<b>RQ</b> This of <i>(IRQ)</i>	otion specifies the IRQ used by the parallel port. The settings are Auto, 5, or ( <i>IRQ</i> ) 7. The Optimal and Fail-Safe default settings are Auto.		

## SAVE AND EXIT SETUP

Select this option when you finished setup the CMOS and it will save the change you made and reboot the system after you press "YES".

## **EXIT WITHOUT SAVING**

If you decided not to save any change you had made, you can select this option to exit the CMOS setup and all the change you made will be ignored.

