

SY-7VCA2 Motherboard

Pentium[®] III & Celeron™

Processor supported

Via 694X AGP/PCI/AMR Motherboard

66/100/133 MHz Front Side Bus supported

ATX Form Factor

User's Manual

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About This Guide:

This Quick Start Guide can help system manufacturers and end users in setting up and installing the Motherboard. Information in this guide has been carefully checked for reliability; however, to the correctness of the contents there is no guarantee given. The information in this document is subject to amend without notice.

For further information, please visit our **Web Site** on the Internet. The address is "http://www.soyo.com.tw".

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Version 1.0 7VCA2 SERIAL FC Tested To Comply
With FCC Standards
FOR HOME OR OFFICE USE

100% POST CONSUMER RECYCLED PAPER



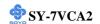
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Chapter 1

MOTHERBOARD DESCRIPTION

1-1 INTRODUCTION

The **SY-7VCA2** AGP/PCI/AMR Motherboard is a high-performance Socket 370 supported ATX form-factor system board. The **SY-7VCA2** uses VIA Chipset technology and supports Socket 370 class processors. This Motherboard is fully compatible with industry standards and adds many technical enhancements.

1-2 KEY FEATURES

> CPU SUPPORT

The SY-7VCA2 supports a wide range of INTEL CPUs:

- FSB 133MHz: Intel Pentium ® III 533EBMHz~1.13 GHz
- FSB 100MHz: Intel Pentium ® III 500E~800 MHz
- FSB 66MHz: Intel Celeron ® 400~533 MHz

New released Intel Socket 370 CPUs will very likely be supported by the SY-7VCA2 as well.

> CPU SETTINGS

The SY-7VCA2 provides the user with a very complete and convenient CPU setting environment. The CPU settings are all adjusted through the special SOYO COMBO page in the BIOS, therefore rendering the use of jumpers obsolete.

■ CPU FSB Frequency

The SY-7VCA2 supports an incredible wide range of CPU FSB frequency settings:

66, 75, 81, 83, 90, 95, 100, 105, 110, 112, 113, 115, 117, 118, 120, 122, 124, 126, 133, 135, 137, 138, 140, 142, 144, 150 and 155 MHz.

This ensures that the SY-7VCA2 has an overwhelming overclocking potential.



■ CPU Multiplier

The SY-7VCA2 supports a wide range of multipliers:

2.0x, 2.5x, 3.0x, 3.5x, 4.0x, 4.5x, 5.0x, 5.5x, 6.0x, 6.5x, 7.0x, 7.5x and 8.0x

> EXPANDABILITY

The SY-7VCA2 provides all the standard expansion slots, and many more additional expansion features:

♦ Expansion slots

- 1 x 32-bit bus master AGP slot
- 5 x 32-bit bus master PCI slots
- 1 x 16-bit ISA slots

◆ Enhanced IO

- Floppy disk controller
- 2x EIDE controllers with support for up to 4 Ultra DMA 33/66 /100 devices
- Standard/EPP/ECP parallel port
- 2x 16550 compatible serial ports
- IrDA compatible infrared port
- 4x USB (Universal Serial Bus) connectors
- PS/2 mouse connector
- PS/2 keyboard connector



> ADVANCED FUNCTIONS

The SY-7VCA2 supports advanced functions such as:

■ Wake-On-LAN

Supports Wake-On-LAN (Some advanced network cards can wake the system up over the network, the WOL connector is provided by the SY-7VCA2 to support this function).

■ Multiple boot

The SY-7VCA2 supports booting from devices such as CD-ROM.

Power on by modem or alarm

If the SY-7VCA2 system is in suspend mode, it can be switched back on through the modem or RTC alarm through this function. This opens a lot of possibilities, such as remote access that switches the system on only after the modem receives a call.

> FAIL SAFE

The SY-7VCA2 comes with added functionality to make managing the system easy and safe

♦ Hardware Monitor

The integrated Hardware Monitor IC and Hardware doctor software enables the user to monitor system voltages, temperatures and FAN speeds. This makes sure that the user is full control of the system

♦ Power Failure Resume Function

This function can be set in the BIOS, and determines whether the system will automatically turn on again after a power failure. This function is indispensable for server systems that need to always be on line.

♦ 5VSB Indicator

This green LED gives an indication of the presence of the 5V Standby voltage. This voltage is always fed into the motherboard and is used for functions such as WOL.

NORTON SOFTWARE PACK

> COMPLIANCE

The SY-7VCA2 complies with all important industry standards. The



following underlines the reliability of the SY-7VCA2, a motherboard to trust.

■ PC99, ACPI compliant

USER FRIENDLY

- SOYO Combo Setup
- Jumperless design
- You can set up the following options trough the BIOS setting
 - CPU FSB frequency
 - CPU multiplier
 - CPU Vcore voltage
 - PCI clock
 - AGP Clock
 - SDRAM Clock



1-3 HANDLING THE MOTHERBOARD

To avoid damage to your Motherboard, follow these simple rules while unpacking:

- ➤ Before handling the Motherboard, ground yourself by grasping an unpainted portion of the system's metal chassis.
- Remove the Motherboard from its anti-static packaging. Hold the Motherboard by the edges and avoid touching its components.
- > Check the Motherboard for damage. If any chip appears loose, press carefully to seat it firmly in its socket.



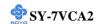
Warning: Do not apply power if the Motherboard appears damaged. If there is damage to the board, contact your dealer immediately.

1-4 ELECTROSTATIC DISCHARGE PRECAUTIONS

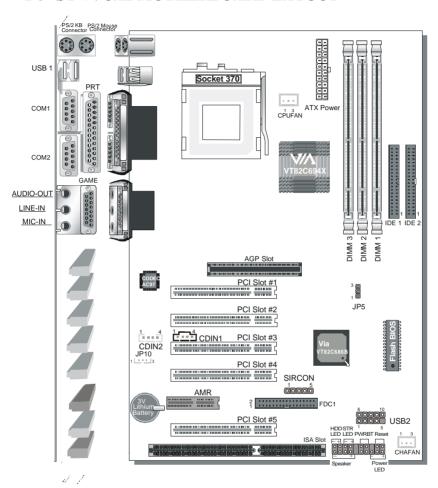
Make sure to ground yourself before handling the Motherboard or other system components. Electrostatic discharge can easily damage the components. Note that you must take special precautions when handling the Motherboard in dry or air-conditioned environment.

To protect your equipment from electrostatic discharge, take the following precautions:

- > Do not remove the anti-static packaging until you are ready to install.
- Ground yourself before removing any system component from its protective anti-static packaging. (To ground yourself, grasp the expansion slot covers or other unpainted portions of the computer chassis.)
- Frequently ground yourself while working or use a grounding strap.
- ➤ Handle the Motherboard by its edges and avoid touching its components.

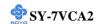


1-5 SY-7VCA2 MOTHERBOARD LAYOUT

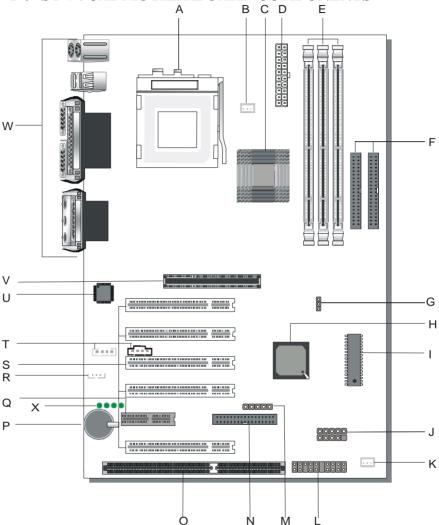


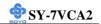
Back Panel

SY-7VCA2 Platform

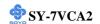


1-6 SY-7VCA2 MOTHERBOARD COMPONENTS





- A Socket 370 Connector
- **B** CPU Cooling Fan Connector
- C Via 694X North Bridge chip
- **D** ATX Power Supply Connector
- E DIMM Banks
- F Bus Mastering e-IDE/ATAPI Ports
- **G** CMOS Clear Jumper
- H Via 686B South Bridge Chip
- I Flash BIOS
- J USB Ports
- K Chassis Cooling Fan
- L Front panel connectors
- M Serial Infrared (IrDA) Device Header
- N Floppy Disk Drive (FDD) Port
- O 16-bit ISA Slot
- P 3V Lithium Battery
- O Audio Modem Riser Slot
- R Wake-On-LAN (WOL) Header
- S 32-bit PCI Mastering Slots
- T CD-IN Connectors
- U AC97 Codec Chip
- V 32-bit AGP Slot
- W Back panel Connectors
- X Debug LEDs



1-7 MICROPROCESSOR

The motherboard supports a single Socket 370 processor. The processor's VID pins automatically program the voltage regulator on the motherboard to the required processor voltage. In addition, the front side bus speed (100 MHz and 133 MHz) is automatically selected. The motherboard supports all current Socket 370 processor speeds, voltages, and bus frequencies.

1-7.1 Microprocessor Packaging

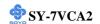
The CPU is packaged in a 370 pin PGA package. A fan must be used to ensure adequate cooling.

1-7.2 Second Level Cache

The second-level cache is located on the substrate of the CPU package. The cache includes 0KB/128KB or 512 KB of synchronous pipelined burst static RAM. All supported onboard memory can be cached.

1-7.3 Microprocessor Upgrades

The motherboard can be upgraded with Socket 370 processors that run at higher speeds. When upgrading the processor, use the BIOS configuration mode to change the processor speed.



1-8 MEMORY

1-8.1 Main Memory

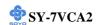
The motherboard has three DIMM sockets. SDRAM can be installed in one, two, or three sockets. Using the serial presence detect (SPD) data structure, programmed into an E²PROM on the DIMM, the BIOS can determine the SDRAM's size and speed. Minimum DIMM memory size is 8 MB; maximum DIMM memory size is 256/512 MB. Memory size and speed can vary between sockets.

The motherboard supports the following memory features:

- DRAM interface synchronous with host CPU (66/100/133MHz) or AGP (66MHz) for most flexible configuration
- DRAM interface may be faster than CPU by 33 MHz to allow use of PC100 memory modules with 66MHz Celeron or use of PC133 with 100MHz Pentium II or Pentium III
- DRAM interface may be slower than CPU by 33 MHz to allow use of older memory modules with newer CPUs (e.g., PC66 memory modules with 100MHz Pentium II or Pentium III)
- Concurrent CPU, AGP, and PCI access
- Supports FP,EDO, SDRAM, ESDRAM and VCM SDRAM memory types
- Different DRAM types may be used in mixed combinations
- Different DRAM timing for each bank
- Dynamic Clock Enable (CKE) control for SDRAM power reduction in high speed system.
- Mixed 1M/2M/4M/8M/16M/32MxN DRAMs
- The 7VCA2 supports 3 DIMMs or 6 banks at 133MHz for 1.5GB max memory.
- Flexible row and column addresses
- 64-bit data width only
- 3.3V DRAM interface with 5V-tolerant inputs
- Programmable I/O drive capability for MA, command, and MD signals



- Dual copies of MA signals for improved drive
- Optional bank-by-bank ECC (single-bit error correction and multibit error detection) or EC(error checking only) for DRAM integrity
- Two-bank interleaving for 16Mbit SDRAM support
- Two-bank and four bank interleaving for 64Mbit SDRAM support
- Independent SDRAM control for each bank
- Seamless DRAM command scheduling for maximum DRAM bus utilization (e.g., precharge other banks while accessing the current bank)
- Four cache lines (16quadwords) of CPU to DRAM write buffers
- Four cache lines of CPU to DRAM read prefetch buffers
- Read around write capability for non-stalled CPU read
- Speculative DRAM read before snoop result
- Burst read and write operation
- x-2-2-2-2-2 bank-to-back accesses for EDO DRAM from CPU or from DRAM controller
- x-1-1-1-1-1 back-to back accesses for SDRAM
- BIOS shadow at 16KB increment
- Decoupled and burst DRAM refresh with staggered RAS timing
- CAS before RAS or self refresh



1-9 CHIPSET

> VT82C694X

The VIA 694X is a high performance, cost-effective and energy efficient chip set for the implementation of AGP/PCI/ISA desktop personal computer systems from 66MHz, 100MHz and 133MHz based on 64-bit Socket 370 super-scalar processors.

The Via 694X chip set consists of the VT82C694A system controller (510 pin BGA) and the VT82C686B PCI to ISA bridge (352 pin BGA). The system controller provides superior performance between the CPU, DRAM, AGP bus, and PCI bus with pipelined, burst, and concurrent operation.

The VT82C694X supports eight banks of DRAMs up to 1.5GB. The DRAM controller supports standard Fast Page Mode (FPM) DRAM, EDO-DRAM, Synchronous DRAM (SDRAM) and Virtual Channel SDRAM (VC SDRAM), in a flexible mix / match manner. The Synchronous DRAM interface allows zero wait state bursting between the DRAM and the data buffers at 66/100/133 MHz. The eight banks of DRAM can be composed of an arbitrary mixture of 1M/2M/4M/8M/16M/32MxN DRAMs. The DRAM controller also supports optional ECC (single-bit error correction and multi-bit detection) or EC (error checking) capability separately selectable on a bank-by-bank basis. The DRAM controller can run at either the host CPU bus frequency (66/100/133 MHz) or at the AGP bus frequency (66 MHz) with built-in PLL timing control.

The VT82C694X system controller also supports full AGP v2.0 capability for maximum bus utilization including 4x mode transfers, SBA (Side Band Addressing), Flush/Fence commands, and pipelined grants. An eight level request queue plus a four level post-write request queue with thirty-two and sixteen quad words of read and write data FIFO's respectively are included for deep pipelined and split AGP transactions. A single-level GART TLB with 16 full associative entries and flexible CPU/AGP/PCI remapping control is also provided for operation under protected mode



operating environments. Both Window 95 VxD and Windows 98 /NT5.0 mini port drivers are supported for interoperability with major AGP-based 3D and DVD- capable multimedia accelerators.

The VT82C694X supports two 32-bit 3.3/5V system buses (one AGP and one PCI) that are synchronous/pseudo-synchronous to the CPU bus. The chip also contains a built-in bus-to-bus bridge to allow simultaneous concurrent operations on each bus. Five levels (double words) of post write buffers are included to allow for concurrent CPU and PCI operation. For PCI master operation, forty-eight levels (double words) of post write buffers and sixteen levels (double words) of prefetch buffers are included for concurrent PCI bus and DRAM/cache accesses. The chip also supports enhanced PCI bus commands such as Memory-Read-Line, Memory-Read-Multiple and Memory-Write-Invalid commands to minimize snoop overhead. In addition, advanced features are supported such as snoop ahead, snoop filtering, L1 write-back forward to PCI master, and L1 writeback merged with PCI post write buffers to minimize PCI master read latency and DRAM utilization. Delay transaction and read caching mechanisms are also implemented for further improvement of over all system performance.

The 352-pin Ball Grid Array VT82C686B PCI to ISA bridge supports four levels (double words) of line buffers, type F DMA transfers and delay transaction to allow efficient PCI bus utilization The VT82C686B also includes an integrated keyboard controller with PS/2 mouse support, integrated DS12885 style real time clock with extended 256 byte CMOS RAM, integrated master mode enhanced IDE controller with full scatter/gather capability and extension to UltraDMA-33/66 for 33/66 MB/sec transfer rate, integrated USB interface with root Hub and two function ports with built-in physical layer transceivers, Distributed DMA supports, and On Now/ ACPI compliant advanced configuration and power management interface.

For sophisticated power management, the Via 694X provides independent clock stop control for the CPU/SDRAM, PCI, and AGP buses and



Dynamic CKE control for powering down of the SDRAM. A separate suspend-well plane is implemented for the SDRAM control signals for Suspend-to-DRAM operation. Coupled with the VT82C686B south bridge chip, a complete power conscious PC motherboard can be implemented with no external TTLs.

The Via 694X chipset is ideal for high performance, high quality, high energy efficient and high integration desktop and notebook AGP / PCI / ISA computer systems.

> VT82C686B

The VT82C686B PSIPC (PCI Super-I/O integrated Peripheral Controller) is a high integration, high performance, power-efficient, and high compatibility device that supports Intel and non-Intel based processor to PCI bus bridge functionality to make a complete Microsoft PC99-compliant PCI/ISA system In addition to complete ISA extension bus functionality, the VT82C686B includes standard intelligent peripheral controllers:

- 1) Master mode enhanced IDE controller with dual channel DMA engine and interlaced dual channel commands. Dedicated FIFO coupled with scatter and gather master mode operation allows high performance transfers between PCI and IDE \devices. In addition to standard PIO and DMA mode operation, the VT82C686B also supports the UltraDMA-33 standard to allow reliable data transfer rates up to 33MB/sec throughput. The VT82C686B also supports the UltraDMA-66/100 standard. The IDE controller is SFF-8038I v1.0 and Microsoft Windows-family compliant.
- 2) Universal Serial Bus controller that is USB v1.1 and Universal HCI v1.1 compliant. The VT82C686B includes the root hub with four function ports with integrated physical layer transceivers. The USB controller allows hot plug and play and isochronous peripherals to be inserted into the system with universal driver support. The controller also implements legacy keyboard and mouse support so that legacy



- software can run transparently in non-USB-aware operating system environment
- 3) Keyboard controller with PS2 mouse support.
- 4) Real Time Clock with 256 byte extended CMOS. In addition to the standard ISA RTC functionality, the integrated RTC also includes the date alarm, century field, and other enhancements for compatibility with the ACPI standard.
- 5) Hardware monitoring subsystem for managing system/ motherboard voltage levels, temperatures, and fan speeds
- 6) Full System Management Bus (SMBus) interface.
- 7) Two 16550-compatible serial I/O ports with infrared communications port option. A third serial port is available to be dedicated to the IR interface.
- 8) Integrated PCI-mastering dual full-duplex direct-sound AC97-link-compatible sound system. Hardware soundblaster-pro and hardware-assisted FM blocks are included for Windows DOS box and real-mode DOS compatibility. Loopback capability is also implemented for directing mixed audio streams into USB and 1394 speakers for high quality digital audio
- 9) Two game ports and One MIDI port
- 10) ECP/EPP-capable parallel port
- 11) Standard floppy disk drive interface
- 12) Distributed DMA capability for support O ISA legacy DMA over the PCI bus. Serial IRQ is also supported for docking and non-docking application.
- 13) Plug and Play controller that allows complete steerability of all PCI interrupts and internal interrupts/ DMA channels to any interrupt channel. One additional steerable interrupt channel is provided to allow plug and play and reconfigurability of onboard peripherals for Windows family compliance.

The VT82C686B also enhances the functionality of the standard ISA peripherals. The integrated interrupt controller supports both edge and



level triggered interrupts channel by channel. The integrated DMA controller supports type F DMA in addition to standard ISA DMA modes. Compliant with the PCI2.2 specification, the VT83C686B supports delayed transactions and remote power management so that slower ISA peripherals do not block the traffic of the PCI bus. Special circuitry is built in to allow concurrent operation without causing dead lock even in a PCI-to PCI bridge environment. The chip also includes eight levels (doublewords) of line buffers from the PCI bus to the ISA bus to further enhance overall system performance.

1-10 I/O INTERFACE CONTROLLER

The motherboard uses the VT82C686B Super-I/O controller which features:

- Supports 2 serial ports, IR port, parallel port, and floppy disk controller functions
- ➤ Two UARTs for Complete Serial Ports
 - Even, odd, stick or no parity bit generation and detection
 - Programmable baud rate generator
 - High speed baud rate (230Kbps, 460Kbps) support
 - Independent transmit/receiver FIFOs
 - Modem Control
 - Plug and play with 96 base IO address and 12 IRQ options
- One dedicated IR port
 - Third serial port dedicated to IR function
 - IR function either through the two complete serial ports or the third dedicated port
 - Infrared-IrDA (HPSIR) and ASK (Amplitude Shift Keyed) IR
- Multi-mode parallel port
 - Standard mode, ECP and EPP support
 - Plug and play with 192 base IO address, 12 IRQ and 4 DMA options
- ➤ Floppy Disk Controller
 - 16bytes of FIFO



- Data rates up to 1Mbps
- Perpendicular recording driver support
- Two FDDs with drive swap support
- Plug and play with 48 base IO address, 12 IRQ and 4 DMA options

The Setup program provides configuration option for the I/O controller.

1-10.1 Serial Ports

The motherboard has two 9-pin D-Sub serial port connectors located on the back panel. The NS16C5450-compatible UARTs support data transfers at speeds up to 115.2 Kbits/sec with BIOS support.

1-10.2 Parallel Port

The connector for the multimode bi-directional parallel port is a 25-pin D-Sub connector located on the back panel of the motherboard. In the Setup program, there are four options for parallel port operation:

- Compatible (standard mode)
- Bi-directional (PS/2 compatible)
- Bi-directional EPP. A driver from the peripheral manufacturer is required for operation.
- Bi-directional high-speed ECP

1-10.3 Diskette Drive Controller

The I/O controller is software compatible with the 82077 diskette drive controller and supports both PC-AT and PS/2 modes. In the Setup program, the diskette drive interface can be configured for the following diskette drive capacities and sizes.

- 360 KB, 5.25-inch
- 1.2 MB, 5.25-inch
- 720 KB, 3.5-inch
- 1.2 MB. 3.5-inch (driver required)
- 1.25-1.44 MB, 3.5-inch
- 2.88 MB, 3.5-inch



1-10.4 PS/2 Keyboard and Mouse Interface

PS/2 keyboard and mouse connectors are located on the back panel of the motherboard. The +5 V lines to keyboard and mouse connectors are protected with a fuse that prevents motherboard components from being damaged when an over-current condition occurs.



Note

The mouse and keyboard can be plugged into either PS/2 connector.

Power to the computer should be turned off before a keyboard or mouse is connected or disconnected.

The keyboard controller contains code, which provides the traditional keyboard and mouse control functions, and also supports Power On/Reset password protection. Power On/Reset password can be specified in the BIOS Setup program.

The keyboard controller also supports the hot-key sequence <Ctrl><Alt>, software reset. This key sequence resets the computer's software by jumping to the beginning of the BIOS code and running the Power On Self Test (POST).

1-10.5 Infrared Support

On the front panel I/O connector, there are six pins that support Hewlett Packard HSDL-1000 compatible infrared (IR) transmitters and receivers. In the Setup program, Serial Port B can be direct4d to a connected IR device. (In this case, the Serial Port B connector on the back panel cannot be used.) The IR connection can be used to transfer files to or from portable devices like laptops, PDAs, and printers. The Infrared Data Association (IrDA) specification supports data transfers of 115Kbits/sec at a distance of 1 meter.

1-11 HARDWARE MONITOR

The optional hardware monitor subsystem provides low-cost instrumentation capabilities. The features of the hardware monitor



subsystem include:

- > An integrated ambient temperature sensor
- Fan speed sensors, which monitor the fan 1 and fan 2 connector.
- Power supply voltage monitoring to detect levels above or below acceptable values

When suggested ratings for temperature, fan speed, or voltage are exceeded, an interrupt is activated. The hardware monitor component connects to the SMBus.

1-12 WAKE ON LAN TECHNOLOGY

Wake on LAN technology enables remote wakeup of the computer through a network. Wake on LAN technology requires a PCI add-in network interface card (NIC) with remote wakeup capabilities. The remote wakeup connector on the NIC must be connected to the onboard Wake on LAN connector. The NIC monitors network traffic at the Ethernet interface; upon detecting a Magic Packet, the NIC asserts a wakeup signal that powers up the computer. This feature uses the Wake on LAN connector.

CAUTION

For Wake on LAN, the 5-V standby line for the power supply must be capable of delivering $+5V \pm 5$ % at 720 mA. Failure to provide adequate standby current when implementing Wake on LAN can damage the power supply.



Chapter 2

HARDWARE INSTALLATION

Congratulations on your purchase of **SY-7VCA2** Motherboard. You are about to install and connect your new Motherboard.



Note: Do not unpack the Motherboard from its protective antistatic packaging until you have made the following preparations.

2-1 PREPARATIONS

Gather and prepare all the following hardware equipment to complete the installation successfully:

1. Socket 370 processor with built-in CPU cooling fan (boxed type).



Note: This Motherboard supports non-boxed type CPUs. The heavier CPU cooling fan requires the installation of a CPU support stand.

- 2. DIMM memory module (s)
- 3. Computer case and chassis with adequate power supply unit
- 4. Monitor
- 5. PS/2 Keyboard
- 6. Pointing Device (PS/2 mouse)
- 7. Speaker(s) (optional)
- 8. Disk Drives: HDD, CD-ROM, Floppy drive ...
- 9. External Peripherals: Printer, Plotter, and Modem (optional)
- 10. Internal Peripherals: Modem and LAN cards (optional)



2-2 UNPACKING THE MOTHERBOARD

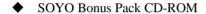
When unpacking the Motherboard, check for the following items:

◆ The SY-7VCA2 VT82C694X AGP/PCI Motherboard



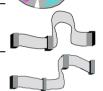
◆ The Installation CD-ROM

The Ouick Start Guide



◆ One IDE Device ATA 66 Flat Cable

◆ One Floppy Disk Drive Flat Cable





Warning: Do not unpack the Motherboard from its anti-static packaging until you are ready to install it.

Like most electronic equipment, your Motherboard may be damaged by electrostatic discharge. To avoid permanent damage to components ground yourself while working by using a grounding strap. Otherwise, ground yourself frequently by touching the unpainted portion of the computer chassis to drain the static charges.

Handle the Motherboard carefully, holding it by the edges.

You are now ready to start the installation.



2-3 INSTALLATION GUIDE

We will now begin the installation of the Motherboard. Please follow the step-by-step procedure designed to lead you to a complete and correct installation.



Warning: Turn off the power to the Motherboard, system chassis, and peripheral devices before performing any work on the Motherboard or system.

BEGIN THE INSTALLATION



2-3.1 CPU Installation

Your SY-7VCA2 motherboard comes with a CPU retention set kit. The retention set is used to hold the processor attached to the Socket 370 CPU connector on the motherboard.

Mark your CPU Frequency: Record the working frequency of your CPU that should be clearly marked on the CPU cover.

FSB 66MHz 400MHz (66 x 6.0) 466MHz (66 x 7.0) 533MHz (66 x 8.0) 600MHz (66 x 9.0) 667MHz (66 x 10.0) 433MHz (66 x 6.5) 500MHz (66 x 7.5) 566MHz (66 x 8.5) 633MHz (66 x 9.5) 700MHz (66 x 10.5) FSB 100MHz 500MHz (100 x 5.0) 600MHz (100 x 6.0) 700MHz (100 x 7.0) 800MHz (100 x 8.0) 550MHz (100 x 5.5) 650MHz (100 x 6.5) 750MHz (100 x 7.5) 850MHz (100 x 8.5) FSB 133MHz 533MHz (133x4.0) 667MHz (133x5.0) 800MHz (133x 6.0) 933MHz (133x 7.0) 1.13GHz (133x 8.5) 600MHz (133x4.5) 733MHz (133x5.5) 866MHz (133x 6.5) 1000MHz (133x 7.5)

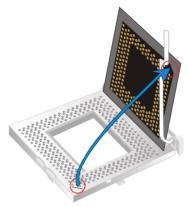
CPU Mount Procedure: To mount the Celeron $^{\text{TM}}$ processor that you have purchased separately, follow these instructions.

1. Lift the socket handle up to a vertical position.

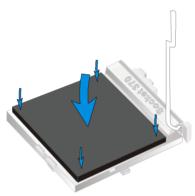




2. Align the blunt edge of the CPU with the matching pinhole distinctive edge on the socket.



3. Seat the processor in the socket completely and without forcing.



4. Then close the socket handle to secure the CPU in place.







Remember to connect the CPU Cooling Fan to the appropriate power connector on the Motherboard. *The fan is a key component that will ensure system stability. The fan prevents overheating, therefore prolonging the life of your CPU.*

2-3.1.1 CPU Fan Installation

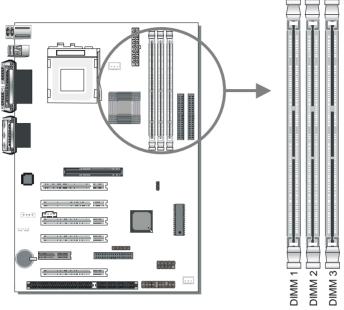
Your Socket 370 processor kit comes with a cooling fan. Mount the fan on the processor according to the instructions provided by the manufacturer. The fan is a key component that will ensure system stability. The fan prevents overheating, therefore prolonging the life of your CPU.



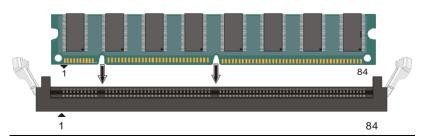
Note: Remember to connect the fan to the appropriate power source.



2-3.2 SDRAM Memory Module Installation



Your board comes with three DIMM sockets, providing support for up to 1.5GB of main memory using unbuffered and registered DIMM modules from 8MB to 512MB. On this motherboard, DRAM speed can be set independent from the CPU front side bus speed. Depending on the DRAM clock speed setting in the BIOS setup (Chapter 3), appropriate memory modules must be used. For 66MHz DRAM speed, use PC66 memory; for 100MHz DRAM speed, use PC100 memory; for 133MHz DRAM speed, use PC133 memory.





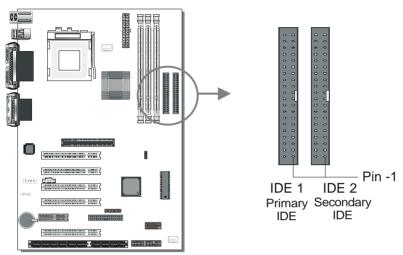
Memory Configuration Table

Number of Memory Modules	DIMM 1	DIMM 2	DIMM 3
RAM Type	SDRAM		
Memory Module Size (MB)	8/16/32/64/128/256/512 MB		



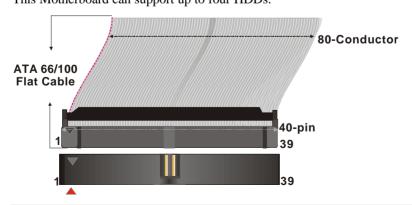
2-3.3 Motherboard Connector

2-3.3.1 IDE Device Installation (HDD, CD-ROM)



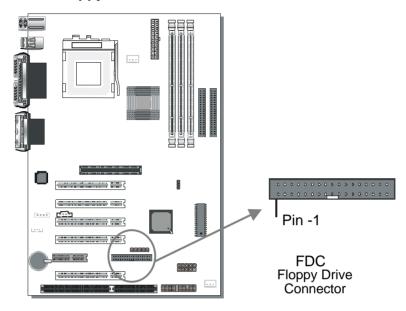
This Motherboard offers two primary and secondary IDE device connectors (IDE1, IDE2). It can support up to four high-speed Ultra DMA 33/66/100HDD or CD-ROM.

Connect one side of the ATA66/100 flat cable to the IDE device (HDD or CD-ROM) and plug the other end to the primary (IDE1) or secondary (IDE2) directionally keyed IDE connector on the Motherboard. The ATA66/100 cable is backward compatible with ATA33 HDDs. This Motherboard can support up to four HDDs.





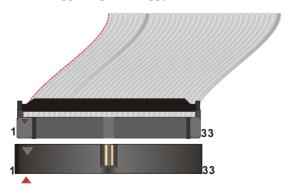
2-3.3.2 Floppy Drive Installation



The system supports 5 possible floppy drive types: 720 KB, 1.2 MB, 1.44 MB, 2.88 MB, and LS-120.

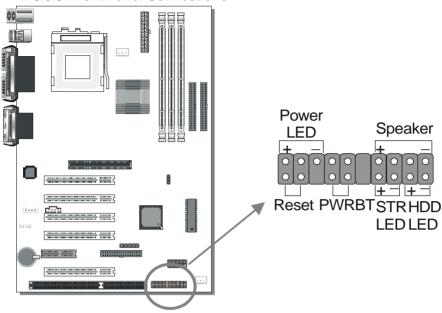
Connect one side of the 34-pin flat cable to the floppy drive and plug the other end to the floppy drive connector on the Motherboard.

This Motherboard can support up to 2 floppy drives.





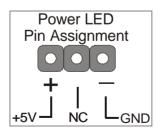
2-3.3.3 Front Panel Connections



Plug the computer case's front panel devices to the corresponding headers on the Motherboard.

1. Power LED

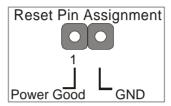
Please install according to the following pin assignment: pin 1,3 are for Power LED.





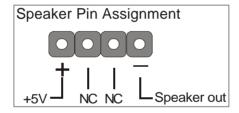
2. Reset

Plug the Reset push-button cable into the 2-pin Reset header on the Motherboard. Pushing the Reset button on the front panel will cause the system to restart the boot-up sequence.



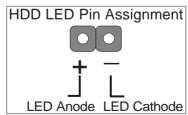
3. Speaker

Attach the 4-pin PC speaker cable from the case to the Speaker header on the Motherboard.



4. IDE LED

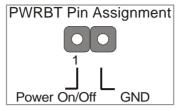
Attach the 2-pin IDE device LED cable to the corresponding IDE LED header on the Motherboard. This will cause the LED to light up when an IDE (HDD, CD-ROM) device is active.





5. ATX Power On/Off Switch

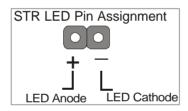
Attach the 2-pin momentary type switch to the PWRBT header for turning On or Off your ATX power supply.



6. STR LED

The STR LED is connected to the Voltage that feeds the DIMM sockets. Therefore the following table applies:

	Suspend to RAM	Normal Operation	Power Off
STR LED	ON	ON	OFF



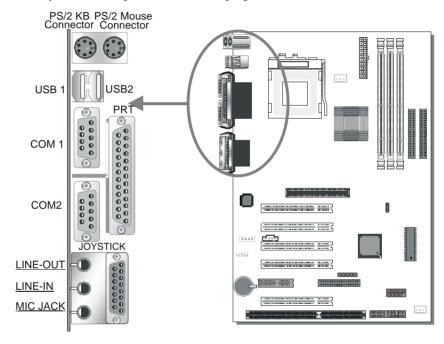


2-3.3.4 Back Panel Connections

All external devices such as the PS/2 keyboard, PS/2 mouse, printer, modem, USB can be plugged directly onto the Motherboard back panel.

Only after you have fixed and locked the Motherboard to the computer case can you start connecting the external peripheral devices.

When connecting an external device, use the following figure to locate and identify which back panel connector to plug the device to.





1. Onboard Serial Ports COM1/COM2

External peripherals that use serial transmission scheme include:

- serial mouse.
- and modem.

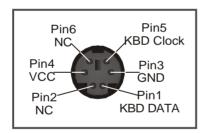
Plug the serial device cables directly into the COM1/COM2 9-pin male connectors located at the rear panel of the Motherboard.

2. Parallel Port PRT

This parallel port is used to connect the printer or other parallel devices. Plug the parallel device cable into the 25-pin female connector located at the rear panel of the Motherboard.

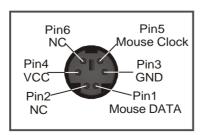
3. PS/2 Keyboard

Plug the keyboard jack directly into the 6-pin female PS/2 keyboard connector located at the rear panel of the Motherboard.



4. PS/2 Mouse

Similarly, plug the mouse jack directly into the 6-pin female PS/2 mouse connector.



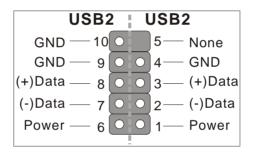


5. Universal Serial Bus USB1 (2 Ports)/USB2 (2 Ports)

This Motherboard provides four USB ports for your additional devices. Plug the USB device jack into the available USB connector USB1 or USB2.

- Standard device drivers come with the Win98 for commonly used USB devices.
- With Win95, use the flow UHCI specifications. To use USB devices under Win95, usually you have to install the device that driver comes with the USB device you have purchased.

USB2 is available. To make use of these USB ports, purchase a USB cable from your dealer. The lay-out of USB2 is as follows:



6. Onboard Joystick port/audio

This Motherboard provides Joystick port and audio.

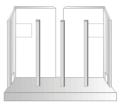
- Attach the joystick cable to the 15-pin JOYSTICK port at the rear panel of you motherboard.
- This Motherboard features three built-in audio-stereo ports (labeled line-in, line-out, and mic jack) convenient to directly plug-in all your external audio devices.



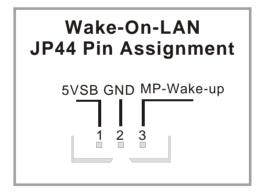
2-3.3.5 Other Connections

1. Wake-On-LAN (WOL)

Attach the 3-pin connector from the LAN card which supports the Wake-On-LAN (WOL) function to the JP10 header on the Motherboard. This WOL function lets users wake up the connected computer through the LAN card.



Please install according to the following pin assignment:





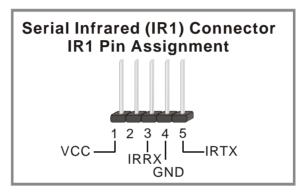
2. Infrared (IR1)

Plug the 5-pin infrared device cable to the IR1 header. This will enable the



infrared transfer function. This Motherboard meets both the ASKIR and HPSIR specifications.

Please install according to the following pin assignment:



3. Other Display Cards

Insert other types of VGA cards into the PCI or ISA expansion slots according to card specifications.

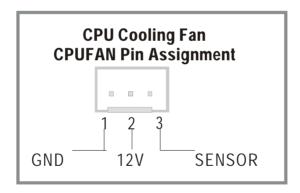


4. Cooling Fan Installation



(1) CPU Cooling Fan

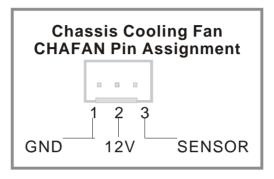
After you have seated the CPU properly on the processor, attach the 3-pin fan cable to the CPUFAN connector on the Motherboard. The fan will stop when the system enters into Suspend Mode. (Suspend mode can be enabled from the BIOS Setup Utility, [POWER MANAGEMENT] menu.) To avoid damage to the system, install according to the following pin assignment:





(2) Chassis Cooling Fan

Some chassis also feature a cooling fan. This Motherboard features a CHAFAN connector to provide 12V power to the chassis fan. Connect the cable from the chassis fan to the CHAFAN 3-pin connector. Install according to the following pin assignment:





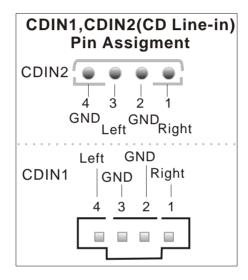
Note: CPUFAN must be installed for this Motherboard, CHAFAN is optional.



5. CD Line-in (CDIN1,CDIN2)

This Motherboard provides two CD-Line in connectors. Please connect the 4-pin audio cable from your CD-ROM drive to either CDIN1 or CDIN2. (It fits in only one, depending on the cable that came with your CD-ROM drive)

Please install according to the following pin assignment:



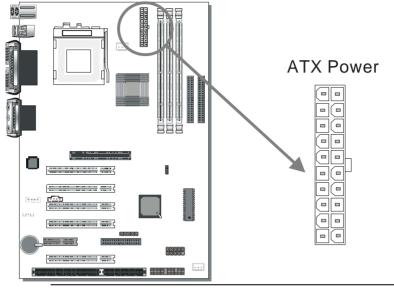
2-3.3.6 AGP VGA Card

Insert the AGP VGA card into the AGP slot. Then connect the monitor information cable to the AGP card back plane external connector. Follow the manufacturer's instructions to perform the AGP VGA drivers installation.



2-3.3.7 ATX Power Supply

Plug the connector from the power directly into the 20-pin male ATX PW connector on the Motherboard, as shown in the following figure.





Warning: Follow these precautions to preserve your Motherboard from any remnant currents when connecting to ATX power supply:

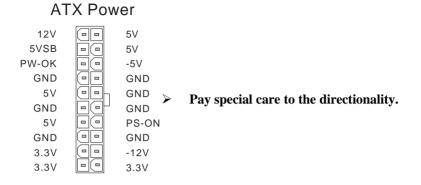
Turn off the power supply and unplug the power cord of the ATX power supply before connecting to ATX PW connector.

The Motherboard requires a power supply with at least 200 Watts and a "power good" signal. Make sure the ATX power supply can take at least 720 mA * load on the 5V Standby lead (5VSB) to meet the standard ATX specification.

* *Note:* If you use the Wake-On-LAN (WOL) function, make sure the ATX power supply can support at least 720 mA on the 5V Standby lead (5VSB).

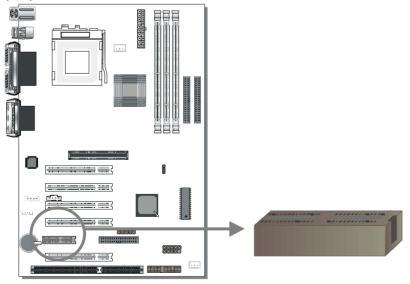


Please install the ATX power according to the following pin assignment:



2-3.3.8 AMR (Audio Modem Riser)Connector

This motherboard supports AMR (Audio-Modem Riser) slot that facilitates audio or modem riser card expansion. Since AC97 audio codec is built-in down on the motherboard, only "Secondary" mode modem riser (MR) card can be used on the AMR slot.





2-3.4 CMOS Clearing (JP5)

After you have turned off your computer, clear the CMOS memory by momentarily shorting pins 2-3 on jumper JP5, for a few seconds. Then restore JP5 to the initial 1-2 jumper setting in order to recover and retain the default settings.

Jumper JP5 can be easily identified by its white colored cap.

CMOS Clearing	Clear CMOS I	Retain CM	OS Data	
JP5 Setting	Short pin 2-3 for at least 5 seconds to clear the CMOS		Short pin 1-2 to retain new settings	1 2 3

Note: You must unplug the ATX power cable from the ATX power connector when performing the CMOS Clear operation.

2-3.5 Power On

You have now completed the hardware installation of your Motherboard successfully.

- 1. Turn the power on
- 2. To enter the BIOS Setup Utility, press the key while the system is performing the diagnostic checks,



Note: If you have failed to enter the BIOS, wait until the boot up sequence is completed. Then push the RESET button and press key again at the beginning of boot-up, during diagnostic checks.



Repeat this operation until you get the following screen.

3. The BIOS Setup screen appears:

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software				
➤ Soyo Combo Feature	▶ PC Health Status			
➤ Standard CMOS Features	Load Optimized Defaults			
➤ Advanced BIOS Features	Load Fail - Safe Defaults			
➤ Advanced Chipset Features	Set Supervisor Password			
➤ Integrated Peripherals	Set User Password			
▶ Power Management Setup	Save & Exit Setup			
▶ PnP/PCI Configurations	Exit Without Saving			
Esc : Quit $\uparrow \lor \rightarrow \leftarrow$: Select Item F10 : Save & Exit Setup				
Change CPU's Clock & Voltage				

2-3.6 Quick BIOS Setup

This Motherboard does not use any hardware jumpers to set the CPU frequency. Instead, CPU settings are software configurable with the BIOS [SOYO COMBO SETUP]. The [SOYO COMBO SETUP] menu combines the main parameters that you need to configure, all in one menu, for a quick setup in BIOS.

After the hardware installation is complete, turn the power switch on, then press the key during the system diagnostic checks to enter the Award BIOS Setup program. The CMOS SETUP UTILITY will display on screen. Follow these steps to configure the CPU settings.



Step 1. Select [STANDARD CMOS SETUP]

Set [Date/Time] and [Floppy drive type], then set [Hard Disk Type] to "Auto".

Step 2. Select [LOAD SETUP DEFAULT]

Select the "LOAD SETUP DEFAULT" menu and type "Y" at the prompt to load the BIOS optimal setup.

Step 3. Select [SOYO COMBO SETUP]

Move the cursor to the [CPU Frequency] field to set the CPU frequency.

Available [CPU Frequency] settings on your SY-7VCA2 Motherboard are detailed in the following table.

If you set this field to [Manual], you are then required to fill in the next two consecutive fields: (1) the CPU Host/PCI Clock, and (2) the CPU Ratio.

CPU Frequency (MHz)	667MHz (66 x 10.0)	600MHz (133 x 4.5)				
Manual	700MHz (66 x 10.5)	666MHz (133 x 5.0)				
400MHz (66 x 6.0)	500MHz (100 x 5.0)	733MHz (133 x 5.5)				
433MHz (66 x 6.5)	550MHz (100 x 5.5)	800MHz (133 x 6.0)				
466MHz (66 x 7.0)	600MHz (100 x 6.0)	866MHz (133 x 6.5)				
500MHz (66 x 7.5)	650MHz (100 x 6.5)	933MHz (133 x 7.0)				
533MHz (66 x 8.0)	700MHz (100 x 7.0)	1000MHz (133 x 7.5)				
566MHz (66 x 8.5)	750MHz (100 x 7.5)	1.13GHz (133 x 8.5)				
600MHz (66 x 9.0)	800MHz (100 x 8.0)					
633MHz (66 x 9.5)	850MHz (100 x 8.5)					
Select the working frequency of your Pentium® III, Celeron™ processor among these preset values.						
Note: Mark the checkbox that corresponds to the working frequency of your Pentium III,						
	CMOS configuration should be lost					

Step 4. Select [SAVE & EXIT SETUP]

Press **<Enter>** to save the new configuration to the CMOS memory, and continue the boot sequence.



2-3.7 Troubleshooting at First Start

Video (no display) related issues

I built a new computer system using a Soyo board and nothing happens when turning it on, no video and no beeps from the PC speaker. What is happening and how can it be fixed?

No screen and no beeps mean that your CPU and motherboard do not work at all. It could be that the CPU is not seated correctly or that a component on the M/B is grounded (shorted) with the case. Also make sure to check the voltage setting switch (110V/220V) on the back of the power supply. To isolate the problem do the following:

- 1. Press and hold down on the "Ins" (insert) key while turning on the computer until you get video. If you do not get video then,
- 2. Double-check jumpers setting on you motherboard and remove all add-on cards, unplug all hard-disk and floppy-disk drive cables and see if you can hear some beeps. If you still do not get any beeps, then try putting the motherboard on the table (to isolate it from the case) with the CPU and speaker only, and give it one more try.

I hear a series of beeps and I do not get anything from my monitor. What could be wrong?

The following lists some basic beep codes and their possible meanings:

- One long beep and 3 very short beeps The video card is not detected by the motherboard. Please re-seat your video card. If you are using an AGP card, please push your AGP card down real hard. You may have to push VERY hard without the AGP card mounting screw. Make sure not to insert the card the other way around.
- Continuous beeps One or more of the memory modules is not seated correctly in its socket.



My PCI VGA card works fine with my system, but when I put in a new AGP card, it does not give me any video. Is my AGP slot bad?

This is a common problem with AGP video cards. The reason is that your AGP card did not get seated into the AGP slot fully and firmly. Please push your AGP card down into the socket real hard, it should snap twice. You may have to unscrew the AGP card to allow the card to go further down. Do take care not to damage the card by using too much force.

I get distorted video my AGP card right after I save my bios. Why is that?

The cause is likely that your AGP card is not running at the correct bus speed. To fix this, please clear the CMOS via JP5 and if it still does not work, please upgrade your motherboard bios to the latest version.

BIOS Issues

Where can I find the BIOS revision of my mainboard?

It will be displayed on the up-left corner on the screen during boot-up. It will show as your board type followed by the revision number, such as 5EH_2CA1 (meaning revision 2CA1 for the SY-5EH board) or 6BA+ IV_2AA2 which means SY-6BA+ IV motherboard with 2AA2 bios.

Where can I find the latest BIOS of my motherboard?

Please go to the technical support page of one of the SOYO websites (Taiwan: www.soyo.com.tw), and look up your motherboard to find the latest BIOS revision.

Hard disk, floppy drive, CD-ROM etc

When I boot up my new computer I got "floppy boot failure" and the LED on the floppy stays on

Make sure the red wire of floppy ribbon cable goes to Pin1 on the floppy drive side (don't trust the "key lock" or "notch") and use the end-connector of the cable (don't use middle one).

Modem issues

I get an "I/O Conflict" message when I turn on my system and I can not get my modem to work



What you need to do is to disable 'COM2' (or UART2 or serial port 2) in the bios under integrated peripheral setup.

I have installed my modem drivers several times and I still cannot get my modem to work. Why?

If you are sure that the modem driver has been installed correctly, then you need to install the south bridge driver from the SOYO CD, this is because Windows does not properly recognize relatively new chipsets.

Audio Issues

I do not get any sound from my sound card. What could be wrong? Please make sure the speaker is connected to the speaker out port on your sound card.

In Device Manager, I keep getting yellow exclamation signs on my sound port even though I have installed my sound driver several times and I could not get my sound card to work. What is wrong?

It is likely that you did not have the correct driver installed. If you are sure that the correct sound driver has been installed, then please install the 'south bridge' driver for the motherboard.

The sound is working in my system, but when I play CD music from the CD-ROM, I do not get any sound. What is wrong?

This is because the 3-wire audio cable from the CD-ROM to the sound card is not connected or it is loose.

The sound from my sound card is distorted when Windows starts. What is wrong?

First, if you are using an ISA sound card, please make sure the IRQ needed for the sound card is set to 'Legacy ISA' in the bios. In other words, if your ISA sound card takes IRQ5, then set IRQ5 to 'Legacy ISA'. Next, install the 'south bridge' driver for the motherboard.

The sound and everything else works fine except that the recorder and microphone do not work. What is wrong?



This is because the recorder and microphone in the Windows are not enabled. Please go to sound properties and enable them.

Lock up (freeze)

When I boot up my system, everything works fine. It sees my CPU and memory, detects my hard drive, floppy drive and CD-ROM but locks up at "Verify DMI pool data...", and it won't go any further. What should I do?

Please clear the CMOS via JP5 then choose 'load setup default' in the bios and save the bios and exit. Next, unplug all other add-on cards except the video card and floppy drive controller, and see if it can boot from floppy. Then put back the peripherals one by one to identify which one causes the lockup. If you are running a Cyrix CPU, make sure the 'linear burst function' is enabled in the bios.

I can not get my board to run properly.

Please make sure you have the latest bios and driver from the SOYO web site at: http://www.soyo.com

2-3.8 Power Off

There are two possible ways to turn off the system:

- 1. Use the **Shutdown** command in the **Start Menu** of Windows 95/98 to turn off your computer.
- Press the mechanical power-button and hold down for over 4
 seconds, to shutdown the computer. If you press the power-button for
 less than 4 seconds, then your system will enter into Suspend Mode.



2-3.9 Debug LEDsThe Debug LEDs give an indication of the status of the system during boot-up. If the system does not boot-up properly, use the table below to find out at what point in boot-up sequence the problem arises. A x means the LED is off, a L means the LED is lit.

X		Co			Explanation
No.	X	X	X	L	Initialization of the chipset. The MCH and ICH registers will be set to the
X X L X The BIOS is now decompressing the BIOS code into shadow RAM. Most of the BIOS code is stored in the BIOS Flash ROM IC in compressed form to save space. At boot-up this code is decompressed and stored to RAM for faste execution. X X L L The checksum for the decompressed code is calculated and checked. The checksum is calculated by adding all data bytes together and storing the result in the BIOS ROM. If the calculated checksum is different from the checksum stored in the ROM, the code stored in the FLASH ROM is corrupted. X L X X The chipset (MCH and ICH) registers are set to the values selected by the user are inappropriate. Please select more conservative settings. X L L X The Hardware Monitor Functionality is initialized. The settings the user selected in the BIOS for the Hardware Monitor are written into the Hardware Monitor registers. X L L L If the VGA card does not work or is not inserted at all, the system will display this code for a short period of time. L X X X At this point the BIOS is initializing the CPU. The L2 cache latency values are set, and more important, the CPU micro-code is written into the CPU. L X X L Now the system is testing the RAM inserted on the board. The screen will show the BIOS testing the RAM inserted on the board. The screen will show the BIOS testing the RAM inserted on the board. The screen will show the amount of RAM in your system. L X L L At this point the IDE busses (primary and secondary) are scanned for devices. This includes the Serial, parallel and IR etc settings. L X L L Now the BIOS will scan the peripheral busses for add-on cards. This includes the PCI and AGP busses. If any add-on cards are detected, the resources it requires are assigned by the BIOS in accordance with user settings. L L L A The serial and parallel port registers are set to the values selected by the user. DB D3 D2 D1 ROM C. L L A At this point the BIOS will flash the new values into the BIOS Flash ROM IC. L L L L At this point the BIOS passes control to the Operating System	D8	D3	D2	D1	
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L L L At this point the BIOS passes control to the Operating System (OS). All LED	D8	D3	D2	D1	there are changes, the BIOS will flash the new values into the BIOS Flash
no local part will be on and will stay on to indicate a successful boot	L	L	L	L	At this point the BIOS passes control to the Operating System (OS). All LEDs
D8 D3 D2 D1 will be on and will stay on to indicate a successful boot.	D8	D3	D2	D1	will be on and will stay on to indicate a successful boot.



You are now ready to configure your system with the BIOS setup program. Go to *Chapter 3: BIOS SETUP*



Chapter 3

BIOS SETUP UTILITY

This Motherboard's BIOS setup program uses the ROM PCI/ISA BIOS program from Award Software Inc.

To enter the Award BIOS program's Main Menu:

- 1. Turn on or reboot the system.
- 2. After the diagnostic checks, press the [Del] key to enter the Award BIOS Setup Utility.

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software					
▶ Soyo Combo Feature	▶ PC Health Status				
➤ Standard CMOS Features	Load Fail - Safe Defaults				
➤ Advanced BIOS Features	Load Optimized Defaults				
➤ Advanced Chipset Features	Set Supervisor Password				
▶ Integrated Peripherals	Set User Password				
▶ Power Management Setup	Save & Exit Setup				
▶ PnP/PCI Configurations	Exit Without Saving				
Esc : Quit	↑↓→← : Select Item				
F10 : Save & Exit Setup					
Change CPU's Clock & Voltage					

Selecting items

- Use the arrow keys to move between items and select fields.
- From the Main Menu press arrow keys to enter the selected submenu. **Modifying selected items**
- Use the [Up]/[Down] keys to modify values within the selected fields. Some fields let you enter values directly.



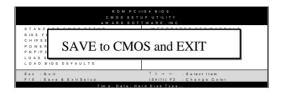
Hot Keys: Function keys give you access to a group of commands throughout the BIOS utility.

Function	Command	Description
F1	General Help	Gives the list of options available for each
1.1	General Ticip	item.
F5	Previous	Restore the old values. These are the values
F5	Values	that the user started the current session with.
F6	Load Fail-	Loads all items with the most conservative
ro	Safe Defaults	values.
	Load	
F7	Optimized	Loads all options with the optimize values.
	Defaults	
F10	Save	Saves your changes and reboots the system.
[Faal	Exit	Returns at anytime and from any location to
[Esc]	EXIL	the Main Menu.
[[Endow]	Select	Will display a overlapping window with all
[Enter]	Select	options for the current item.
		Using the +, -, Page Up and Page Down
[+/-/PU/PD]	Value	keys the user can toggle the value of the
		current item.



SAVE AND EXIT SETUP

Select the [SAVE & EXIT SETUP] option from the Main Menu to save data to CMOS and exit the setup utility. This option saves all your changes and causes the system to reboot.



Type [Y] to save the changes and exit or [N] to return to the Main Menu and keep current values.

EXIT WITHOUT SAVING

Selecting the [EXIT WITHOUT SAVING] option allows you to abandon all data and exit setup, therefore ignoring all your changes.



Type [Y] to abandon changes and exit or [N] to return to the Main Menu and keep current values.



3-1 SOYO COMBO SETUP

This Motherboard does not use any hardware jumpers to set the CPU frequency. Instead, CPU settings are software configurable with the BIOS [SOYO COMBO SETUP].

After the hardware installation is complete, turn the power switch on, then press the key during the system diagnostic checks to enter the Award BIOS Setup program. The CMOS SETUP UTILITY will display on screen. Then, select the [SOYO COMBO SETUP] option from the main menu and press the <Enter> key.

CMOS Setup Utility - Copyright (C) 1984-2000 Award Software Soyo Combo Feature CPU/PCI Frequency **HW** Default Item Help CPU Ratio X 3 Auto detect DIMM/PCI Clk Enabled Menu Level Spread Spectrum Disabled C.I.H. 4-WAY Protection Enabled CPU L2 Latency Control Default x CPU L2 Latency Value CPU L2 Cache ECC Checking Enabled Processor Number Feature Disabled Vcore Voltage Adjust Normal Quick Power On Self Test Enabled First Boot Device Floppy Second Boot Device HDD-0 Third Boot Device LS/ZIP Boot Other Device Enabled OnChip Sound Auto OnChip Modem Auto $\wedge \psi \rightarrow \leftarrow$:Move Enter:Select +/-/PU/PD:Value F10:Save **ESC:Exit** F1:General Help F5:Previous Values F6:Fail-Safe Defaults F7: Optimized Defaults

The [SOYO COMBO SETUP] menu combines the main parameters that you need to configure, all in one menu, for a quick setup in BIOS.



3-1.1 Quick CPU Frequency Setup

Quick CPU Frequency Setup	Setting	•	Description	Note
CPU Host/PCI Clock	HW Default 66/33 MHz 68/34 MHz 75/37 MHz 80/40 MHz 83/41 MHz 85/28 MHz 90/30 MHz 100/33 MHz 103/34 MHz 105/35 MHz 109/36 MHz 112/37 MHz 114/38 MHz 115/38 MHz	120/40 MHz 124/31 MHz 126/31 MHz 129/32 MHz 133/33 MHz 135/33 MHz 138/34 MHz 140/35 MHz 141/35 MHz 143/35 MHz 145/36 MHz 147/36 MHz 150/37 MHz 154/38 MHz 160/40 MHz 166/41 MHz	Select the host clock of your Slot 1 processor among thes values. Note: For the Via 694X chipset, 66 and 100 and 133 MHz host clock frequencies are acceptated. However, the system stability is not guarantee for other frequencies due to the limitations of this chipset.	
CPU Ratio	After you have multiplier for the 5.5,6,6.5,7.0,7.1 [host clock free	selected the hose CPU. Options 5,8.0]. The CPU	st clock, choose the rest are: [2, 2.5, 3., 3.5, frequency is then deand should the work & Pentium ®II proc	4, 4.5, 5, efined as ing
Auto Detect DIMM/PCI Clk	Enabled a F v a d d s	nd PCI devices	e DIMM and e devices and signal to DIMM s. When send the clock	Default
Spread Spectrum	Enabled e	This item allow nable/disable t pectrum modu	he spread	Default



3-1.2 C.I.H. 4-WAY Protection Settings

		<u>_</u>	
	Setting	Description	Note
C.I.H. 4-	Disabled	When set to enabled, the BIOS can	
WAY	Enabled	only be programmed through	Default
Protection		AWDFLASH, making sure that any	
		virus is unable to program the system	
		BIOS. Set to disable the BIOS can be	
		programmed the traditional way.	

3-1.3 L2 Cache Memory

3-1.3 LZ Cache Memory				
	Setting	Description	Note	
CPU L2	Default	If set to Default, the CPU	Default	
Latency	Manual	default value for L2 cache		
Control		latency is used. If set to manual,		
		the 'Latency Value' item will		
		allow you to specify a latency		
		value.		
	1	, and a		
CPU L2	1~15	If the item above is set to		
Latency Value		'manual', a latency value		
•		ranging from 1 to 15 can be set.		
		The smaller the number, the		
		faster the performance.		
		However, the system may		
		become unstable at higher		
		speeds.		
CDILLA Cl	Disabled	speeds.		
CPU L2 Cache	Disabled			
ECC Checking		The state of CDV	5 6 1	
	Enabled	This option activates the CPU	Default	
		L2 cache ECC checking		
		function.		
		function.		



3-1.4 Processor Number Feature

	Setting	Description	Note
Processor	Disabled	Setting this item to enabled will allow	Default
Number	Enabled	application programs to read the ID-	
Feature		code in your Pentium III CPU,	
		disabling this item will not allow any	
		program to read the CPU ID code.	

3-1.5 Vcore voltage Setup

Vcore voltage			Note
0	Setting	Description	Note
Setup			
Vcore voltage	Normal		Default
Adjust	+1.5 % +3.0 % +4.5 % +6.0 % +7.5 % +9.0 % +10.0%	The CPU notifies the board of what core it requires by its VID outputs. The on-bovoltage regulator uses the VID code to so core voltage. If the Vcore Voltage Adju t normal, the Vcore will be exactly what the code specifies. If an adjustment percentage than the VID code specifies. For instance VID code specifies 2.0V and the Vcore valuet is set to +10.0% the actual CPU V	eard et the est is set to he VID ge is e higher e the CPU Voltage
		will be 2.2V. This function should only be the CPU is running on FSB Frequencies the CPU specifications, note that SOYO guarantee system stability if this item is normal.	be used if beyond does not

3-1.6 Quick Power On Self Test

<u> </u>					
	Setting	Description	Note		
Quick Power	Disabled				
On Self Test	Enabled	Provides a fast POTS at boot-up.	Default		



3-1.7 System Boot Control Settings

System Boot	Setting	Description	Note
Control Settings			
First	Floppy	Select Your Boot Device	
/Second/Third	LS/ZIP	Priority	
Boot Device	HDD-0		
	SCSI		
	CDROM		
	HDD-1		
	HDD-2		
	HDD-3		
	LAN		
	Disabled		
Boot Other	Disabled	Select Your Boot Device	
Device	Enabled	Priority	Default
0. 01.1	D: 11 1	TTM 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
OnChip	Disabled	This item allows you to	
Sound/Modem	Auto	control the onboard AC 97	Default
		audio/ MC 97 Modem.	



3-2 STANDARD CMOS SETUP

Select the [STANDARD CMOS SETUP] option from the Main Menu and press [Enter] key.

➤ IDE Primary Slave Pr ➤ IDE Secondary Master Pr	ess Enter None ess Enter None	Menu Level	•
► IDE Secondary Slave Pr	ess Enter None ess Enter None		
Drive B No	44M, 3.5 in. one isabled		
	GA/VGA Il Errors		
	640K 30720K 31744K		

This screen allows you to modify the basic CMOS settings.

After you have completed the changes, press [Esc] key to return to the Main Menu.

3-2.1 Date & Time

	Display	Setting	Please Note
Date	mm/dd/yyyy	Type the current date	You can also the
			PUp/PDn keys to toggle
Time	hh:mm:ss	Type the current time	24-hour clock format
			3:15 PM is displayed as
			15:15:00



3-2.2 Hard Disks Type & Mode

Choose the type and mode for the hard disks that you have already installed.

Primary (Secondary) Master & Slave	Setting	Description	Note
IDE HDD Auto- Detection	Press Enter	To auto-detect the HDD's size, headon this channel	
IDE Primary Slave	Auto	BIOS detects hard disk type automatically.	Default
(User Type)	User None	User defines the type of hard disk.	
Access Mode	Auto	BIOS detects hard disk mode automatically.	Default
	Normal	Normal IDE hard disk	<528MB
	LBA	Enhanced IDE hard disk	>528MB
	Large	Large IDE hard disk (for certain hard disk)	



Note: If you have any questions on your hard disk type or mode, ask your hard disk provider or previous user for details.

3-2.3 Floppy Drives

Floppy Drives	Setting	Description	Note
Drives A & B	360KB, 5.25 in. 1.2MB, 5.25 in. 720KB, 3.5 in. 1.44MB, 3.5 in. 2.88MB, 3.5 in.		Default
	None	Not installed	
Floppy 3-Mode Support	Disabled Drive A Drive B Both	Supports 3-mode floppy diskette: 740KB/1.2MB/ 1.44MB on selected disk drive.	Default Special disk drive commonly used in Japan



3-2.4 Others Optional

	Setting	Description	Note
772 J	ECA/MCA	Calant day of the man to	D . C 14
Video	EGA/VGA	Select the video mode.	Default
	CGA 40		
	CGA 80		
	MONO		
	(Monochrome)		
Halt On	ALL Errors	When the BIOS detects system	Default
	No Errors	errors, this function will stop the	
	All, But Keyboard	system. Select which type of	
	All, But Diskette	error will cause the system halt.	
	All, But Disk/Key		



3-3 ADVANCED BIOS FEATURES

Select the [Advanced BIOS Features] option from the Main Menu and press [Enter] key.

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software Advanced BIOS Features

Anti-Virus Protection	Disabled			Item Help
CPU Internal Cache	Enabled			nem neip
External Cache	Enabled		3.6	
Swap Floppy Drive	Disabled		Menu	Level •
Boot Up Floppy Seek	Enabled			
Boot Up NumLock Status	On			
Gate A20 Option	Fast			
Typematic Rate Setting	Disabled			
x Typematic Rate (Chars/Sec)	6			
x Typematic Delay (Msec)	250			
Security Option	Setup			
OS Select For DRAM > 64M	B Non-OS2			
Report No FDD For WIN 95	Yes			
Video BIOS Shadow	Enabled			
C8000-CBFFF Shadow	Disabled			
CC000-CFFFF Shadow	Disabled			
D0000-D3FFF Shadow	Disabled			
D4000-D7FFF Shadow	Disabled			
D8000-DBFFF Shadow	Disabled			
DC000-DFFFF Shadow	Disabled			
↑↓→←:Move Enter:Select	+/-/PU/PD:Value	F10:Save	ESC:Exit	F1:General Help
F5:Previous Values	F6:Fail-Safe Defaults		F7: Op	timized Defaults

After you have completed the changes, press [Esc] key and follow the instructions on your screen to save your settings or exit without saving.



3-3.1 Virus Warning

Setting		Setting Description I	
Anti - Virus	Disabled	If set to enabled, the Paragon Anti-	Default
Protection	Enabled	Virus. Function will scan your boot	
2 2 0 0 0 0 1 0 1 2	Lindoled	drive for boot virusses. If a boot	
		virus is detected, the BIOS will	
		display a warning message.	

3-3.2 Cache Memory Options

o ole odolio memory optiono					
	Setting	Description	Note		
CPU Internal Cache	Disabled				
	Enabled	Enables the CPU's internal	Default		
		cache.			
External Cache	Disabled				
	Enabled	Enables the external	Default		
		memory.			

3-3.3 Floppy Driver Settings

5 6.6 1 16pp) 211161 Gettinige					
	Setting	Description	Note		
Swap Floppy	Disabled		Default		
Drive	Enabled	Changes the sequence of A and B			
		drives.			

3-3.4 Boot Up Floppy Seek

	or in a contract to the contra						
	Setting	Description	Note				
Boot Up Floppy	Disabled	Seeks disk drives during boot up.					
Seek		Disabling speeds boot up.					
	Enabled		Default				

3-3.5 Boot Up NumLock Status

	Setting	Description	Note
Boot Up NumLock	On	Puts numeric keypad in NumLock mode at boot-up.	Default
Status	Off	Puts numeric keypad in arrow key mode at boot-up.	



3-3.6 Gate A20 Options

	Setting	Description	Note
Gate A20	Normal	Lets chipset control GateA20.	
Options	Fast	A pin in the keyboard controller	Default
		controls GateA20.	

3-3.7 Typematic Settings					
Typematic Settings	Setting	Description	Note		
Typematic Rate Setting	Disabled	Keystrokes repeat at a rate determined by the keyboard.	Default		
	Enabled	When enables, the typematic rate and typematic delay can be selected.			
The following [Typematic Rate] and [Typematic Delay] fields are active only if [Typematic Rate Setting] is set to [Enabled]					
Typematic Rate	6 (Char/sec) 8 (Char/sec) 10 (Char/sec) 12 (Char/sec) 15 (Char/sec) 20 (Char/sec) 24 (Char/sec) 30 (Char/sec)	Choose the rate at which a character is repeated when holding down a key.	Default		
Typematic Delay	250 (msec) 500 (msec) 750 (msec) 1000 (msec)	Choose how long after you press a key down the character begins repeating.	Default		



3-3.8 Security Option

Use this feature to prevent unauthorized system boot-up or use of BIOS Setup. The following table describes the security settings.

	Setting	Description
Security Option	System	Each time the system is booted, the
		password prompt appears.
	Setup	If a password is set, the password prompt
	_	only appears when you attempt to enter the
		BIOS Setup program.

3-3.9 Other Control Options

3-3.9 Other Control Options				
Other Control Options	Setting	Description	Note	
OS Select for DRAM>64MB	OS2 Non-OS2	When using an OS2 operating system. When using another, non-OS2 operating system.	Default	
Report No FDD For WIN 95	Yes	Windows will release IRQ line 6 (normally used by the Floppy Disk Drive) after you disable your on-board FDD and set this field to [Yes]. Windows will reserve INT 6 for	Default	
	110	your FDD, whether it is disabled or not.		
Video BIOS	Disabled			
Shadow	Enabled The BIOS is shadowed in a 16K segment if it is enabled and if it has BIOS present. These 16 segments can be shadowed from ROM to RAM. BIOS shadow copies BIOS code from slower ROM to faster RAM. BIOS can then execute from RAM.		Default	



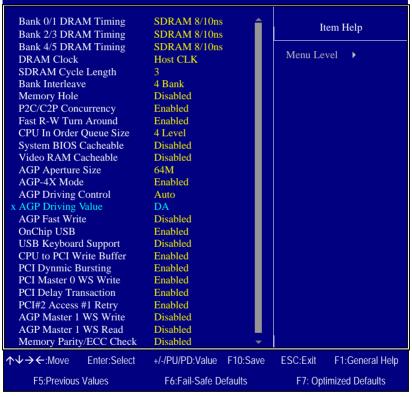
3-4 ADVANCED CHIPSET FEATURES



Caution: Change these settings only if you are already familiar with the Chipset.

The [Advanced Chipset Features] option changes the values of the chipset registers. These registers control the system options in the computer.

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software Advanced Chipset Features





After you have completed the changes, press [Esc] and follow the instructions on your screen to save your settings or exit without saving. The following table describes each field in the Advanced Chipset Features Menu and how to configure each parameter.

3-4.1 CHIPSET FEATURES SETUP

CHIPSET FEATURES	Setting	Description	Note
Bank 0/1, 2/3, 4/5 DRAM Timing	SDRAM 8/10ns SDRAM 8ns Normal Medium Fast Turbo	This item allows you to select the value in this field, depending on whether the board has paged DRAMs or EDO (extended data output) DRAMs.	Default
DRAM Clock	Host Clock HCLK-33M HCLK+33M	This item allows you to control the DRAM speed.	Default
SDRAM Cycle Length	2 3	When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not reset this field from the default value specified by the system designer.	Default
Bank Interleave	4 Bank Disabled 2 Bank	Increase DRAM performance.	Default
Memory Hole	Disabled Enabled	Some interface cards will map their ROM address to this area. If this occurs, select [Enabled] in this field.	Default
P2C/C2P Concurrency	Disabled Enabled	This item allows you to enable/disable the PCI to CPU, CPU to PCI concurrency	Default



CHIPSET FEATURES SETUP (Continued)

CHIPSET FEATURES	Setting	Description	Note
Fast R-W Turn Around	Disabled Enabled	This item controls the DRAM timing. It allows you to enable/ disable the fast read/write turn around.	Default
CPU In Order Queue Size	4 Level 1 Level	This item is related to cache performance. A queue size of 4 gives highest performance.	Default
System BIOS Cacheable	Disabled Enabled	The ROM area F0000H-FFFFH is cacheable.	Default
Video RAM Cacheable	Disabled Enabled	When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not reset this field from the default value specified by the system designer	Default
AGP Aperture Size	64M 4M, 8M, 16M, 32M, 128M, 256M.	Select the size of Accelerated Graphics Port (AGP) aperture. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation.	Default
AGP-4X Mode	Disabled Enabled	This item allows you to enable / disable the AGP-4X Mode.	Default
AGP Driving Control	Auto Manual	This item allows you to adjust the AGP driving force. Choose <i>Manual</i> to key in a AGP Driving Value in the next selection. This field is recommended to set in Auto for avoiding any error in your system.	Default



CHIPSET FEATURES SETUP (Continued)

CHIPSET FEATURES		Description	Note
AGP Driving Value	Min=0000 ~ Max=00FF	This item allows you to adjust the AGP driving force.	
AGP Fast Write	Disabled Enabled	The VIA chipset will use fast write to AGP if this item is enabled. Not all AGP cards support fast write.	Default
OnChip USB	Disabled Enabled	This should be enabled if your system has a USB installed on the system board and you want to use it. Even when so equipped, if you add a higher performance controller, you will need to disable this feature.	Default
USB	Disabled		Default
Keyboard Support	Enabled	Select <i>Enabled</i> if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.	
CPU to PCI	Disabled	When this field is <i>Enabled</i> , writes	
Write Buffer	Enabled	from the CPU to the PCI bus are buffered, to compensate for the speed differences between the CPU and the PCI bus. When <i>Disabled</i> , the writes are not buffered and the CPU must wait until the write is complete before starting another write cycle.	Default
PCI Dynmic	Disabled	When Enabled, every write	
Bursting	Enabled	transaction goes to the write buffer. Burstable transactions then burst on the PCI bus and nonburstable transactions don't.	Default



CHIPSET FEATURES SETUP (Continued)

CHIPSET FEATURES	Setting	Description	Note
PCI Master 0	Disabled		
WS Write	Enabled	When <i>Enabled</i> , writes to the PCI bus are executed with zero wait states.	Default
PCI Delay	Disabled	The chipset has an embedded 32-bit	
Transaction	Enabled	posted write buffer to support delay transactions cycles. Select <i>Enabled</i> to support compliance with PCI specification version 2.1.	Default
PCI#2 Access	Disabled	When disabled, PCI#2 will not be	
#1 Retry	Enabled	disconnected until access finishes (difault). When enabled, PCI#2 will be disconnected if max retries are attempted without success.	Default
AGP Master 1	Disabled		Default
WS Write	Enabled	When <i>Enabled</i> , writes to the AGP(Accelerated Graphics Port) are executed with one wait states.	
AGP Master 1	Disabled		Default
WS Read	Enabled	When <i>Enabled</i> , read to the AGP (Accelerated Graphics Port) are executed with one wait states.	
Memory	Disabled		Default
Parity/ECC Check	Enabled	This item enabled to detect the memory parity and Error Checking & Correcting.	



3-5 INTEGRATED PERIPHERALS



Caution: Change these settings only if you are already familiar with the Chipset.

The [INTEGRATED PERIPHERALS] option changes the values of the chipset registers. These registers control the system options in the computer. The following screen shows setup default settings.

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software **Integrated Peripherals** OnChip IDE Channel0 Enabled Item Help OnChip IDE Channel1 Enabled IDE Prefetch Mode Enabled Menu Level Primary Master PIO Auto Primary Slave PIO Auto Secondary Master PIO Auto Secondary Slave PIO Auto Primary Master **UDMA** Auto Primary Slave **UDMA** Auto Secondary Master UDMA Auto Secondary Slave UDMA Auto Init Display First PCI Slot IDE HDD Block Mode Enabled Onboard FDD Controller Enabled Onboard Serial Port 1 3F8/IRO4 Onboard Serial Port 2. 2F8/IRO3 **UART 2 Mode** Standard x IR Function Duplex Half x TX,RX inverting enable No, Yes Onboard Parallel Port 378/IRO7 Onboard Parallel Mode Normal x ECP Mode Use DMA x Parallel Port EPP Type Epp1.9 Onboard Legacy Audio Enabled Sound Blaster Disabled SB I/O Base Address 220H SB IRO Select IRO 5 SB DMA Select DMA₁ MPU-401 Disabled MPU-401 I/O Address 330-333H Game Port (200-207H) Enabled $\wedge \psi \rightarrow \leftarrow$:Move Enter:Select +/-/PU/PD:Value F10:Save **ESC:Exit** F1:General Help F5:Previous Values F6:Fail-Safe Defaults F7: Optimized Defaults



The following tables describe each field in the INTEGRATED PERIPHERALS Menu and provide instructions on how to configure the IDE controls, FDC controls, and the onboard serial and parallel ports.

3-5.1 IDE Device Controls

IDE Controls	Setting	Description	Note
On-Chip PCI IDE ➤ Primary	Disabled	Turn off the on-board IDE	
> Secondary	Enabled	Use the on-board IDE	Default
IDE > Primary Master PIO	mode 0-4	0 is the slowest speed 4 is the fastest speed	
 ➢ Primary Slave PIO ➢ Secondary Master PIO ➢ Secondary Slave PIO 	Auto	For better performance and stability, we suggest you use the Auto setting to set the HDD control timing.	Default
IDE	Disabled		
>Primary Master UDMA >Primary Slave UDMA >Secondary Master UDMA >Secondary Slave UDMA	Auto	Select Auto to enable Ultra DMA Mode support.	Default

3-5.2 Keyboard Controls

Keyboard Controls	Setting	Description	Note
Init Display First	PCI Slot	Choose which card – AGP	Default
Int Display 111st		Display card or PCI VGA card –	Deraut
		to initialize first.	

3-5.3 IDE HDD Block Mode

	Setting	Description	Note
IDE HDD Block Mode	Disabled		
	Enabled	Invokes multi-sector transfer instead of one sector per transfer. Not all HDDs support this function.	Default



3-5.4 FDC Controls

FDC Controls	Setting	Description	Note
Onboard FDD	Disabled	Turn off the on-board	
controller		floppy controller	
	Enabled	Use the on-board floppy	Default
		controller	

3-5.5 Onboard Serial Ports

Onboard Serial Ports	Setting	Description	Note
Onboard Serial Port 1 / Serial Port 2	Disabled 3F8/IRQ4 2F8/IRQ3 3E8/IRQ4 2E8/IRQ3 Auto	Choose serial port 1 & 2's I/O address. Do not set port 1 & 2 to the same address except for Disabled or Auto.	Default (port 1) Default (port 2)
UART 2 Mode If [UART Mode S	Standard HPSIR ASKIR	The second serial port offers these InfraRed interface modes.	Default
ii [OAKI Wode 5	ciccij is set to [ii		
IR Function Duplex	Half Full	Choose [Half] or [Duplex] to set UR2 in half duplex mode or full duplex mode respectively. Refer to your IR device specifications to select the suitable mode.	Default
TX,RX inverting enable	No, Yes No, No/ Yes, No/ Yes, Yes.	This item allow you to enable the TX, RX inverting which depends on different H/W requirement. This field is not recommended to change its default setting for avoiding any error in your system	Default



3-5.6 Onboard Parallel Ports

Onboard Parallel	Setting	Description	Note
Ports	Ö	•	
Onboard Parallel	Disabled	Choose the printer I/O	
Port	378/IRQ7	address.	Default
	3BC/IRQ7		
	278/IRQ5		
Parallel Port Mode	Normal	The mode depends on your	Default
	EPP	external device that	
	ECP	connects to this port.	
	ECP+EPP		
If [Parallel Port Mode] is	s set to [ECP] mo	ode	
ECP Mode use	3	Choose DMA3	Default
DMA	1	Choose DMA1	
If [Parallel Port Mode] is	s set to [EPP] mo	ode	
Parallel Port EPP	EPP 1.9	Select EPP port type 1.9	
TYPE	EPP 1.7	Select EPP port type 1.7	Default

3-5.7 Onboard Legacy Audio

This field controls the onboard legacy audio.

	Setting	Description	Note
Onboard	Disabled	Set this item to Enabled if using	
Legacy Audio	Enabled	software (like DOS games) that needs a 'legacy' audio device.	Default
Sound Blaster	Disabled	Set this item to enabled if your on-	Default
	Enabled	board audio-chip is sound Blaster compatible.	
SB I/O Base Address	220H	Select the base address for your sound Blaster (SB) compatible Audio IC.	Default
SB IRQ Select	IRQ 5	Select the IRQ your SB Audio IC uses.	Default
SB DMA Select	DMA1	Select the DMA channel your SB Audio IC uses.	Default



Onboard Legacy Audio (Continued)

This field controls the onboard legacy audio.

	Setting	Description	Note
MPU-401	Disabled	Set this item to enabled to make use	Default
	Enabled	of MIPI.	
MPU-401 I/O	330-333H	Select the I/O address for your MIDI	Default
Address	330-303H	functionality.	
	310-313H		
	320-323H		
Game Port	Disabled	Set this item to enabled to make use	
(200-207H)	Enabled	of the game port.	Default



3-6 POWER MANAGEMENT SETUP

The [POWER MANAGEMENT SETUP] sets the system's power saving functions.

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software Power Management Setup

		0 1	
+	Power Management	Press Enter	Item Help
	ACPI Suspend Type PM Control by APM Video Off Option Video Off Method MODE Use IRQ Soft-Off by PWRBTN	S1 (POS) Yes Suspend -> Off V/H SYNC+Blank 3 Instant-Off	Menu Level ▶
	State After Power Failure Wake Up Events	Off Press Enter	
\uparrow	·→←:Move Enter:Select	+/-/PU/PD:Value F10:Save	ESC:Exit F1:General Help
F5:Previous Values		F6:Fail-Safe Defaults	F7: Optimized Defaults

CMOS Setup Utility – Copyright (C) 1984-1999 Award Software Wake Up Events

VGA	OFF	Item Help
LPT & COM HDD & FDD	LPT/COM ON	Menu Level ▶
PCI Master	OFF	
PowerOn by PCI Card Modem Ring Resume	Disabled Disabled	
RTC Alarm Resume	Disabled	
Primary INTR IRQs Activity Monitoring	ON Press Enter	
↑↓→←:Move Enter:Select	+/-/PU/PD:Value F10:Save	ESC:Exit F1:General Help
F5:Previous Values	F6:Fail-Safe Defaults	F7: Optimized Defaults



After you have completed the Power Management Setup, press [Esc] to return to the Main Menu.

3-6.1 Power Management Controls

Power	Setting	Description			Note	
Management Controls						
Power Management		Lets you d			Default	
		Doze timer	Standby timer	timer	HDD power down	
	Min Saving Max Saving	1 Hour 1 Min	1 Hour 1 Min	1 Hour 1 Min	15 Min 1 Min	
PM Control by APM	Yes NO	When enabled, an Advanced Power Management device will be activated to enhance the Max. Power Saving mode and stop the CPU internal clock. If Advance Power Management (APM) is installed on your system, selecting Yes gives better power savings.				
Video Off Option	Suspend> Off Always On All Modes > Off	When ena allows the operate in	Default			
Video Off Method	V/H Sync+Blank Blank screen DPMS	Selects the monitor is		y which the	Default	



Power Management Controls (Continued)

Power Management Controls	Setting	Description	Note
MODEM Use IRQ	3 3-11, NA	Assigns an IRQ# to the modem device.	Default
Soft-Off by	Instant-off		Default
PWR-BTTN	Delay 4 Sec.	Turns off the system power 4 seconds after pushing the power button.	
State After Power Failure	On	The system will switch on when power comes back after a power failure.	
	Off	The system will remain off when power comes back after a power failure.	Default
	Auto	The system will return to the state it was in before the power failure when power returns. (i.e: If the system was on, it will switch on again, if it was off, it will remain off)	
Wake Up Events	Press Enter	Select items that will wake up your system when in one of sleep modes. Press enter to go the select item page.	

Wake Up Events	Setting	Description	Note
VGA	OFF		Default
	ON	You can set the VGA awakens the system.	
LPT & COM	LPT/COM	When On of LPT & COM, any	
	NONE, LPT,	activity from one of the listed system peripheral devices or IRQs	Default
	COM	wakes up the system.	



Wake Up Events	Setting	Description	Note
HDD & FDD OFF ON		When <i>On of HDD</i> & FDD, any activity from one of the listed system peripheral devices wakes up the system.	Default
PCI Master	When On of PCI Master, any activity from one of the listed system peripheral devices wakes up the system		
PowerOn by PCI Card	Disabled Enabled	If enabled any PCI interrupt will wake up the system.	Default
Modem Ring Resume	Disabled Enabled	An input signal on the serial Ring Indicator (RI) line (in other words, an incoming call on the modem) awakens the system from a soft off state.	Default
Resume by Alarm Disabled Enabled		The system ignores the alarm. Set alarm to power on the system by the date (1-31) or time (hh:mm:ss). If the date is set to [0], the system will self-power on by alarm everyday at the set time.	Default
Primary INTR	ON OFF	When set to <i>On</i> , any event occurring at will awaken a system which has been powered down.	Default
IRQs Activity Monitoring (Press Enter)	Primary	IRQ3(COM2), IRQ4(COM1), IRQ5(LPT2), IRQ6(Floppy Disk), IRQ7(LPT1), IRQ12(PS/2 mouse), IRQ13(Coprocessor), IRQ14(HardDsik)	
	Secondary	IRQ9(IRQ2 Redir), IRQ10(Reserved), IRQ11(Reserved)	
	Disabled	IRQ8 (RTC Alarm), IRQ15 (Reserved)	



3-7 PNP/PCI CONFIGURATION SETUP

This option sets the Motherboard's PCI Slots.

CMOS Setup Utility - Copyright (C) 1984-2000 Award Softwa	re
PnP/PCI Configurations	

PNP OS Installed Reset Configuration Data	NO Disabled	Item Help
Resources Controlled By x IRQ Resources x DMA Resources	Auto (ESCD) Press Enter Press Enter	Menu Level →
PCI/VGA Palette Snoop Assign IRQ For VGA Assign IRQ For USB	Disabled Enabled Enabled	
INT Pin 1 Assignment INT Pin 2 Assignment INT Pin 3 Assignment INT Pin 4 Assignment	Auto Auto Auto Auto	
↑↓→←:Move Enter:Select	+/-/PU/PD:Value F10:Save	ESC:Exit F1:General Help
F5:Previous Values	F6:Fail-Safe Defaults	F7: Optimized Defaults



Note: Starred (*) items will disappear when the [Resources Controlled By] option is set to [Auto].

After you have completed the PCI Slot Configuration, press [Esc] and follow the instructions on your screen to save your settings or exit without saving.



3-7.1 PNP/PCI Configuration Controls

PNP/PCI Controls	Setting	Description Note			Note	
PnP OS Installed	Yes	are	this field to [Yes] if you running Windows 95, ich is PnP compatible.			
	No	doe	he OS you are running es not support PnP nfiguration.		Default (If there is any doubt, set this field to [No])	
Reset Configuration	Disabled		tain PnP configuration a in BIOS.		Default	
Data	Enabled	Reset PnP configuration data in BIOS.				
Resources Controlled By	Manual	car	OS does not manage PC d IRQ assignment.			
	ISA PnP m IRQ-3,4,5,	Quires to assign IRQ-# and DMA-# to PCI or A PnP manually. Q-3,4,5,7,9,10,11,12,14,15 assigned to: _ MA-0,1,3,5,6,7 assigned to: _				
	Auto (ESCD)	The Plug-and-Play BIOS Recommended				
If [Resources Co	ntrolled By] is	set to [Manual]			
IRQ-# and DMA-# assigned to:	PCI/ISA P	I/ISA PnP Choose IRQ-# and DMA-# assigned to PCI/ISA PnP card. IRQ-3,4,5,7,9 11,12,14,15 DMA-0,1,3,5,				
	Legacy ISA	DMA-# assigned to 11,12,			Q-3,4,5,7,9,10, ,12,14,15 MA-0,1,3,5,6,7	
Under this item the	user can ass	ion	an IRO to a PCI slot. Howe	_		

Under this item the user can assign an IRQ to a PCI slot. However, there under some conditions the IRQ will not be assigned as selected under this item:

- 1. IRQs 0, 1, 2, 6, 8, 13 can NOT be assigned, because they are fixed.
- 2. IRQs 5, 9, 10, 11 are available
- 3. IRQs 3,4,7,12,14 and 15 will only be assigned if they are free. See the table below on how to free them:



PNP/PCI Configuration Setup (Continued)

PNP/PC	I	Setting	Desc	cription	Note	
Setup						
Interrupt	How to set the BIOS to release the IRQ to the PnP Interrupt pool:					
Line	PnP / I	PCI configura	ation	Integrated Peripherals		
IRQ 15	IRQ 1:	5: PCI / IS	A PnP	On-Chip Secondary PC	IDE: disabled	
IRQ 14	IRQ 14	4: PCI / IS	A PnP	On-Chip Primary PCI II	DE: disabled	
				Interrupt 12 will be rele	ased by the PnP	
IRQ 12	IRQ 1	2: PCI/IS	A PnP	BIOS automatically if th	ne PS/2 Mouse Port	
				is not used.		
IRQ 7	IRQ 7:	PCI / IS	A PnP	Onboard parallel port:	disabled	
IRQ 4	IRQ 4:	PCI / IS.	A PnP	Onboard Serial port 1:	disabled	
IRQ 3	IRQ 3:	PCI / IS.	A PnP	Onboard Serial port 2:	disabled	
				errupt to a PCI slot after	BIOS passes control	
to the	OS, esp	pecially if you	ı use W	indows 95, 98 or NT.		
Assign I	RO	Disabled	BIOS v	will assign IRQ for		
For				JSB port.		
VGA/US	SB	Enabled	BIOS v	won't assign IRQ for	Default	
			VGA/USB port.			
5. Your OS may reassign another interrupt to a PCI slot after BIOS passes control						
to the OS, especially if you use Windows 95, 98 or NT.						
INT Pin			Set to Auto the BIOS will Default			
1/2/3/4			using	IRQs Automatically.		
Assignm	ent					



3-7.2 MULTI I/O ADDRESSES

Default settings for multi-I/O addresses are as follows:

Port	I/O Address	IRQ	Status
LPT1	378H	7	ECP/EPP
COM1	3F8H	4	
COM2	2F8H	3	



Warning: If a default I/O address conflicts with other I/O cards such as sound card, you must change one of the I/O addresses to remedy to this address conflict. (I/O addresses can be adjusted from the BIOS Setup Utility)

3-8 PC HEALTH STATUS

This option sets the Motherboard's PC Health Status.

CMOS Setup Utility – Copyright (C) 1984-2000 Award Software PC Health Status

		1 0 11001111		
Current CPU		25 °C / 77 ° 40 °C / 104		Item Help
Current CHA Vcore 2.5V 3.3V	JFAN Speed AFAN Speed	5192 RP 0 RP 2.02 V 2.50 V 3.31 V	Menu	Level >
5V 12V		4.97 V 11.70 V		
↑↓→←:Move F5:Previous	Enter:Select	+/-/PU/PD:Value F6:Fail-Safe D	ESC:Exit	F1:General Help



Note: Starred (*) items will disappear when the [Resources Controlled By] option is set to [Auto].



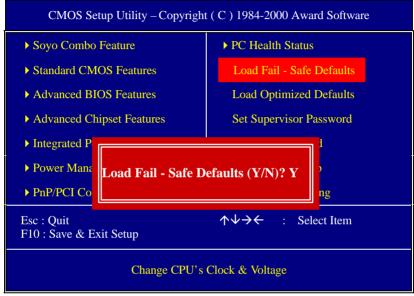
3-8.1 CPU Device Monitoring

CPU Device	Setting	Description	Note
Monitoring		•	
		G1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
Current CPU Temp.	°C/°F	Show the current status of CPU temperature.	
Current System Temp.	°C/°F	Show the current status of the system temperature.	
Current CPUFAN Speed	°C/°F	Show the current status of CPU Fan	
Current CHSFAN Speed	°C/°F	Show the current status of the chassis Fan	
Vcore, 2.5V, 3.3V, +5V, +12V	V	Show the current voltage status.	



3-9 LOAD FAIL-SAFE DEFAULTS

Select the [Load Fail-Safe Defaults] option from the Main Menu to load the system values you have previously saved. This option is recommended if you need to reset the system setup and to retrieve the old values.



Type [Y] to use the Setup Defaults followed by [Enter] or otherwise [N] to return to the Main Menu and keep current values.

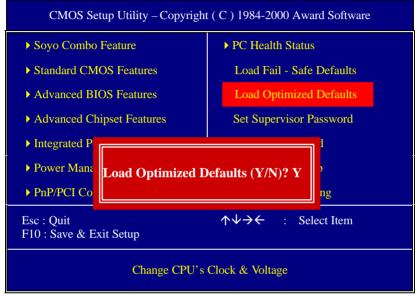


Warning: If you run into any problem after changing the BIOS configuration, please load the SETUP DEFAULTS for stable performance.



3-10 LOAD OPTIMIZED DEFAULTS

Select the [Load Optimized Defaults] option from the Main Menu to load the system values you have previously saved. This option is recommended if you need to reset the system setup and to retrieve the old values.



Type [Y] to use the Setup Defaults followed by [Enter] or otherwise [N] to return to the Main Menu and keep current values.



Warning: If you run into any problem after changing the BIOS configuration, please load the SETUP DEFAULTS for stable performance.



3-11 SUPERVISOR PASSWORD

Based on the setting you have made in the [Security Option] of the [BIOS FEATURES SETUP] section, the password prevents access to the system or the setup program by unauthorized users. Follow this procedure to set a new password or disable the password:

- 1. Choose [BIOS FEATURES SETUP] in the Main Menu and press [Enter]. Select the [Security Options] item and set the field to:
 - a. [System]: The password is required every time the system is booted. This means only a person who knows the password can use this computer.
 - b. [Setup]: The password is required only when you attempt to enter the BIOS Setup program.
- 2. Choose [SUPERVISOR PASSWORD] from the Main Menu and press [Enter]. The following prompt appear:

Enter Password:



Warning: If you forget or lose the password, the only way to access the system is to set jumper JP5 to clear the CMOS RAM. All setup information is lost and you must run the BIOS setup program again.



Note: If you do not wish to use the password function, press [Enter] directly and the following message appears:

Password Disabled!!

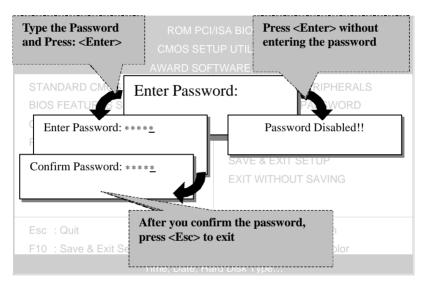


3. Enter your new password and press [Enter]. The following message appears, prompting to confirm the new password:

Confirm Password:

4. Re-enter your password and then press [Enter] to exit to the Main Menu.

This diagram outlines the password selection procedure:



3-12 USER PASSWORD

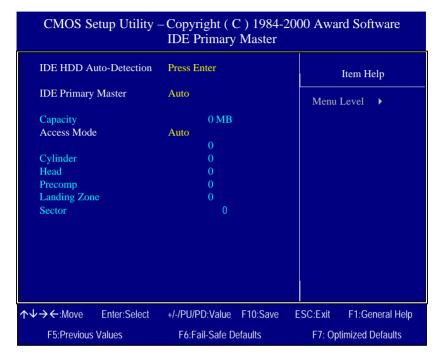
When the user password option is on, you are not allowed to change any setting in the [CMOS SETUP UTILITY] except for changing the user's password.

The password setting procedure is similar to that for the [SUPERVISOR PASSWORD] (Refer to section 3-9).



3-13 IDE HDD AUTO DETECTION

This Main Menu function automatically detects the hard disk type and configures the [Standard CMOS Features] accordingly.





Note: This function is only valid for IDE type of hard disk drives.



Chapter 4

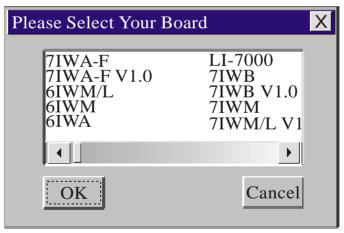
DRIVERS INSTALLATION

Your SY-7VCA2 Motherboard comes with a CD-ROM labeled "SOYO CD." The SOYO CD contains the user's manual file for your new Motherboard, the drivers software available for installation, and a database in HTML format with information on SOYO Motherboards and other products.

Step 1. Insert the SOYO CD into the CD-ROM drive

The SOYO CD will auto-run, and the SOYO CD Start Up Menu will be as shown.

If you use Windows NT, the SOYO-CD will not detect your motherboard type. In that case the following dialog will pop up, please choose your motherboard and press OK. Now the SOYO-CD Start Up Menu will be shown.



(SOYO CD Start Up Program Menu)



If you use Windows 95 or 98, the SOYO CD Start Up Program automatically detects which SOYO Motherboard you own and displays the corresponding model name.



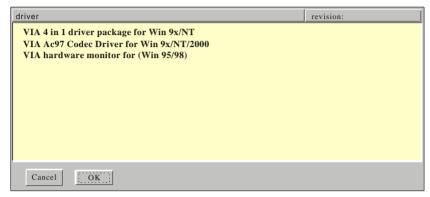
The user's manual files included on the SOYO CD are in PDF (Postscript Document) format. In order to read a PDF file, the appropriate Acrobat Reader software must be installed in your system.

Note: The Start Up program automatically detects if the Acrobat Reader utility is already present in your system, and otherwise prompts you on whether or not you want to install it. You must install the Acrobat Reader utility to be able to read the user's manual file. Follow the instructions on your screen during installation, then once the installation is completed, restart your system and re-run the SOYO CD.



Step 2. Install Drivers and Utilities

Click the *Install Drivers* button to display the list of drivers software that can be installed with your Motherboard. The Start Up program displays the drivers available for the particular model of Motherboard you own. We recommend that you only install those drivers.



(Driver Installation Menu)

A short description of all available drivers follows:

> VIA 4in1 driver package

The 4 in 1 driver package includes all drivers your motherboard needs. After selecting this driver package, one driver will be installed automatically (the IRQ remapping utility), the other three are installed if selected. By default all three drivers are selected. A description of the 4 drivers follows:

—Bus Master PCI IDE Driver

This driver will speed up the data-transfer rate to and from the harddisk.

—AGP VxD Driver

This driver must be installed in order to be able to make use of the on-board AGP Video functionality.



-VIA Chipset Functions Registry

This driver will make the necessary changes to the Windows registry, in order to make sure that Windows has no problems recognizing your VIA chipset.

—IRQ remapping utility (This driver is installed automatically) This utility will remap the IRQ lines to make sure that everything functions properly under Windows.

VIA Ac97 Codec Driver for Win 9x/NT/2000

You have to install the drivers before installing any application for the Ac97 codec.

> VIA hardware monitor for (Win 95/98)

Your motherboard comes with a hardware monitoring IC. By installing this utility Temperature, Fan speed and Voltages can be monitored. It is also possible to set alarms when current system values exceed or fall below pre-set values.

Step 3. Select which driver you want to install and click OK

- Notice 1: Once you have selected a driver, the system will automatically exit the SOYO CD to begin the driver installation program. When the installation is complete, most drivers require to restart your system before they can become active.
- **Notice 2:** You may click **Cancel** to abort the driver installation and return to the main menu

Step 4. Check the Latest Releases

Click the 'Check the latest Releases' button to go the SOYO Website to automatically find the latest BIOS, manual and driver releases for your motherboard. This button will only work if your computer is connected to the internet through a network or modem connection. Make sure to get your modem connection up before clicking this button.

