

SY-7VBA 133 Motherboard

Pentium®III & CeleronTM

Processor supported

Via 693A AGP/PCI Motherboard

66/100/133 MHz Front Side Bus supported

ATX Form Factor

User's Manual

SY-7VBA 133

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About This Guide:

This Quick Start Guide can help system manufacturers and end users in setting up and installing the Motherboard. Information in this guide has been carefully checked for reliability; however, to the correctness of the contents there is no guarantee given. The information in this document is subject to amend without notice.

For further information, please visit our **Web Site** on the Internet. The address is "http://www.soyo.com.tw".

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7VBA 133 SERIAL

FC Tested To Comply
With FCC Standards
FOR HOME OR OFFICE USE

100% POST CONSUMER RECYCLED PAPER

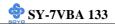
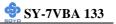


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Chapter 1

MOTHERBOARD DESCRIPTION

1-1 INTRODUCTION

The **SY-7VBA 133** AGP/PCI Motherboard is a high-performance Socket 370 supported ATX form-factor system board. The **SY-7VBA 133** uses VIA Chipset technology and supports Socket 370 class processors. This Motherboard is fully compatible with industry standards and adds many technical enhancements.

1-2 KEY FEATURES

> CPU SUPPORT

The SY-7VBA 133 supports a wide range of INTEL CPUs:

■ FSB 133MHz: Intel Pentium ® III 533B~866 MHz

■ FSB 100MHz: Intel Pentium ® III 500E~800 MHz

■ FSB 66MHz: Intel Celeron ® 400~533 MHz

New released Intel Socket 370 CPUs will very likely be supported by the SY-7VBA 133 as well.

> CPU SETTINGS

The SY-7VBA 133 provides the user with a very complete and convenient CPU setting environment. The CPU settings are all adjusted through the special SOYO COMBO page in the BIOS, therefore rendering the use of jumpers obsolete.

■ CPU FSB Frequency

The SY-7VBA 133 supports an incredible wide range of CPU FSB frequency settings:

66, 75, 81, 83, 90, 95, 100, 105, 110, 112, 113, 115, 117, 118, 120, 122, 124, 126, 133, 135, 137, 138, 140, 142, 144, 150 and 155 MHz.

This ensures that the SY-7VBA 133 has an overwhelming overclocking potential.



■ CPU Multiplier

The SY-7VBA 133 supports a wide range of multipliers:

2.0x, 2.5x, 3.0x, 3.5x, 4.0x, 4.5x, 5.0x, 5.5x, 6.0x, 6.5x, 7.0x, 7.5x and 8.0x

EXPANDABILITY

The SY-7VBA 133 provides all the standard expansion slots, and many more additional expansion features:

♦ Expansion slots

- 1 x 32-bit bus master AGP slot
- 5 x 32-bit bus master PCI slots
- 2 x 16-bit ISA slots

♦ Enhanced IO

- Floppy disk controller
- 2x EIDE controllers with support for up to 4 Ultra DMA 33/66 devices
- Standard/EPP/ECP parallel port
- 2x 16550 compatible serial ports
- IrDA compatible infrared port
- 4x USB (Universal Serial Bus) connectors
- PS/2 mouse connector
- PS/2 keyboard connector



> ADVANCED FUNCTIONS

The SY-7VBA 133 supports advanced functions such as:

■ Wake-On-LAN

Supports Wake-On-LAN (Some advanced network cards can wake the system up over the network, the WOL connector is provided by the SY-7VBA 133 to support this function).

■ Multiple boot

The SY-7VBA 133 supports booting from devices such as CD-ROM.

Power on by modem or alarm

If the SY-7VBA 133 system is in suspend mode, it can be switched back on through the modem or RTC alarm through this function. This opens a lot of possibilities, such as remote access that switches the system on only after the modem receives a call.

> FAIL SAFE

The SY-7VBA 133 comes with added functionality to make managing the system easy and safe

♦ Hardware Monitor

The integrated Hardware Monitor IC and Hardware doctor software enables the user to monitor system voltages, temperatures and FAN speeds. This makes sure that the user is full control of the system

♦ Power Failure Resume Function

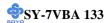
This function can be set in the BIOS, and determines whether the system will automatically turn on again after a power failure. This function is indispensable for server systems that need to always be on line.

♦ 5VSB Indicator

This green LED gives an indication of the presence of the 5V Standby voltage. This voltage is always fed into the motherboard and is used for functions such as WOL.

NORTON SOFTWARE PACK

- Antivirus
- Ghost



■ Virtual Drive

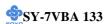
> COMPLIANCE

The SY-7VBA 133 complies with all important industry standards. The following underlines the reliability of the SY-7VBA 133, a motherboard to trust.

■ PC99, ACPI compliant

> USER FRIENDLY

- SOYO Combo Setup
- Jumperless design
- You can set up the following options trough the BIOS setting
 - CPU FSB frequency
 - CPU multiplier
 - CPU Vcore voltage
 - PCI clock
 - AGP Clock
 - SDRAM Clock



1-3 HANDLING THE MOTHERBOARD

To avoid damage to your Motherboard, follow these simple rules while unpacking:

- ➤ Before handling the Motherboard, ground yourself by grasping an unpainted portion of the system's metal chassis.
- Remove the Motherboard from its anti-static packaging. Hold the Motherboard by the edges and avoid touching its components.
- Check the Motherboard for damage. If any chip appears loose, press carefully to seat it firmly in its socket.



Warning: Do not apply power if the Motherboard appears damaged. If there is damage to the board, contact your dealer immediately.

1-4 ELECTROSTATIC DISCHARGE PRECAUTIONS

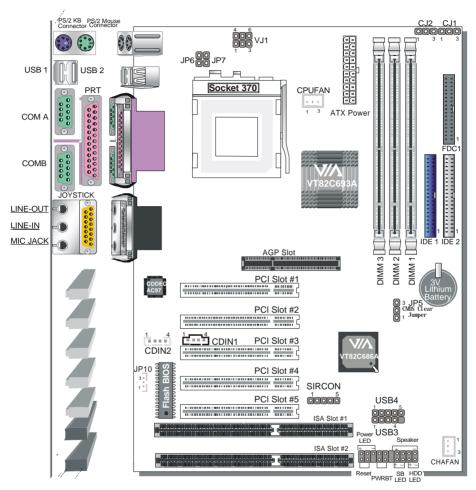
Make sure to ground yourself before handling the Motherboard or other system components. Electrostatic discharge can easily damage the components. Note that you must take special precautions when handling the Motherboard in dry or air-conditioned environment.

To protect your equipment from electrostatic discharge, take the following precautions:

- > Do not remove the anti-static packaging until you are ready to install.
- Ground yourself before removing any system component from its protective anti-static packaging. (To ground yourself, grasp the expansion slot covers or other unpainted portions of the computer chassis.)
- Frequently ground yourself while working or use a grounding strap.
- ➤ Handle the Motherboard by its edges and avoid touching its components.

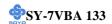


1-5 SY-7VBA 133 MOTHERBOARD LAYOUT



Back Panel

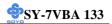
SY-7VBA 133 Platform



1-6 SY-7VBA 133 MOTHERBOARD COMPONENTS 00000 -K Х L М W-N 0 00000 Q R Ś



- A CPU AJ33 and AJ31 Setting Jumper (JP6, JP7)
- **B** Socket 370 Connector
- C CPU Core Voltage Adjust Jumper
- D CPU Cooling Fan Connector
- E Via 693A North Bridge chip
- F ATX Power Supply Connector
- G DIMM Banks
- H CPU AJ33 and AJ31 Setting Jumper (CJ1, CJ2)
- I Floppy Disk Drive (FDD) Port
- J Bus Mastering e-IDE/ATAPI Ports
- K 32-bit AGP Slot
- L 3V Lithium Battery
- M CMOS Clear Jumper
- N Via 686A South Bridge Chip
- O 32-bit PCI Mastering Slots
- P Standard Infrared (IrDA) Device Header
- Q USB Ports
- R Chassis Cooling Fan
- **S** Front panel connectors
- T 16-bit ISA Slots
- U Flash BIOS
- V Wake-On-LAN (WOL) Header
- W CD-IN Connectors
- X AC97 Codec Chip
- Y Back panel Connectors



1-7 MICROPROCESSOR

The motherboard supports a single Socket 370 processor. The processor's VID pins automatically program the voltage regulator on the motherboard to the required processor voltage. In addition, the front side bus speed (100 MHz and 133 MHz) is automatically selected. The motherboard supports all current Socket 370 processor speeds, voltages, and bus frequencies.

1-7.1 Microprocessor Packaging

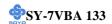
The CPU is packaged in a 370 pin PGA package. A fan must be used to ensure adequate cooling.

1-7.2 Second Level Cache

The second-level cache is located on the substrate of the CPU package. The cache includes 0KB/128KB or 256 KB of synchronous pipelined burst static RAM. All supported onboard memory can be cached.

1-7.3 Microprocessor Upgrades

The motherboard can be upgraded with Socket 370 processors that run at higher speeds. When upgrading the processor, use the BIOS configuration mode to change the processor speed.



1-8 MEMORY

1-8.1 Main Memory

The motherboard has three DIMM sockets. SDRAM can be installed in one, two, or three sockets. Using the serial presence detect (SPD) data structure, programmed into an EPROM on the DIMM, the BIOS can determine the SDRAM's size and speed. Minimum DIMM memory size is 8 MB; maximum DIMM memory size is 256/512 MB. Memory size and speed can vary between sockets.

The motherboard supports the following memory features:

- DRAM interface synchronous with host CPU (66/100/133MHz) or AGP (66MHz) for most flexible configuration
- DRAM interface may be faster than CPU by 33 MHz to allow use of PC100 memory modules with 66MHz Celeron or use of PC133 with 100MHz Pentium III
- DRAM interface may be slower than CPU by 33 MHz to allow use of older memory modules with newer CPUs (e.g., PC66 memory modules with 100MHz Pentium III)
- Concurrent CPU, AGP, and PCI access
- Supports FP,EDO, SDRAM, ESDRAM and VCM SDRAM memory types
- Different DRAM types may be used in mixed combinations
- Different DRAM timing for each bank
- Dynamic Clock Enable (CKE) control for SDRAM power reduction in high speed system.
- Mixed 1M/2M/4M/8M/16M/32MxN DRAMs
- The 7VBA 133 supports 3 DIMMs or 6 banks at 133MHz for 1.5GB max memory.
- Flexible row and column addresses
- 64-bit data width only
- 3.3V DRAM interface with 5V-tolerant inputs
- Programmable I/O drive capability for MA, command, and MD signals



- Dual copies of MA signals for improved drive
- Optional bank-by-bank ECC (single-bit error correction and multibit error detection) or EC(error checking only) for DRAM integrity
- Two-bank interleaving for 16Mbit SDRAM support
- Two-bank and four bank interleaving for 64Mbit SDRAM support
- Independent SDRAM control for each bank
- Seamless DRAM command scheduling for maximum DRAM bus utilization (e.g., precharge other banks while accessing the current bank)
- Four cache lines (16quadwords) of CPU to DRAM write buffers
- Four cache lines of CPU to DRAM read prefetch buffers
- Read around write capability for non-stalled CPU read
- Speculative DRAM read before snoop result
- Burst read and write operation
- x-2-2-2-2-2 bank-to-back accesses for EDO DRAM from CPU or from DRAM controller
- x-1-1-1-1-1 back-to back accesses for SDRAM
- BIOS shadow at 16KB increment.
- Decoupled and burst DRAM refresh with staggered RAS timing
- CAS before RAS or self refresh



1-9 CHIPSET

> VT82C693A

The VIA 693A is a high performance, cost-effective and energy efficient chip set for the implementation of AGP/PCI/ISA desktop personal computer systems from 66MHz, 100MHz and 133MHz based on 64-bit Socket 370 super-scalar processors.

The Via 693A chip set consists of the VT82C693A system controller (510 pin BGA) and the VT82C686A PCI to ISA bridge (324 pin BGA). The system controller provides superior performance between the CPU, DRAM, AGP bus, and PCI bus with pipelined, burst, and concurrent operation.

The VT82C693A supports eight banks of DRAMs up to 1.5GB. The DRAM controller supports standard Fast Page Mode (FPM) DRAM, EDO-DRAM, Synchronous DRAM (SDRAM) and Virtual Channel SDRAM (VC SDRAM), in a flexible mix / match manner. The Synchronous DRAM interface allows zero wait state bursting between the DRAM and the data buffers at 66/100/133 MHz. The eight banks of DRAM can be composed of an arbitrary mixture of 1M/2M/4M/8M/16M/32MxN DRAMs. The DRAM controller also supports optional ECC (single-bit error correction and multi-bit detection) or EC (error checking) capability separately selectable on a bank-by-bank basis. The DRAM controller can run at either the host CPU bus frequency (66/100/133 MHz) or at the AGP bus frequency (66 MHz) with built-in PLL timing control.

The VT82C693A system controller also supports full AGP v2.0 capability for maximum bus utilization including 4x mode transfers, SBA (Side Band Addressing), Flush/Fence commands, and pipelined grants. An eight level request queue plus a four level post-write request queue with thirty-two and sixteen quad words of read and write data FIFO's respectively are included for deep pipelined and split AGP transactions. A single-level GART TLB with 16 full associative entries and flexible CPU/AGP/PCI remapping control is also provided for operation under protected mode



operating environments. Both Window 95 VxD and Windows 98 /NT5.0 mini port drivers are supported for interoperability with major AGP-based 3D and DVD- capable multimedia accelerators.

The VT82C693A supports two 32-bit 3.3/5V system buses (one AGP and one PCI) that are synchronous/pseudo-synchronous to the CPU bus. The chip also contains a built-in bus-to-bus bridge to allow simultaneous concurrent operations on each bus. Five levels (double words) of post write buffers are included to allow for concurrent CPU and PCI operation. For PCI master operation, forty-eight levels (double words) of post write buffers and sixteen levels (double words) of prefetch buffers are included for concurrent PCI bus and DRAM/cache accesses. The chip also supports enhanced PCI bus commands such as Memory-Read-Line, Memory-Read-Multiple and Memory-Write-Invalid commands to minimize snoop overhead. In addition, advanced features are supported such as snoop ahead, snoop filtering, L1 write-back forward to PCI master, and L1 writeback merged with PCI post write buffers to minimize PCI master read latency and DRAM utilization. Delay transaction and read caching mechanisms are also implemented for further improvement of over all system performance.

The 352-pin Ball Grid Array VT82C686A PCI to ISA bridge supports four levels (double words) of line buffers, type F DMA transfers and delay transaction to allow efficient PCI bus utilization The VT82C686A also includes an integrated keyboard controller with PS/2 mouse support, integrated DS12885 style real time clock with extended 256 byte CMOS RAM, integrated master mode enhanced IDE controller with full scatter/gather capability and extension to UltraDMA-33/66 for 33/66 MB/sec transfer rate, integrated USB interface with root Hub and two function ports with built-in physical layer transceivers, Distributed DMA supports, and On Now/ ACPI compliant advanced configuration and power management interface.

For sophisticated power management, the Via 693A provides independent clock stop control for the CPU/SDRAM, PCI, and AGP buses and



Dynamic CKE control for powering down of the SDRAM. A separate suspend-well plane is implemented for the SDRAM control signals for Suspend-to-DRAM operation. Coupled with the VT82C686A south bridge chip, a complete power conscious PC motherboard can be implemented with no external TTLs.

The Via 693A chipset is ideal for high performance, high quality, high energy efficient and high integration desktop and notebook AGP / PCI / ISA computer systems.

> VT82C686A

The VT82C686A PSIPC (PCI Super-I/O integrated Peripheral Controller) is a high integration, high performance, power-efficient, and high compatibility device that supports Intel and non-Intel based processor to PCI bus bridge functionality to make a complete Microsoft PC99-compliant PCI/ISA system In addition to complete ISA extension bus functionality, the VT82C686A includes standard intelligent peripheral controllers:

- 1) Master mode enhanced IDE controller with dual channel DMA engine and interlaced dual channel commands. Dedicated FIFO coupled with scatter and gather master mode operation allows high performance transfers between PCI and IDE \devices. In addition to standard PIO and DMA mode operation, the VT82C686A also supports the UltraDMA-33 standard to allow reliable data transfer rates up to 33MB/sec throughput. The VT82C686A also supports the UltraDMA-66 standard. The IDE controller is SFF-8038I v1.0 and Microsoft Windows-family compliant.
- 2) Universal Serial Bus controller that is USB v1.1 and Universal HCI v1.1 compliant. The VT82C686A includes the root hub with four function ports with integrated physical layer transceivers. The USB controller allows hot plug and play and isochronous peripherals to be inserted into the system with universal driver support. The controller also implements legacy keyboard and mouse support so that legacy software can run transparently in non-USB-aware operating system



environment.

- 3) Keyboard controller with PS2 mouse support.
- 4) Real Time Clock with 256 byte extended CMOS. In addition to the standard ISA RTC functionality, the integrated RTC also includes the date alarm, century field, and other enhancements for compatibility with the ACPI standard.
- 5) Hardware monitoring subsystem for managing system/ motherboard voltage levels, temperatures, and fan speeds
- 6) Full System Management Bus (SMBus) interface.
- 7) Two 16550-compatible serial I/O ports with infrared communications port option. A third serial port is available to be dedicated to the IR interface.
- 8) Integrated PCI-mastering dual full-duplex direct-sound AC97-link-compatible sound system. Hardware soundblaster-pro and hardware-assisted FM blocks are included for Windows DOS box and real-mode DOS compatibility. Loopback capability is also implemented for directing mixed audio streams into USB and 1394 speakers for high quality digital audio
- 9) Two game ports and One MIDI port
- 10) ECP/EPP-capable parallel port
- 11) Standard floppy disk drive interface
- 12) Distributed DMA capability for support O ISA legacy DMA over the PCI bus. Serial IRQ is also supported for docking and non-docking application.
- 13) Plug and Play controller that allows complete steerability of all PCI interrupts and internal interrupts/ DMA channels to any interrupt channel. One additional steerable interrupt channel is provided to allow plug and play and reconfigurability of onboard peripherals for Windows family compliance.

The VT82C686A also enhances the functionality of the standard ISA peripherals. The integrated interrupt controller supports both edge and level triggered interrupts channel by channel. The integrated DMA



controller supports type F DMA in addition to standard ISA DMA modes. Compliant with the PCI2.2 specification, the VT83C686A supports delayed transactions and remote power management so that slower ISA peripherals do not block the traffic of the PCI bus. Special circuitry is built in to allow concurrent operation without causing dead lock even in a PCI-to PCI bridge environment. The chip also includes eight levels (doublewords) of line buffers from the PCI bus to the ISA bus to further enhance overall system performance.

1-10 I/O INTERFACE CONTROLLER

The motherboard uses the VT82C686A Super-I/O controller which features:

- Supports 2 serial ports, IR port, parallel port, and floppy disk controller functions
- > Two UARTs for Complete Serial Ports
 - Even, odd, stick or no parity bit generation and detection
 - Programmable baud rate generator
 - High speed baud rate (230Kbps, 460Kbps) support
 - Independent transmit/receiver FIFOs
 - Modem Control
 - Plug and play with 96 base IO address and 12 IRQ options
- One dedicated IR port
 - Third serial port dedicated to IR function
 - IR function either through the two complete serial ports or the third dedicated port
 - Infrared-IrDA (HPSIR) and ASK (Amplitude Shift Keyed) IR
- ➤ Multi-mode parallel port
 - Standard mode, ECP and EPP support
 - Plug and play with 192 base IO address, 12 IRQ and 4 DMA options
- ➤ Floppy Disk Controller
 - 16bytes of FIFO
 - Data rates up to 1Mbps



- Perpendicular recording driver support
- Two FDDs with drive swap support
- Plug and play with 48 base IO address, 12 IRQ and 4 DMA options

The Setup program provides configuration option for the I/O controller.

1-10.1 Serial Ports

The motherboard has two 9-pin D-Sub serial port connectors located on the back panel. The NS16C5450-compatible UARTs support data transfers at speeds up to 115.2 Kbits/sec with BIOS support.

1-10.2 Parallel Port

The connector for the multimode bi-directional parallel port is a 25-pin D-Sub connector located on the back panel of the motherboard. In the Setup program, there are four options for parallel port operation:

- Compatible (standard mode)
- Bi-directional (PS/2 compatible)
- Bi-directional EPP. A driver from the peripheral manufacturer is required for operation.
- Bi-directional high-speed ECP

1-10.3 Diskette Drive Controller

The I/O controller is software compatible with the 82077 diskette drive controller and supports both PC-AT and PS/2 modes. In the Setup program, the diskette drive interface can be configured for the following diskette drive capacities and sizes.

- 360 KB, 5.25-inch
- 1.2 MB, 5.25-inch
- 720 KB, 3.5-inch
- 1.2 MB. 3.5-inch (driver required)
- 1.25-1.44 MB, 3.5-inch
- 2.88 MB, 3.5-inch



1-10.4 PS/2 Keyboard and Mouse Interface

PS/2 keyboard and mouse connectors are located on the back panel of the motherboard. The +5 V lines to keyboard and mouse connectors are protected with a fuse that prevents motherboard components from being damaged when an over-current condition occurs.



Note

The mouse and keyboard can be plugged into either PS/2 connector.

Power to the computer should be turned off before a keyboard or mouse is connected or disconnected.

The keyboard controller contains code, which provides the traditional keyboard and mouse control functions, and also supports Power On/Reset password protection. Power On/Reset password can be specified in the BIOS Setup program.

The keyboard controller also supports the hot-key sequence <Ctrl><Alt>, software reset. This key sequence resets the computer's software by jumping to the beginning of the BIOS code and running the Power On Self Test (POST).

1-10.5 Infrared Support

On the front panel I/O connector, there are six pins that support Hewlett Packard HSDL-1000 compatible infrared (IR) transmitters and receivers. In the Setup program, Serial Port B can be direct4d to a connected IR device. (In this case, the Serial Port B connector on the back panel cannot be used.) The IR connection can be used to transfer files to or from portable devices like laptops, PDAs, and printers. The Infrared Data Association (IrDA) specification supports data transfers of 115Kbits/sec at a distance of 1 meter.

1-11 HARDWARE MONITOR

The optional hardware monitor subsystem provides low-cost instrumentation capabilities. The features of the hardware monitor



subsystem include:

- An integrated ambient temperature sensor
- Fan speed sensors, which monitor the fan 1 and fan 2 connector.
- Power supply voltage monitoring to detect levels above or below acceptable values

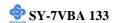
When suggested ratings for temperature, fan speed, or voltage are exceeded, an interrupt is activated. The hardware monitor component connects to the SMBus

1-12 WAKE ON LAN TECHNOLOGY

Wake on LAN technology enables remote wakeup of the computer through a network. Wake on LAN technology requires a PCI add-in network interface card (NIC) with remote wakeup capabilities. The remote wakeup connector on the NIC must be connected to the onboard Wake on LAN connector. The NIC monitors network traffic at the Ethernet interface; upon detecting a Magic Packet, the NIC asserts a wakeup signal that powers up the computer. This feature uses the Wake on LAN connector.

CAUTION

For Wake on LAN, the 5-V standby line for the power supply must be capable of delivering +5V ± 5 % at 720 mA. Failure to provide adequate standby current when implementing Wake on LAN can damage the power supply.



Chapter 2

HARDWARE INSTALLATION

Congratulations on your purchase of **SY-7VBA 133** Motherboard. You are about to install and connect your new Motherboard.



Note: Do not unpack the Motherboard from its protective antistatic packaging until you have made the following preparations.

2-1 PREPARATIONS

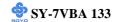
Gather and prepare all the following hardware equipment to complete the installation successfully:

1. Socket 370 processor with built-in CPU cooling fan (boxed type).



Note: This Motherboard supports non-boxed type CPUs. The heavier CPU cooling fan requires the installation of a CPU support stand.

- 2. DIMM memory module (s)
- 3. Computer case and chassis with adequate power supply unit
- Monitor
- 5. PS/2 Keyboard
- 6. Pointing Device (PS/2 mouse)
- 7. Speaker(s) (optional)
- 8. Disk Drives: HDD, CD-ROM, Floppy drive ...
- 9. External Peripherals: Printer, Plotter, and Modem (optional)
- 10. Internal Peripherals: Modem and LAN cards (optional)



2-2 UNPACKING THE MOTHERBOARD

When unpacking the Motherboard, check for the following items:

◆ The SY-7VBA 133 VT82C693A AGP/PCI Motherboard



◆ This Quick Start Guide



◆ The Installation CD-ROM



◆ SOYO 3-in-1 Bonus Pack CD-ROM (Norton AntiVirus Ghost and Virtual Drive)



◆ One IDE Device ATA 66 Flat Cable



◆ One Floppy Disk Drive Flat Cable



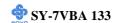


Warning: Do not unpack the Motherboard from its anti-static packaging until you are ready to install it.

Like most electronic equipment, your Motherboard may be damaged by electrostatic discharge. To avoid permanent damage to components ground yourself while working by using a grounding strap. Otherwise, ground yourself frequently by touching the unpainted portion of the computer chassis to drain the static charges.

Handle the Motherboard carefully, holding it by the edges.

You are now ready to start the installation.



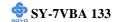
2-3 INSTALLATION GUIDE

We will now begin the installation of the Motherboard. Please follow the step-by-step procedure designed to lead you to a complete and correct installation.



Warning: Turn off the power to the Motherboard, system chassis, and peripheral devices before performing any work on the Motherboard or system.

BEGIN THE INSTALLATION



2-3.1 CPU Installation

Your SY-7VBA 133 motherboard comes with a CPU retention set kit. The retention set is used to hold the processor attached to the Socket 370 CPU connector on the motherboard.

✓ Mark your CPU Frequency: Record the working frequency of your CPU that should be clearly marked on the CPU cover.

FSB 66MHz

400MHz (66 x 6.0)	466MHz (66x7.0)	533MHz (66x8.0)
433MHz (66 x 6.5)	500MHz (66x7.5)	

FSB 100MHz

		700MHz (100 x 7.0)	800MHz (100 x 8.0)
550MHz (100 x 5.5)	650MHz (100 x 6.5)	750MHz (100 x 7.5)	

FSB 133MHz

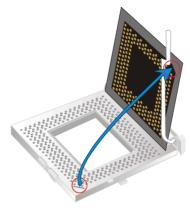
533MHz (133 x 4.0)			
600MHz (133 x 4.5)	733MHz (133 x 5.5)	866MHz (133 x 6.5)	

CPU Mount Procedure: To mount the Celeron $^{\text{TM}}$ processor that you have purchased separately, follow these instructions.

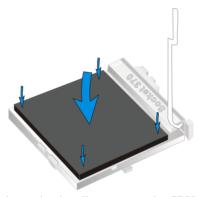
1. Lift the socket handle up to a vertical position.



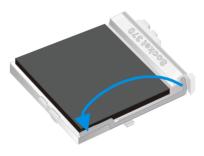
2. Align the blunt edge of the CPU with the matching pinhole distinctive edge on the socket.



3. Seat the processor in the socket completely and without forcing.



4. Then close the socket handle to secure the CPU in place.







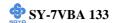
Remember to connect the CPU Cooling Fan to the appropriate power connector on the Motherboard. *The fan is a key component that will ensure system stability. The fan prevents overheating, therefore prolonging the life of your CPU.*

2-3.1.1 CPU Fan Installation

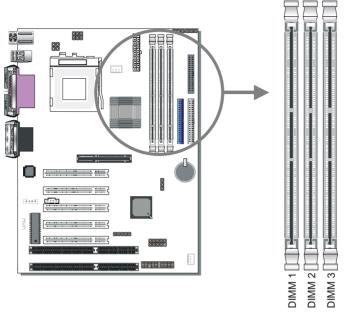
Your Socket 370 processor kit comes with a cooling fan. Mount the fan on the processor according to the instructions provided by the manufacturer. The fan is a key component that will ensure system stability. The fan prevents overheating, therefore prolonging the life of your CPU.



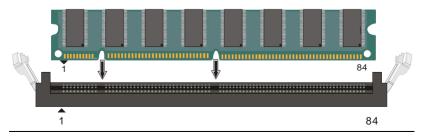
Note: Remember to connect the fan to the appropriate power source.

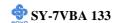


2-3.2 SDRAM Memory Module Installation



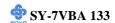
Your board comes with three DIMM sockets, providing support for up to 1.5GB of main memory using unbuffered and registered DIMM modules from 8MB to 512MB. On this motherboard, DRAM speed can be set independent from the CPU front side bus speed. Depending on the DRAM clock speed setting in the BIOS setup (Chapter 3), appropriate memory modules must be used. For 66MHz DRAM speed, use PC66 memory; for 100MHz DRAM speed, use PC100 memory; for 133MHz DRAM speed, use PC133 memory.





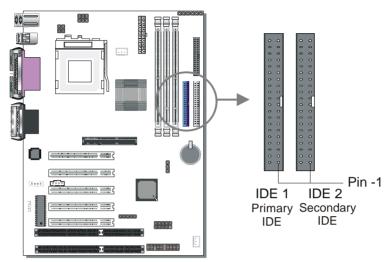
Memory Configuration Table

Number of Memory Modules	DIMM 1	DIMM 2	DIMM 3
RAM Type	SDRAM		
Memory Module Size (MB)	8/16/32/64/128/256/512 MB		



2-3.3 Motherboard Connector

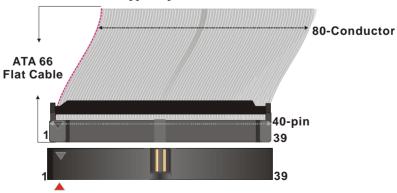
2-3.3.1 IDE Device Installation (HDD, CD-ROM)

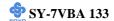


This Motherboard offers two primary and secondary IDE device connectors (IDE1, IDE2). It can support up to four high-speed Ultra DMA 33/66HDD or CD-ROM.

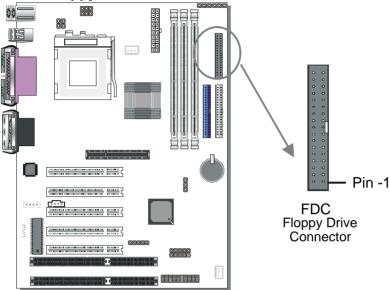
Connect one side of the ATA66 flat cable to the IDE device (HDD or CD-ROM) and plug the other end to the primary (IDE1) or secondary (IDE2) directionally keyed IDE connector on the Motherboard. The ATA66 cable is backward compatible with ATA33 HDDs.

This Motherboard can support up to four HDDs.





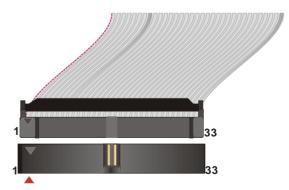
2-3.3.2 Floppy Drive Installation

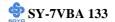


The system supports 5 possible floppy drive types: 720 KB, 1.2 MB, 1.44 MB and 2.88 MB.

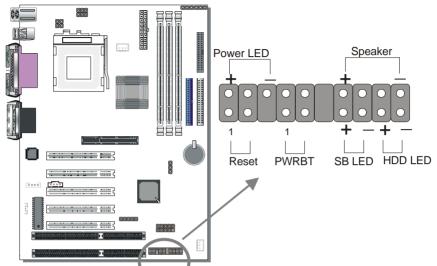
Connect one side of the 34-pin flat cable to the floppy drive and plug the other end to the floppy drive connector on the Motherboard.

This Motherboard can support up to 2 floppy drives.





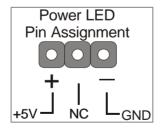
2-3.3.3 Front Panel Connections

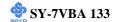


Plug the computer case's Front panel devices to the corresponding headers on the Motherboard.

1. Power LED

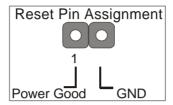
Please install according to the following pin assignment: pin 1,3 are for Power LED.





2. Reset

Plug the Reset push-button cable into the 2-pin Reset header on the Motherboard. Pushing the Reset button on the front panel will cause the system to restart the boot-up sequence.



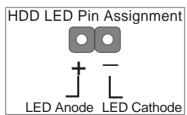
3. Speaker

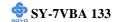
Attach the 4-pin PC speaker cable from the case to the Speaker header on the Motherboard.



4. IDE LED

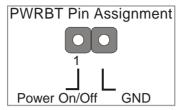
Attach the 2-pin IDE device LED cable to the corresponding IDE LED header on the Motherboard. This will cause the LED to light up when an IDE (HDD, CD-ROM) device is active.





5. ATX Power On/Off Switch

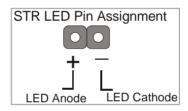
Attach the 2-pin momentary type switch to the PWRBT header for turning On or Off your ATX power supply.

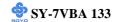


6. STR LED

The STR LED is connected to the Voltage that feeds the DIMM sockets. Therefore the following table applies:

	Suspend to RAM	Normal Operation	Power Off
STR LED	ON	ON	OFF



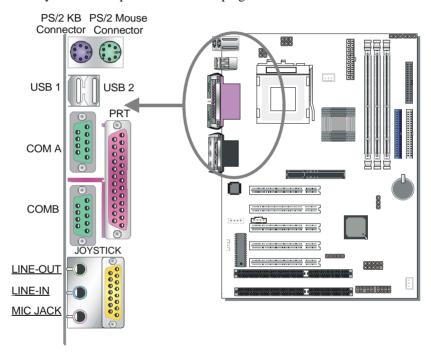


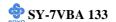
2-3.3.4 Back Panel Connections

All external devices such as the PS/2 keyboard, PS/2 mouse, printer, modem, USB can be plugged directly onto the Motherboard back panel.

Only after you have fixed and locked the Motherboard to the computer case can you start connecting the external peripheral devices.

When connecting an external device, use the following figure to locate and identify which back panel connector to plug the device to.





1. Onboard Serial Ports COM1/COM2

External peripherals that use serial transmission scheme include:

- serial mouse.
- and modem

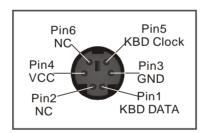
Plug the serial device cables directly into the COM1/COM2 9-pin male connectors located at the rear panel of the Motherboard.

2. Parallel Port PRT

This parallel port is used to connect the printer or other parallel devices. Plug the parallel device cable into the 25-pin female connector located at the rear panel of the Motherboard.

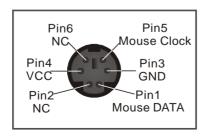
3. PS/2 Keyboard

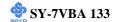
Plug the keyboard jack directly into the 6-pin female PS/2 keyboard connector located at the rear panel of the Motherboard.



4. PS/2 Mouse

Similarly, plug the mouse jack directly into the 6-pin female PS/2 mouse connector.



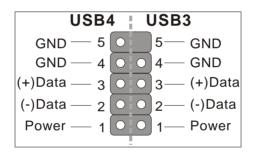


5. Universal Serial Bus USB1/USB2/(USB3, USB4)

This Motherboard provides four USB ports for your additional devices. Plug the USB device jack into the available USB connector USB1 or USB2.

- Standard device drivers come with the Win98 for commonly used USB devices.
- With Win95, use the flow UHCI specifications. To use USB devices under Win95, usually you have to install the device that driver comes with the USB device you have purchased.

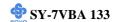
USB3 and 4 are available. To make use of these USB ports, purchase a USB cable from your dealer. The lay-out of USB3 and 4 is as follows:



6. Onboard Joystick port/audio

This Motherboard provides Joystick port and audio.

- Attach the joystick cable to the 15-pin JOYSTICK port at the rear panel of you motherboard.
- This Motherboard features three built-in audio-stereo ports (labeled line-in, line-out, and mic jack) convenient to directly plug-in all your external audio devices.



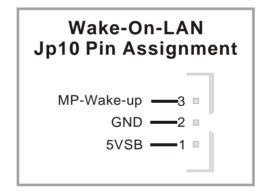
2-3.3.5 Other Connections

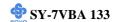
1. Wake-On-LAN (WOL)

Attach the 3-pin connector from the LAN card which supports the Wake-On-LAN (WOL) function to the JP10 header on the Motherboard. This WOL function lets users wake up the connected computer through the LAN card.



Please install according to the following pin assignment:





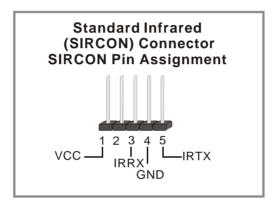
2. Infrared (SIRCON)

Plug the 5-pin infrared device cable to the SIRCON header. This will



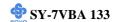
enable the infrared transfer function. This Motherboard meets both the ASKIR and HPSIR specifications.

Please install according to the following pin assignment:



3. Other Display Cards

Insert other types of VGA cards into the PCI or ISA expansion slots according to card specifications.

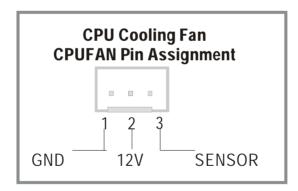


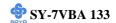
4. Cooling Fan Installation



(1) CPU Cooling Fan

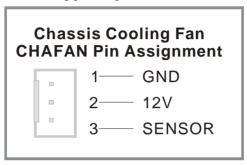
After you have seated the CPU properly on the processor, attach the 3-pin fan cable to the CPUFAN connector on the Motherboard. The fan will stop when the system enters into Suspend Mode. (Suspend mode can be enabled from the BIOS Setup Utility, [POWER MANAGEMENT] menu.) To avoid damage to the system, install according to the following pin assignment:





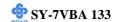
(2) Chassis Cooling Fan

Some chassis also feature a cooling fan. This Motherboard features a CHAFAN connector to provide 12V power to the chassis fan. Connect the cable from the chassis fan to the CHAFAN 3-pin connector. Install according to the following pin assignment:





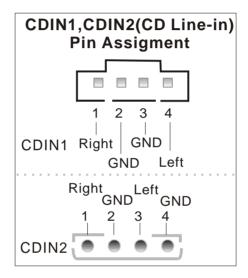
Note: CPUFAN must be installed for this Motherboard, CHAFAN is optional.



5. CD Line-in (CDIN1,CDIN2)

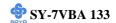
This Motherboard provides two CD-Line in connectors. Please connect the 4-pin audio cable from your CD-ROM drive to either CDIN1 or CDIN2. (It fits in only one, depending on the cable that came with your CD-ROM drive)

Please install according to the following pin assignment:



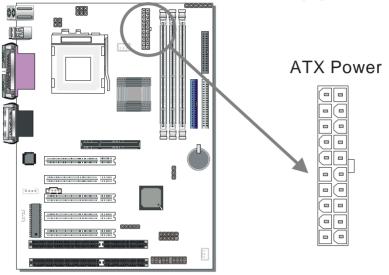
2-3.3.6 AGP VGA Card

Insert the AGP VGA card into the AGP slot. Then connect the monitor information cable to the AGP card back plane external connector. Follow the manufacturer's instructions to perform the AGP VGA drivers installation.



2-3.3.7 ATX Power Supply

Plug the connector from the power directly into the 20-pin male ATX PW connector on the Motherboard, as shown in the following figure.



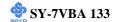


Warning: Follow these precautions to preserve your Motherboard from any remnant currents when connecting to ATX power supply:

Turn off the power supply and unplug the power cord of the ATX power supply before connecting to ATX PW connector.

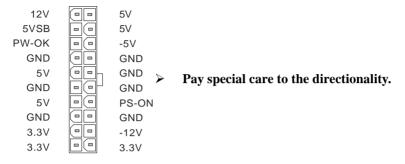
The Motherboard requires a power supply with at least 200 Watts and a "power good" signal. Make sure the ATX power supply can take at least 720 mA * load on the 5V Standby lead (5VSB) to meet the standard ATX specification.

* *Note:* If you use the Wake-On-LAN (WOL) function, make sure the ATX power supply can support at least 720 mA on the 5V Standby lead (5VSB).



Please install the ATX power according to the following pin assignment:



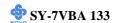


2-3.4 Jumper Setting

Step 1. CPU Core Voltage Adjust (VJ1)

In case your CPU is running on a frequency higher then it is specified for, increasing its core voltage can enhance its stability. Over-clocking your CPU does however force your system to operate outside of its specifications, and therefore SOYO can not guarantee system stability.

CPU Core Voltage Adjust	VJ1 Setting		
Default Vcore	Short pin 1-2	4 5 6	
3% higher	Short pin 2-3	4 5 6 1 2 3	
6% higher	Short pin 4-5	4 5 6 1 2 3	
10% higher	Short pin 5-6	1 2 3	



Step 2. CPU AJ33 and AJ31 Settings: JP6, JP7, CJ1 and CJ2

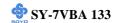
For certain Intel CPUs, the multiplier is not locked such that setting a multiplier higher than specified on the CPU is possible. For technical details read the following:

Your PII /III 100/66 MHz FSB CPU has a pin AJ33 (100/66# signal) to tell it what FSB frequency the CPU requires; The PIII 133 MHz FSB CPU has two pins AJ33and AJ31 (133/100# signal). JP6 and JP7 are connected to the AJ33 and AJ31 input respectively and are used to tell the motherboard what the CPU FSB frequency is. If these two jumpers are shorted the motherboard will automatically run at the frequency the CPU was specified to run at.

However, the FSB Frequency can also be set through jumpers CJ1 and CJ2 and it may therefore differ from the Frequency the CPU specifies. If CJ1 and CJ2 are to be used, JP6 & JP7 must be open. Doing so may however force your CPU to operate out of its specifications and therefore SOYO can not guarantee the proper functioning of your system.

Refer to the following table:

CPU Frequency		JP6		JP7		CJ1		C	J2
Automatic		Short	1 2	Short	1 2	Open	1 2 3	Oper	1 2 3
	66MHz	Open	1 2	Open	1 2	2-3	1 2 3	2-3	1 2 3
Manual	100MHz	Open	1 2	Open	1 2	1-2	1 2 3	2-3	1 2 3
	133MHz	Open	1 2	Open	1 2	1-2	1 2 3	1-2	1 2 3



2-3.5 CMOS Clearing (JP5)

In some cases the CMOS memory may contain wrong data, follow the steps below to clear CMOS memory.

- 1. Clear the CMOS memory by momentarily shorting pin 2-3 on jumper JP5. This jumper can be easily identified by its white colored cap.
- 2. Then put the jumper back to 1-2 to allow writing new of data into the CMOS memory.

CMOS Clearing	Clear CMOS I	Retain CM	OS Data	
JP5 Setting	Short pin 2-3 for at least 5 seconds to clear the CMOS	1 2 3	Short pin 1-2 to retain new settings	1 2 3

Note: You must unplug the ATX power cable from the ATX power connector when performing the CMOS Clear operation.

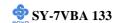
2-3.6 Power On

You have now completed the hardware installation of your Motherboard successfully.

- 1. Turn the power on
- 2. To enter the BIOS Setup Utility, press the key while the system is performing the diagnostic checks,



Note: If you have failed to enter the BIOS, wait until the boot up sequence is completed. Then push the RESET button and press key again at the beginning of boot-up, during diagnostic checks.



Repeat this operation until you get the following screen.

3. The BIOS Setup screen appears:

ROM PCI/ISA BIOS						
CMOS SET	UP UTILITY					
AWARD SOF	TWARE, INC.					
SOYO COMBO SETUP	INTEGRATED PERIPHERALS					
STANDARD CMOS SETUP	SUPERVISOR PASSWORD					
BIOS FEATURES SETUP	USER PASSWORD					
CHIPSET FEATURES SETUP	IDE HDD AUTO DETECTION					
POWER MANAGEMENT SETUP	SAVE & EXIT SETUP					
PNP/PCI CONFIGURATION	EXIT WITHOUT SAVING					
LOAD SETUP DEFAULTS						
Esc : Quit	$\uparrow \downarrow \rightarrow \leftarrow$: Select Item					
F10 : Save & Exit Setup	(Shift) F2 : Change Color					
Time, Date, Ha	rd Disk Type					

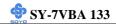
2-3.7 Quick BIOS Setup

This Motherboard does not use any hardware jumpers to set the CPU frequency. Instead, CPU settings are software configurable with the BIOS [SOYO COMBO SETUP]. The [SOYO COMBO SETUP] menu combines the main parameters that you need to configure, all in one menu, for a quick setup in BIOS.

After the hardware installation is complete, turn the power switch on, then press the key during the system diagnostic checks to enter the Award BIOS Setup program. The CMOS SETUP UTILITY will display on screen. Follow these steps to configure the CPU settings. SETUP UTILITY will display on screen. Then, follow these steps to configure the CPU settings.

Step 1. Select [STANDARD CMOS SETUP]

Set [Date/Time] and [Floppy drive type], then set [Hard Disk Type] to "Auto".



Step 2. Select [LOAD SETUP DEFAULT]

Select the "LOAD SETUP DEFAULT" menu and type "Y" at the prompt to load the BIOS optimal setup.

Step 3. Select [SOYO COMBO SETUP]

Move the cursor to the [CPU Frequency] field to set the CPU frequency.

(1) CPU Name & CPU ID

The BIOS will read the CPU name string and CPU ID code From the CPU and it will display it here. This item provides information only and can not be change.

(2) CPU Frequency

Move the cursor to the **[CPU Frequency]** field to set the CPU frequency. Available [CPU Frequency] settings on your SY-7VBA 133 Motherboard are detailed in the following table.

CPL	J Frequency (MHz)		500MHz(66 x 7.5)		750MHz (100 x 7.5)		
	Manual		533MHz (66 x 8.0)		800MHz (100 x 8.0)		
	200MHz (66 x 3.0)		300MHz (100 x 3.0)		400MHz (133 x 3.0)		
	233MHz (66 x 3.5)		350MHz (100 x 3.5)		466MHz (133 x 3.5)		
	266MHz (66 x 4.0)		400MHz (100 x 4.0)		533MHz (133 x 4.0)		
	300MHz (66 x 4.5)		450MHz (100 x 4.5)		600MHz (133 x 4.5)		
	333MHz (66 x 5.0)		500MHz (100 x 5.0)		666MHz (133 x 5.0)		
	366MHz (66 x 5.5)		550MHz (100 x 5.5)		733MHz (133 x 5.5)		
	100MHz (66 x 6.0)		600MHz (100 x 6.0)		800MHz (133 x 6.0)		
	133MHz (66 x 6.5)		650MHz (100 x 6.5)		866MHz (133 x 6.5)		
4	66MHz (66 x 7.0)		700MHz(100 x 7.0)		933MHz (133 x 7.0)		
Select the working frequency of your Pentium®III, Pentium®II, Celeron™ processor							
among these preset values.							
Note: Mark the checkbox that corresponds to the working frequency of your							
Ponti	ium® III. Pontium® II. C	oloro	$\mathrm{pn^{TM}}$ processor in case th		MOS configuration		
	idili III, Feridalii II, C	CICIO	ni case in	e Ci	vios coringulation		

If you set this field to [Manual], you are then required to fill in the next two consecutive fields: (1) the CPU Host/PCI Clock, and (2) the CPU Ratio.

(1) CPU Host/PCI Clock

	CPU	Host / PC	[Clock		
4 66/33	9 5/31	1 15/38	1 24/41	1 40/35	Under this item you find the
1 75/37	1 00/33	1 17/39	1 26/31	1 42/35	frequencies your PCI slots run at.
□ 78/39	1 05/35	1 118/39	1 33/33	1 44/36	
1 81/40	1 10/36	1 20/40	1 35/33	1 50/37	
□83/41	1 12/37	1 22/37	1 37/34	1 55/38	
1 90/30	1 13/37	1 24/31	1 38/34		

(2) CPU Ratio

After you have selected the CPU Host/ PCI Clock, choose the right multiplier for the CPU. CPU Ratio options are:

□ x 2	□ x 2.5	□ x 3	□ x 3.5	□ x 4
□ x 4.5	□ x 5	□ x 5.5	□ x 6	□ x 6.5
□ x 7	□ x 7.5	□ x 8		

The CPU frequency is then defined as [host clock freq.] x [multiplier], and should equal the working frequency of your CPU.

(3) DRAM Clock

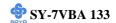
(3) DRAM Clock

Now select the DRAM clock source. It is derived form the CPU FSB clock and it can, depending on the BIOS setting, be:

- > Equal to the CPU FSB clock
- ➤ CPU FSB clock PCI clock
- CPU FSB clock + PCI clock

As an example: If the user sets the CPU Host/PCI clock to 105/35 MHz, the options will be:

- > Equal to the CPU FSB clock = 105 MHz
- ➤ CPU FSB clock PCI clock = 70 MHz
- CPU FSB clock + PCI clock = 140 MHz



Depending on the DRAM speeds, the user can select one of these speeds through the BIOS.

(4) AGP Clock

The AGP clock is derived from the CPU FSB frequency. It is divided by 1.0, 1.5 or 2.0 depending on the setting of JP6, JP7, CJ1 and CJ2:

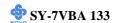
Please refer to page 43 for the JP6, JP7, CJ1 and CJ2 settings.

(5) Vcore Voltage Adjust

The CPU notifies the board of what core voltage it requires by its VID outputs. The on-board voltage regulator uses the VID code to set the core voltage. If the **Vcore Voltage Adjust** is set to normal, the Vcore will be exactly what the VID code specifies. If an adjustment percentage is selected the Vcore will be that percentage higher than the VID code specifies. For instance the CPU VID code specifies 2.0V and the Vcore Voltage adjust is set to +10.0% the actual CPU Voltage will be 2.2V. This function should only be used if the CPU is running on FSB Frequencies beyond the CPU specifications, note that SOYO does not guarantee system stability if this item is not set to normal.

Step 4. Select [SAVE & EXIT SETUP]

Press **<Enter>** to save the new configuration to the CMOS memory, and continue the boot sequence.



2-3.8 Troubleshooting at First Start

Video (no display) related issues

I built a new computer system using a Soyo board and nothing happens when turning it on, no video and no beeps from the PC speaker. What is happening and how can it be fixed?

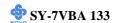
No screen and no beeps mean that your CPU and motherboard do not work at all. It could be that the CPU is not seated correctly or that a component on the M/B is grounded (shorted) with the case. Also make sure to check the voltage setting switch (110V/220V) on the back of the power supply. To isolate the problem do the following:

- 1. Press and hold down on the "Ins" (insert) key while turning on the computer until you get video. If you do not get video then,
- 2. Double-check jumpers setting on you motherboard and remove all add-on cards, unplug all hard-disk and floppy-disk drive cables and see if you can hear some beeps. If you still do not get any beeps, then try putting the motherboard on the table (to isolate it from the case) with the CPU and speaker only, and give it one more try.

I hear a series of beeps and I do not get anything from my monitor. What could be wrong?

The following lists some basic beep codes and their possible meanings:

- One long beep and 3 very short beeps The video card is not detected by the motherboard. Please re-seat your video card. If you are using an AGP card, please push your AGP card down real hard. You may have to push VERY hard without the AGP card mounting screw. Make sure not to insert the card the other way around.
- Continuous beeps One or more of the memory modules is not seated correctly in its socket.



My PCI VGA card works fine with my system, but when I put in a new AGP card, it does not give me any video. Is my AGP slot bad?

This is a common problem with AGP video cards. The reason is that your AGP card did not get seated into the AGP slot fully and firmly. Please push your AGP card down into the socket real hard, it should snap twice. You may have to unscrew the AGP card to allow the card to go further down. Do take care not to damage the card by using too much force.

I get distorted video my AGP card right after I save my bios. Why is that?

The cause is likely that your AGP card is not running at the correct bus speed. To fix this, please clear the CMOS via JP5 and if it still does not work, please upgrade your motherboard bios to the latest version.

BIOS Issues

Where can I find the BIOS revision of my mainboard?

It will be displayed on the up-left corner on the screen during boot-up. It will show as your board type followed by the revision number, such as 5EH_2CA1 (meaning revision 2CA1 for the SY-5EH board) or 6BA+ IV 2AA2 which means SY-6BA+ IV motherboard with 2AA2 bios.

Where can I find the latest BIOS of my motherboard?

Please go to the technical support page of one of the SOYO websites (Taiwan: www.soyo.com.tw), and look up your motherboard to find the latest BIOS revision.

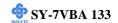
Hard disk, floppy drive, CD-ROM etc

When I boot up my new computer I got "floppy boot failure" and the LED on the floppy stays on

Make sure the red wire of floppy ribbon cable goes to Pin1 on the floppy drive side (don't trust the "key lock" or "notch") and use the end-connector of the cable (don't use middle one).

Modem issues

I get an "I/O Conflict" message when I turn on my system and I can not get my modem to work



What you need to do is to disable 'COM2' (or UART2 or serial port 2) in the bios under integrated peripheral setup.

I have installed my modem drivers several times and I still cannot get my modem to work. Why?

If you are sure that the modem driver has been installed correctly, then you need to install the south bridge driver from the SOYO CD, this is because Windows does not properly recognize relatively new chipsets.

Audio Issues

I do not get any sound from my sound card. What could be wrong? Please make sure the speaker is connected to the speaker out port on your sound card.

In Device Manager, I keep getting yellow exclamation signs on my sound port even though I have installed my sound driver several times and I could not get my sound card to work. What is wrong?

It is likely that you did not have the correct driver installed. If you are sure that the correct sound driver has been installed, then please install the 'south bridge' driver for the motherboard.

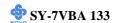
The sound is working in my system, but when I play CD music from the CD-ROM, I do not get any sound. What is wrong?

This is because the 3-wire audio cable from the CD-ROM to the sound card is not connected or it is loose.

The sound from my sound card is distorted when Windows starts. What is wrong?

First, if you are using an ISA sound card, please make sure the IRQ needed for the sound card is set to 'Legacy ISA' in the bios. In other words, if your ISA sound card takes IRQ5, then set IRQ5 to 'Legacy ISA'. Next, install the 'south bridge' driver for the motherboard.

The sound and everything else works fine except that the recorder and microphone do not work. What is wrong?



This is because the recorder and microphone in the Windows are not enabled. Please go to sound properties and enable them.

Lock up (freeze)

When I boot up my system, everything works fine. It sees my CPU and memory, detects my hard drive, floppy drive and CD-ROM but locks up at "Verify DMI pool data...", and it won't go any further. What should I do?

Please clear the CMOS via JP5 then choose 'load setup default' in the bios and save the bios and exit. Next, unplug all other add-on cards except the video card and floppy drive controller, and see if it can boot from floppy. Then put back the peripherals one by one to identify which one causes the lockup. If you are running a Cyrix CPU, make sure the 'linear burst function' is enabled in the bios.

I can not get my board to run properly.

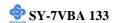
Please make sure you have the latest bios and driver from the SOYO web site at: http://www.soyo.com

2-3.9 Power Off

There are two possible ways to turn off the system:

- 1. Use the **Shutdown** command in the **Start Menu** of Windows 95/98 to turn off your computer.
- Press the mechanical power-button and hold down for over 4
 seconds, to shutdown the computer. If you press the power-button for
 less than 4 seconds, then your system will enter into Suspend Mode.

You are now ready to configure your system with the BIOS setup program. Go to *Chapter 3: BIOS SETUP*



Chapter 3

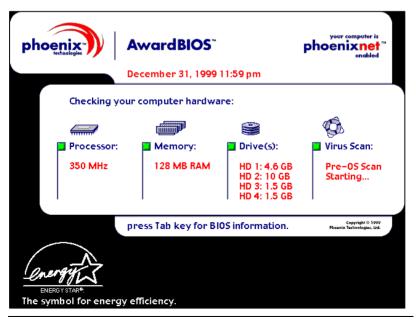
BIOS SETUP UTILITY

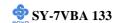
This Motherboard's BIOS setup program uses the ROM PCI/ISA BIOS program from Award Software Inc.

Phoenixnet Graphical Launch Screen

The graphical launch screen displays a graphical screen to the user early in the boot process, as the first image displayed on the screen. This display remains on the screen throughout the normal BIOS initialization phase called POST.

The graphical launch screen will replace the old text based POST with a full graphical screen. The screen will display PC information such as CPU vendor, model and speed, memory and hard disk size. This screen will also display graphical logos from SOYO, Phoenix/ AwardBIOS, and the PhoenixNet logos.





If the user wants to see the old boot-up screen, pressing the TAB key will switch the screen mode back to the conventional boot-up screen.

The PhoenixNet BIOS will put several Icons on the windows desktop after windows is installed for the first time. Clicking these items will allow the user to go to the respective websites, including the SOYO website. If the user does not need these icons they can be removed.

To enter the Award BIOS program's Main Menu:

- 1. Turn on or reboot the system.
- 2. After the diagnostic checks, press the [Del] key to enter the Award BIOS Setup Utility.

ROM PCI/ISA BIOS					
CMOS SETU	JP UTILITY				
AWARD SOFT	WARE, INC.				
SOYO COMBO SETUP	INTEGRATED PERIPHERALS				
STANDARD CMOS SETUP	SUPERVISOR PASSWORD				
BIOS FEATURES SETUP	USER PASSWORD				
CHIPSET FEATURES SETUP	IDE HDD AUTO DETECTION				
POWER MANAGEMENT SETUP	SAVE & EXIT SETUP				
PNP/PCI CONFIGURATION	EXIT WITHOUT SAVING				
LOAD SETUP DEFAULTS					
Esc : Quit	- ¯ ® ¬ : Select Item				
F10 : Save & Exit Setup	(Shift) F2 : Change Color				
Time, Date, Hard	l Disk Type				

- 3. Selecting items
- Use the arrow keys to move between items and select fields.
- From the Main Menu press arrow keys to enter the selected submenu.

Modifying selected items

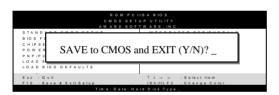
• Use the [Up]/[Down] keys to modify values within the selected fields. Some fields let you enter values directly.

Hot Keys: Function keys give you access to a group of commands throughout the BIOS utility.

Function	Command	Description
F1	Help	Gives the list of options available for each item.
Shift F2	Color	Change the color of the display window.
F5	Old values	Restore the old values. These are the values that the user started the current session with.
F7	Load Setup Defaults	Loads all options with the Power-On default values.
F10	Save & Exit Setup	Saves your changes and reboots the system.
[Esc]	Quit	Lets you return at anytime and from any location to the Main Menu.

SAVE AND EXIT SETUP

Select the [SAVE & EXIT SETUP] option from the Main Menu to save data to CMOS and exit the setup utility. This option saves all your changes and causes the system to reboot.



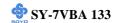
Type [Y] to save the changes and exit or [N] to return to the Main Menu and keep current values.

EXIT WITHOUT SAVING

Selecting the [EXIT WITHOUT SAVING] option allows you to abandon all data and exit setup, therefore ignoring all your changes.



Type [Y] to abandon changes and exit or [N] to return to the Main Menu and keep current values



3-1 SOYO COMBO SETUP

This motherboard does not use any hardware jumpers to set the CPU frequency. Instead, CPU settings are software configurable with the BIOS [SOYO COMBO SETUP].

After the hardware installation is complete, turn the power switch on, then press the key during the system diagnostic checks to enter the Award BIOS Setup program. The CMOS SETUP UTILITY will display on screen. Then, select the [SOYO COMBO SETUP] option from the main menu and press the <Enter> key.

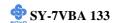
ROM PCI/ISA BIOS SOYO COMBO SETUP								
AWARD SOFTWARE, INC. CPU Frequency (MHz) : Manual Current System Temp. : 26 ½ C / 78 ½ F								
CPU Host/PCI Clock	: 100/33 MHz	Current CPU Temperature : 28 ½ C / 82 ½ F						
CPU Ratio	: X 5	Current CPUFAN Speed : 5433 RPM						
DRAM Clock	: Host CLK	Current CHAFAN Speed : 0 RPM						
SDRAM Cycle Length	: 3	Vcore : 2.01 V 2.5V : 1.5 V						
CPU L2 Latency Adjust	: Defxx	3.3V : 3.52 V 5V : 4.91 V						
C.I.H. 4-WAY Protection	: Enabled	12V : 12.09 V						
Quick Power On Self Test	: Enabled							
Boot Sequence	: A,C,SCSI							
Soft-Off by PWRBTN	: Instant-off							
Memory Ultra fast	:Enabled							
Modem Ring Resume	: Disabled							
RTC Alarm Resume	: Disabled	ESC : Quit $\uparrow \downarrow \rightarrow \leftarrow$: Select Item:						
Date (of Month)	:	F1 : Help PU / PD / + / - : Modify						
Timer (hh:mm:ss)	:	F5 : Old Values (Shift) F2 : Color						
		F7 : Load Setup Defaults						

The [SOYO COMBO SETUP] menu combines the main parameters that you need to configure, all in one menu, for a quick setup in BIOS.



3-1.1 Quick CPU Frequency Setup

Quick CPU Frequency			accrintian		Note
	Setting	ע	escription		Note
Setup					
Setup					
CPU	Manual		550MHz (100 x 5.5) Select the	.
Frequency	200MHz (66 x 3)	600MHz (100 x 6) working	
	233MHz (66 x 3.	5)	650MHz (100 x 6.5) frequency	of of
	266MHz (66 x 4)	700MHz(1	00 x 7) your Socl	ket 370
	300MHz (66 x 4.	5)	750MHz (among
	333MHz (66 x 5)	800MHz (set
	366MHz (66 x 5.	5)	400MHz (
	400MHz (66 x 6)	466MHz (
	433MHz (66 x 6.		533MHz (
	466MHz (66 x 7		600MHz (- requires j	
	500MHz(66 x 7.		666MHz (
	533MHz (66 x 8 300MHz (100 x 3		733MHz (** 800MHz (**	- two conse	
	,		·	100 0.5	
	350MHz (100 x 3.5) 866MHz (133 x 6.5) CPU Host/PCI 400MHz (100 x 4) 933MHz (133 x 7) Clock and (2)				
	450MHz (100 x 4		1.0GHz (1	20 7.5\	
i i	500MHz (100 x 5			the CPU 1 (133 x 8)	Ratio.
If [CPU Frequen	`				
CPU Host/PCI			39 MHz	Select the host clock of	f your
Clock	66/33 MHz	120/	40 MHz	Socket 370 processor a	•
	75/37 MHz	124/	31 MHz	these values.	· ·
	80/40 MHz	126/	31 MHz	Note: For the Via69	
	83/41 MHz	129/	32 MHz	chipset, 66/100 and	1 133
-			33 MHz	MHz host clock	
			33 MHz	frequencies are acco	
			34 MHz	However, the system	
			35 MHz	stability is not guar	
			35 MHz	for other frequencies the limitations of the	
			36 MHz	chipset.	IIS .
			32 MHz	empset.	
			37 MHz		
			38 MHz		
			40 MHz		



Quick CPU Frequency Setup (Continued)

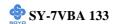
Quick CPU	Setting	Description	Note
Frequency Setup	~ g	.	- 1000
If [CPU Frequency] field is set t	o [Manual]	
CPU Ratio	After you have	e selected the host clock, choose the ri	ght
	multiplier for t	he CPU. Options are: [2, 2.5, 3., 3.5,	4, 4.5, 5,
	5.5,6,6.5,7.0,7	.5,8.0]. The CPU frequency is then de	fined as
	[host clock fre	q.]x[multiplier], and should the worki	ng
	frequency of y	our Pentium®III & Celeron TM proces	sor.
DRAM Clock	HCLK-33M	This item allows you to control	Default
	Host Clock	the DRAM speed.	
	HCLK+33M		
SDRAM Cycle	2	When synchronous DRAM is	Default
Length	3	installed, the number of clock	Deruurt
Length		cycles of CAS latency depends	
		on the DRAM timing. Do not	
		reset this field from the default	
		value specified by the system	
		designer.	

3-1.2 L2 Cache Memory

o 1.2 L2 Gaone memory				
	Setting	Description	Note	
CPU L2 Cache	Dofwy	This item allows the user to adjust		
		This item allows the user to adjust		
Latency Adjust		the CPU L2 cache latency. This	Default	
		item should only be used by		
		experienced users. Setting it to an		
		inappropriate value can crash the		
		system.		

3-1.3 C.I.H. 4-WAY Protection Settings

Setting	Description	Note
	When set to enabled, the BIOS can only be programmed through AWDFLASH, making sure that any virus is unable to program the system BIOS. Set to disable the BIOS can be programmed the traditional way.	Default

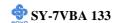


3-1.4 System Boot Control Settings

System Boot	Setting	Description	Note
Control Settings			
Quick Power On Self Test	Disabled Enabled	Provides a fast POTS at boot-up.	Default
Boot Sequence	A, C, SCSI C, A, SCSI C, CDROM, A CDROM, C, A D, A, SCSI E, A, SCSI F, A, SCSI SCSI, A, C SCSI, C, A C only LS/ZIP, C	Choose the boot sequence adapted to your needs, for example: • [A, C, SCSI] means the BIOS will look for an operating system first in drive A, then in drive C, and eventually in SCSI device.	

3-1.5 Other Control Options

Other Control	Setting	Description	Note
Options			
Soft-Off by	Instant-off		Default
PWR-BTTN	Delay 4 Sec.	Turns off the system power 4	
	-	seconds after pushing the power	
		button.	
Memory Ultra	Disabled	Setting this item to be enabled will	
fast	Enabled	set the memory to run at the fastest	Default
		speed. Only use this setting with	
		good quality DIMM modules.	
		g	
Modem Ring	Disabled	An input signal on the serial Ring	Default
Resume	Enabled	Indicator (RI) line (in other words,	
		an incoming call on the modem)	
		awakens the system from a soft off	
		state.	



Other Control Options (Continued)

Other Control Options	Setting	Description	Note
			~
RTC Alarm	Disabled	The system ignores the alarm.	Default
Resume	Enabled	Set alarm to power on the system	
		by the date (1-31) or time	
		(hh:mm:ss). If the date is set to [0],	
		the system will self-power on by	
		alarm everyday at the set time.	

3-1.6 CPU Device Monitoring

CPU Device Monitoring	Setting	Description	Note
Current System Temp.	°C/°F	Show the current status of the system temperature.	
Current CPU Temperature	°C/°F	Show the current status of CPU temperature.	
Current CPUFAN Speed	°C/°F	Show the current status of CPU Fan	
Current CHAFAN Speed	°C/°F	Show the current status of the chassis Fan	
Vcore, 2.5V, 3.3V, 5V, 12V	V	Show the current voltage status.	



3-2 STANDARD CMOS SETUP

Select the [STANDARD CMOS SETUP] option from the Main Menu and press [Enter] key.

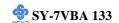
ROM PCI/ISA BIOS								
		STANI	DARD C	MOS SET	TUP			
		AWAR	D SOFT	WARE, II	NC.			
Date (mm:dd:yy)	: Thu, Jan 1 19	998						
Time (hh:mm:ss)	: 1: 9 :2	5						
HARD DISKS	TYPE	SIZE	CYLS	HEAD	PRECOMP	LANDZ	SECTOR	MODE
Primary Master	: Auto	0	0	0	0	0	0	AUTO
Primary Slave	: Auto	0	0	0	0	0	0	AUTO
Secondary Master	: Auto	0	0	0	0	0	0	AUTO
Secondary Slave	: Auto	0	0	0	0	0	0	AUTO
Drive A : 1.44M	I, 3.5 in.		_					
Drive B : None			П		Ba	se Memory:	640K	
Floppy 3 Mode Suppo	rt : Disabled				Extend	ed Memory:	31744K	
					Oth	er Memory:	384K	
Video : E	GA/VGA							
Halt On : A	ll, But Keyboard				To	tal Memory:	32768K	
ESC : Quit			® ¬ :	Select Iter	n	PI / PD / +	/ - : Modify	
F1 : Help		(Shift) F2:	Change C	olor	10/10/1	, . 1410dily	

This screen allows you to modify the basic CMOS settings.

After you have completed the changes, press [Esc] key to return to the Main Menu.

3-2.1 Date & Time

	Display	Setting	Please Note
Date	mm/dd/yyyy	Type the current date	You can also the PUp/PDn keys to toggle
Time	hh:mm:ss	Type the current time	24-hour clock format 3:15 PM is displayed as 15:15:00



3-2.2 Hard Disks Type & Mode

Choose the type and mode for the hard disks that you have already installed.

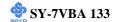
Primary (Secondary) Master & Slave	Setting	Description	Note
Туре	Auto	BIOS detects hard disk type automatically.	Default
	User	User defines the type of hard disk.	
	None		
Mode	Auto	BIOS detects hard disk mode automatically.	Default
	Normal	Normal IDE hard disk	<528MB
	LBA	Enhanced IDE hard disk	>528MB
	Large	Large IDE hard disk (for certain hard disk)	



Note: If you have any questions on your hard disk type or mode, ask your hard disk provider or previous user for details.

3-2.3 Floppy Drives

Floppy Drives	Setting	Description	Note
Drives A & B	360KB, 5.25 in. 1.2MB, 5.25 in. 720KB, 3.5 in. 1.44MB, 3.5 in.	-	Default
Floppy 3-Mode	2.88MB, 3.5 in. None Disabled	Not installed	Default
Support	Drive A Drive B Both	Supports 3-mode floppy diskette: 740KB/1.2MB/ 1.44MB on selected disk drive.	Special disk drive commonly used in Japan

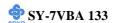


3-2.4 Video

Select the video mode: EGA/VGA (Default), CGA 40, CGA 80, Mono (Monochrome).

3-2.5 Halt On

When the BIOS detects system errors, this function will stop the system. Select which type of error will cause the system halt: All Errors (Default), No Errors, All But Diskette, All But Keyboard, All But Disk/Key.



3-3 BIOS FEATURES SETUP

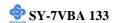
Select the [BIOS FEATURES SETUP] option from the Main Menu and press [Enter] key.

	ROM PCI/	ISA BIOS
	BIOS FEATU	JRES SETUP
	AWARD SOF	ΓWARE, INC.
PBVA-Virus Protection	: Disabled	Video BIOS Shadow : Enabled
CPU Internal Cache	: Enabled	C8000-CBFFF Shadow : Disabled
External Cache	: Enabled	D0000-D7FFF Shadow : Disabled
CPU L2 Cache ECC Checking	: Enabled	D8000-DFFFF Shadow : Disabled
Processor Number Feature	: Enabled	
Swap Floppy Drive	: Disabled	
Boot Up Floppy Seek	: Disabled	
Boot Up NumLock Status	: On	
IDE HDD Block Mode	: Enabled	
Cate A20 Option	: Fast	
Memory Parity / ECC Check	: Disabled	
Typematic Rate Setting	: Disabled	
Typematic Rate (Chars/Sec)	: 6	
Typematic Delay (Msec)	: 250	ESC : Quit - ⁻ ® ¬ : Select Item
Security Option	: Setup	F1 : Help PU/PD/+/- : Modify
PCI/VGA Palette Snoop	: Disabled	F5 : Old Values (Shift) F2 : Color
OS Select For DRAM > 64 MB	: Non-OS2	F7 : Load Setup Defaults
HDD S.M.A.R.T. capability	: Disabled	

After you have completed the changes, press [Esc] key and follow the instructions on your screen to save your settings or exit without saving.

3-3.1 PBVA-Virus Protection

	Setting	Description	Note
PBVA-Virus	Disabled		Default
Protection	Enabled	Enable this option to protect	
		the boot sectors and partition	
		tables of your hard disk. Any	
		attempt to write to them will	
		the system to halt and display a	
		warning message.	



3-3.2 Cache Memory Options

3-3.2 Cache Memory Options				
	Setting	Description	Note	
CPU Internal Cache	Disabled			
	Enabled	Enables the CPU's	Default	
		internal cache.		
External Cache	Disabled			
	Enabled	Enables the external	Default	
		memory.		

3-3.3 L2 Cache Memory

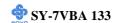
	Setting	Description	Note
CPU L2 Cache ECC	Disabled		
Checking	Enabled	This option activates the	Default
		CPU L2 cache ECC	
		checking function.	

3-3.4 Processor Number Feature

	Setting	Description	Note
Processor	Disabled		
Number Feature	Enabled	If this item is set to enabled,	Default
		Application programs can read	
		the unique CPU-ID number in	
		your Pentium III CPU. Set to	
		disabled, the CPU-ID number	
		can not be read anymore by	
		any software.	

3-3.5 System Boot Control Settings

System Boot Control Settings	Setting	Description	Note
Swap Floppy	Disabled		Default
Drive	Enabled	Changes the sequence of A and B drives.	

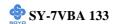


System Boot Control Settings (Continued)

System Boot Control Settings	Setting	Description	Note
Boot Up Floppy	Disabled		Default
Seek	Enabled	If this item is set to enabled, the system will test the floppy drive by moving it's head from and back to pos 0.	
Boot Up NumLock Status	On	Puts numeric keypad in NumLock mode at boot-up.	Default
	Off	Puts numeric keypad in arrow key mode at boot-up.	

3-3.6 Other Control Options

	Setting	Description	Note
IDE HDD Block Mode	Disabled	Invokes multi-sector transfer instead of one sector per transfer. Not all HDDs support this funcion.	Default
	Enabled		
Gate A20 Option	Normal		
	Fast	Allows RAM access above 1MB using the fast gate A20 line.	Default
Memory Parity/ECC	Disabled		Default
check	Enabled	Enabled option this Memory Parity/ECC check.	



3-3.7 Typematic Settings

Typematic Settings		Description	Note
Typematic Settings	betting	Description	11010
Typematic	Disabled		Default
Rate Setting	Enabled	Enables to adjust the	
		keystroke repeat rate.	
The following [Type	matic Rate] and	[Typematic Delay] fields are	active
only if [Typematic R		- · · ·	
Typematic Rate	6 (Char/sec) 8 (Char/sec) 10 (Char/sec) 12 (Char/sec) 15 (Char/sec) 20 (Char/sec) 24 (Char/sec) 30 (Char/sec)	Choose the rate at which a character is repeated when holding down a key.	Default
Typematic Delay	250 (msec) 500 (msec) 750 (msec) 1000 (msec)	Choose how long after you press a key down the character begins repeating.	Default

3-3.8 Security Option

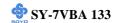
Use this feature to prevent unauthorized system boot-up or use of BIOS Setup. The following table describes the security settings.

	Setting	Description	Note
Security	System	Each time the system is booted, the	
Option		password prompt appears.	
	Setup	If a password is set, the password	Default
		prompt only appears when you attempt	
		to enter the BIOS Setup program.	



3-3.9 Other Control Options

Other Control Options	Setting	Description	Note
PCI/VGA	Disabled		Default
Palette Snoop	Enabled	The color of the monitor may be altered when using an MPEG card. Enable this option to restore the monitor's normal color.	
OS Select for DRAM>64MB	OS2	When using an OS2 operating system.	
	Non-OS2	When using another, non-OS2 operating system.	Default
HDD	Disabled		
S.M.A.R.T. capability	Enabled	Enable this field when your HDD supports the S.M.A.R.T. function. Consult your HDD provider for details.	
Video BIOS	Disabled		
Shadow	Enabled		Default
	The BIOS is is enabled a These 16 se ROM to RA code from s can then exc		



3-4 CHIPSET FEATURES SETUP

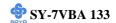


Caution: Change these settings only if you are already familiar with the Chipset.

The [CHIPSET FEATURES SETUP] option changes the values of the chipset registers. These registers control the system options in the computer.

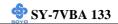
	ROM PC CMOS SET CHIPSET FEA	UP UTI	LITY	
Bank 0/1 Timing	: Normal			
Bank 2/3 Timing				
Bank 4/5 Timing				
Memory Hole	: Disabled			
Read Around Write	: Disabled			
Concurrent PCI/Host	: Disabled			
System BIOS Cacheable	: Disabled			
Video RAM cacheable	: Disabled			
AGP Aperture Size	: 128M			
AGP-2X Mode	: Disabled			
OnChip USB	: Enabled			
USB Keyboard Support	: Disabled	ESC	: Quit	- ¯® ¬ : Select Item
OnChip Sound	: Enabled	F1	: Help	PU/PD/+/-: Modify
		F5	: Old Values	(Shift) F2: Color
		F7	: Load Setup	Defaults

After you have completed the changes, press [Esc] and follow the instructions on your screen to save your settings or exit without saving. The following table describes each field in the CHIPSET FEATURES SETUP Menu and how to configure each parameter.



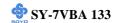
CHIPSET FEATURES SETUP

CHIPSET FEATURES	Setting	Description	Note
Bank 0/1, 2/3, 4/5 DRAM Timing	SDRAM 10ns SDRAM 8ns Normal Medium Fast Turbo	This item allows you to select the value in this field, depending on whether the board has paged DRAMs or EDO (extended data output) DRAMs.	Default
Memory Hole	Disabled 15M -16M	Some interface cards will map their ROM address to this area. If this occurs, select 15M – 16M in this field.	Default
Read Around Write	Disabled Enabled	DRAM optimization feature: If a memory read is addressed to a location whose latest write is being held in a buffer before being written to memory, the read is satisfied through the buffer contents, and the read is not sent to the DRAM.	Default
Concurrent PCI/Host	Disabled Enabled	When disabled, CPU bus will be occupied during the entire PCI operation period.	Default
System BIOS Cacheable	Disabled Enabled	Selecting <i>Enabled</i> allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.	Default
Video RAM Cacheable	Disabled Enabled	The ROM area A0000-BFFFF is cacheable.	Default



CHIPSET FEATURES SETUP (Continued)

CHIPSET	Setting	Description	Note
FEATURES			
AGP Aperture Size	128 4-128MB	AGP could use the DRAM as its video RAM. Choose the DRAM size that you wish to allocate as video RAM.	Default
AGP – 2X Mode	Disabled		Default
	Enabled	Enable only if your AGP card supports 2x mode (faster)	
OnChip USB	Disabled	This should be enabled if your	
	Enabled	system has a USB installed on the system board and you want to use it. Even when so equipped, if you add a higher performance controller, you will need to disable this feature.	Default
USB Keyboard	Disabled		
Support	Enabled	Enabled if you use an USB Keyboard.	Default
OnChip Sound	Disabled		
	Enabled	When using Spread Spectrum Modulated 1.5% or 6% for FCC or DOC testing.	Default

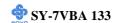


3-5 POWER MANAGEMENT SETUP

The [POWER MANAGEMENT SETUP] sets the system's power saving functions.

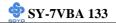
	ROM PC				
POWER MANAGEMENT SETUP AWARD SOFTWARE, INC.					
ACPI function	: Enabled	Primary	<u> </u>	: OFF	
Power Management	: User Define	IRO3	(COM 2)	: Primary	
PM Control by APM	: No	IRO4	(COM 1)	: Primary	
Video Off After	: NA	IRQ5	(LPT 2)	: Primary	
Video Off Method	: Blank Screen	IRQ6	(FLOPPY DISK)	,	
HDD Power Down	: Disable	IRO7	(LPT 1)	: Primary	
Doze Mode	: Disable	IRQ8	(RTC Alarm)	: Disabled	
Suspend Mode	: Disable	IRQ9	(IRQ2 Redir)	: Secondary	
MODEM Use IRQ	: NA	IRQ10	(Reserved)	: Secondary	
		IRQ11	(Reserved)	: Secondary	
		IRQ12	(PS/2 Mouse)	: Primary	
* * PM Events * *		IRQ13	(Coprocessor)	: Primary	
VGA	: OFF	IRQ14	(Hard Disk)	: Primary	
LPT& COM	: LPT/COM	IRQ15	(Reserved)	: Disabled	
HDD & FDD	: ON	IKQIS	(Reserved)	. Disabled	
DMA/master	: OFF				
Divil Pillageor	. 011	ESC		® ¬ : Select Item	
		F1	•	PD/+/- : Modify	
		F5	*	nift) F2 : Color	
		F7	: Load Setup Defau	lts	

After you have completed the Power Management Setup, press [Esc] to return to the Main Menu.



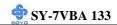
3-5.1 Power Management Controls

Power Management Controls	Setting	Descripti			Note
ACPI function	Disabled Enabled	_	dvanced ation Powe nent Interf		Default
Power Management	User Define Disable	system po Disables	define the ower dowr the Green		Default
	Min Saving Max Saving	Features. Doze timer 1 Hour 1 Min	Standby timer 1 Hour 1 Min	timer 1 Hour	HDD power down 15 Min 1 Min
PM Control by APM	Yes	To use Advanced Power Management (APM) you must run [power.exe] under DOS V6.0 or later version.			Default
Video Off After	No Standby Suspend Doze		go off after	de you want r the mode	
Video Off Method	V/H Sync+Blank Blank screen DPMS Supported 3-11, NA		e method or is blank	~	Default



3-5.2 PM Timers

	3-3.2 PW Hitters							
PM Timers	Setting	Description	Note					
The following []	The following [HDD Power Down] field may be configured only if							
[Power Management] is set to [User Define]								
	,							
HDD Power	Disabled		Default					
Down	1-15Min	When the set time has	Some older model					
		elapsed, BIOS sends a	HDDs may not					
		command to the HDD to	support this					
		power down. This turns off	advanced function.					
		the HDD motor.						
TP1 . C. 11	D M. 1.	3 C 11 1 C 1 1	'CID					
		e] field may be configured only	y if [Power					
Management] is	set to [Use	er Define]						
Doze Mode	Disable		Default					
	1Min-	When the set time has	System clock					
	1Hour	elapsed, BIOS sends a	drops to 33MHz.					
		command to the system to						
		enter Doze Mode.						
The following [Suspend M	ode] field may be configured	only if [Power					
Management] is	set to [Use	er Define]						
Suspend Mode	Disable		Default					
F	1Min-	In Suspend mode, the CPU	Only an SL-					
	1Hour	stops completely (no	Enhanced (or					
	111001	instructions are executed.)	SMI) CPU can					
		moractions are executed.)	enter this mode.					
MODEMI	NIA	A IDO# 4 . 41	D. C. 1					
MODEM Use	NA	Assigns an IRQ# to the	Default					
IRQ	3	modem device.						



3-5.3 PM Events

PM Events	Setting	Description	Note
VGA	ON	When Enabled, your can set the LAN awakens the system.	
	OFF		
LPT & COM	NONE	When On of LPT & COM, any	
	LPT	activity from one of the listed	
	COM	system peripheral devices or IRQs	
	LPT/COM	wakes up the system.	
HDD & FDD	ON	When On of HDD & FDD, any	
		activity from one of the listed	
		system peripheral devices wakes up	
	OFF	the system.	
DMA/master	ON	When you are On of DMA/ Master,	
		any activity from one of the list system peripheral devices wakes up	
		the system.	
	OFF		
Primary INTR	ON	When set to On, any event	Default
	OFF	occurring at will awaken a	Deraure
		system which has been powered	
		down. The following is a list of	
		IRQ's, Interrupt ReQuests,	
		which can be exempted much as	
		the COM ports and LPT ports above can. When an I/O device	
		wants to gain the attention of the	
		operating system, it signals this	
		by causing an IRQ to occur.	
		When the operating system is	
		ready to respond to the request,	
		it interrupts itself and performs	
		the service. When set Off, activity will neither	
		prevent the system from going into	
		a power management mode nor	
		awaken it.	

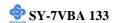


3-6 PNP/PCI CONFIGURATION SETUP

This option sets the motherboard's PCI Slots.

	ROM PCI/ISA BIOS							
	PNP/PCI CONFIGURATION							
	AWARD SOFTWARE, INC.							
PNP OS In	stalled	: No	CPU to PCI Write Buffer : Disabled					
Resources	Controlled By	: Manual	PCI Dynamic Bursting : Disabled					
Reset Conf	iguration Data	: Disabled	PCI Master 0 WS Write : Disabled					
			PCI Delay Transaction : Disabled					
IRQ - 3	assigned to	: Legacy ISA	PCI #2 Access #1 Retry : Disabled					
IRQ - 4	assigned to	: Legacy ISA	AGP Master 1 WS Write : Disabled					
IRQ - 5	assigned to	: PCI/ISA PnP	AGP Master 1 WS Read : Disabled					
IRQ - 7	assigned to	: PCI/ISA PnP						
IRQ - 9	assigned to	: PCI/ISA PnP	Assign IRQ For USB : Enabled					
IRQ - 10	assigned to	: PCI/ISA PnP	Assign IRQ For VGA : Enabled					
IRQ - 11	assigned to	: PCI/ISA PnP	Slot 1/5 Use IRQ NO. : Auto					
IRQ - 12	assigned to	: PCI/ISA PnP	Slot 2 Use IRQ No. : Auto					
IRQ - 14	assigned to	: PCI/ISA PnP	Slot 3 Use IRQ No. : Auto					
IRQ - 15	assigned to	: PCI/ISA PnP	Slot 4 Use IRQ No. : Auto					
DMA - 0	assigned to	: PCI/ISA PnP						
DMA - 1	assigned to	: PCI/ISA PnP	ESC : Quit - ⁻ ® ¬ : Select Item					
DMA - 3	assigned to	: PCI/ISA PnP	F1 : Help PU/PD/+/- : Modify					
DMA - 5	assigned to	: PCI/ISA PnP	F5 : Old Values (Shift) F2 : Color					
DMA - 6	assigned to	: PCI/ISA PnP	F7 : Load Setup Defaults					
DMA - 7	assigned to	: PCI/ISA PnP						

After you have completed the PCI Slot Configuration, press [Esc] and follow the instructions on your screen to save your settings or exit without saving.



3-6.1 PNP/PCI Configuration Controls

PNP/PCI Controls	Setting	Description	Note		
PNP OS Installed	Yes No		Default		
Resources	Manual	not. BIOS does not manage PCI/I.	SA PnP		
Controlled By		card IRQ assignment.			
	Requires to assign IRQ-# and DMA-# to PCI or ISA PnP manually. IRQ-3,4,5,7,9,10,11,12,14,15 assigned to:				
	Auto	3,5,6,7 assigned to: The Plug-and-Play BIOS auto manages PCI/ISA PnP card IRQ assignment automatically.	Recommended		
Reset Configuration	Disabled	Retain PnP configuration data in BIOS.	Default		
Data	Enabled	Reset PnP configuration data in BIOS.			

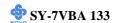
3-6.2 PNP/PCI Configuration Setup

PNP/PCI Setup	Setting	Description	Note
If [Resources Con	ntrolled By] is	set to [Manual]	
IRQ-# and DMA-# assigned to:	PCI/ISA PnP	Choose IRQ-# and DMA-# assigned to PCI/ISA PnP card.	IRQ-3,4,5,7,9,10, 11,12,14,15 DMA-0,1,3,5,6,7
	Legacy ISA	Choose IRQ-# and DMA-# assigned to Legacy ISA card.	IRQ-3,4,5,7,9,10, 11,12,14,15 DMA-0,1,3,5,6,7



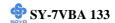
3-6.3 PCI Slot Configuration

PCI Slot Configuration	Setting	Description	Note
CPU to PCI Write Buffer	Disabled Enabled	When this field is <i>Enabled</i> , writes from the CPU to the PCI bus are buffered, to compensate for the speed differences between the CPU and the PCI bus. When <i>Disabled</i> , the writes are not buffered and the CPU must wait until the write is complete before starting another write cycle.	Default
PCI Dynamic Bursting	Disabled Enabled	When <i>Enabled</i> , every write transaction goes to the write buffer. Burstable transactions then burst on the PCI bus and nonburstable transactions don't.	Default
PCI Master 0 WS Write	Disabled Enabled	When <i>Enabled</i> , writes to the PCI bus are executed with zero wait states.	Default
PCI Delay Transaction	Disabled Enabled	The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select <i>Enabled</i> to support compliance with PCI specification version 2.1.	Default
PCI #2 Access #1 Retry	Disabled Enabled	When PCI#2 (AGP bus) access to PCI#1 (PCI bus) has a error occurred.	Default
AGP Master 1 WS Write	Disabled Enabled	When <i>Enabled</i> , writes to the AGP(Accelerated Graphics Port) are executed with one wait states.	Default



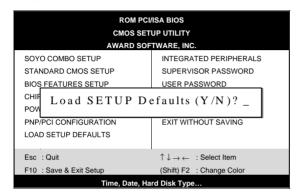
PCI Slot Configuration (Continued)

PCI Slot	Setting	Description	Note
	setting	Description	Note
Configuration			
AGP Master 1	Disabled		Default
WS Read	Enabled	When <i>Enabled</i> , read to the AGP	
		(Accelerated Graphics Port) are	
		executed with one wait states.	
4 · IDO	D: 11 1	DIOC 'II ' IDOC HGD	
Assign IRQ	Disabled	BIOS will assign IRQ for USB port.	
For USB	Enabled	BIOS won't assign IRQ for USB	Default
		port.	2 014410
		•	
Assign IRQ	Enabled	BIOS will assign IRQ for VGA	Default
For VGA		port.	
TOT VOM	Disabled		
	Disabled	BIOS won't assign IRQ for VGA	
		port.	
		interrupt to a PCI slot after BIOS passes	control to
the OS, especially	if you use V	Vindows 95, 98 or NT.	
Slot 1/5, 2, 3,	Auto	Set to Auto the BIOS will using	Default
4 Use IRQ		IRQs Automatically.	
NO.	10,11,12,		
110.			
	14,15		



3-7 LOAD SETUP DEFAULTS

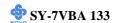
Select the [LOAD SETUP DEFAULTS] option from the Main Menu to load the system values you have previously saved. This option is recommended if you need to reset the system setup and to retrieve the old values.



Type [Y] to use the Setup Defaults followed by [Enter] or otherwise [N] to return to the Main Menu and keep current values.



Warning: If you run into any problem after changing the BIOS configuration, please load the SETUP DEFAULTS for stable performance.



3-8 INTEGRATED PERIPHERALS



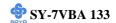
Caution: Change these settings only if you are already familiar with the Chipset.

The [INTEGRATED PERIPHERALS] option changes the values of the chipset registers. These registers control the system options in the computer.

The following screen shows setup default settings.

ROM PCI/ISA BIOS							
INTEGRATED PWEIPHERALS							
AWARD SOFTWARD, INC.							
OnChip IDE Channel0	: Enabled	Onboard Parallel Port : 378/IRQ7					
OnChip IDE Channel1	: Enabled	Onboard Parallel Mode : SPP					
IDE Prefetch Mode	: Enabled	ECP Mode Use DMA : 1					
Primary Master PIO	: Auto	Parallel Port EPP type : EPP 1.9					
Primary Slave PIO	: Auto	Onboard Legacy Audio : Enabled					
Secondary Master PIO	: Auto	Sound Blaster : Disabled					
Secondary Slave PIO	: Auto	SB I/O Base Address : 220H					
Primary Master UDMA	: Auto	SB IRQ Select : IRQ 5					
Primary Slave UDMA	: Auto	SB DMA Select : DMA 0					
Primary Master UNMA	: Auto	MPV-401 : Disabled					
Primary Slave UDMA	: Auto	MPV-401 I/O Address : 300-303H					
Init Display First	: PCI Slot	Game Port (200-207H) : Disabled					
Onboard FDC Controller	: Enabled						
Onboard Serial Port 1	: 3F8/IRQ4	ESC : Quit - ® ¬ : Select Item					
Onboard Serial Port 2	: 2F8/IRQ3	F1 : Help PU/PD/+/- : Modify					
UR2 Mode	: Standard	F5 : Old Values (Shift) F2 : Color					
IR Function Duplex	: Full	F7 : Load Setup Defaults					
TX,RX inverting enable	: No, No						

The following tables describe each field in the INTEGRATED PERIPHERALS Menu and provide instructions on how to configure the IDE controls, FDC controls, and the onboard serial and parallel ports.



3-8.1 IDE Device Controls

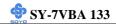
DEC 1 L C C L D C L D C L D C L D C L L D C L L D C L L D C						
IDE Controls	Setting	Description	Note			
On-Chip PCI IDE > Primary	Disabled	Turn off the on-board IDE				
> Secondary	Enabled	Use the on-board IDE	Default			
IDE HDD Block Mode	Disabled					
	Enabled	Invokes multi-sector transfer instead of one sector per transfer. Not all HDDs support this function.	Default			
IDE > Primary Master PIO	mode 0-4	0 is the slowest speed 4 is the fastest speed				
 ➢ Primary Slave PIO ➢ Secondary Master PIO ➢ Secondary Slave PIO 	Auto	For better performance and stability, we suggest you use the Auto setting to set the HDD control timing.	Default			
IDE	Disabled					
>Primary Master UDMA >Primary Slave UDMA	Auto	Select Auto to enable Ultra DMA Mode support.	Default			

3-8.2 Init Display Controls

Init Display Controls	Setting	Description	Note
Init Display First	PCI Slot	Choose which card –	Default
	AGP	AGP Display card or PCI	
		VGA card – to initialize	
		first.	

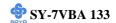
3-8.3 FDC Controls

FDC Controls	Setting	Description	Note
Onboard FDC	Disabled	Turn off the on-board	
controller		floppy controller	
	Enabled	Use the on-board floppy	Default
		controller	



3-8.4 Onboard Serial Ports

Onboard Serial Ports	Setting	Description	Note	
Onboard Serial Port 1 Onboard Serial Port 2	Disabled 3F8/IRQ4	Choose serial port 1 & 2's I/O address.	Default (port 1)	
roit 2	2F8/IRQ3 3E8/IRQ4 2E8/IRQ3 Auto	Do not set port 1 & 2 to the same address except for Disabled or Auto.	Default (port 2)	
UR2 Mode	Standard IrDA 1.0 ASKIR	Supports a Standard serial infrared IrDA. Supports a Sharp serial	Default	
If [UART Mode S	elect] is set to [In	interface format. DA]/[ASKIR]		
IR Function Duplex	Half Full	Choose [Half] or [Duplex] to set UR2 in half duplex mode or full duplex mode respectively. Refer to your IR device specifications to select the suitable mode.	Default	
TX,RX inverting enable	No, No Yes, Yes Yes, No No, Yes	This item allow you to enable the TX, RX inverting which depends on different H/W requirement. This field is not recommended to change its default setting for avoiding any error in your system	Default	



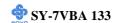
3-8.5 Onboard Parallel Ports

Onboard Parallel	Setting	Description	Note
Ports			
Onboard Parallel Port	Disabled 378H/IRQ7 3BCH/IRQ7	Choose the printer I/O address.	Default
Onboard Parallel Mode	Normal ECP/EPP SPP ECP EPP	The mode depends on your external device that connects to this port.	Default
If [Parallel Port Mode] is ECP Mode use DMA	set to [ECP] mo	Choose DMA3 Choose DMA1	Default
If [Parallel Port Mode] is Parallel Port EPP	EPP 1.9	Select EPP port type 1.9	
TYPE	EPP 1.7	Select EPP port type 1.7	Default

3-8.6 Onboard Legacy Audio

This field controls the onboard legacy audio.

		D '4'	NT 4
	Setting	Description	Note
Onboard	Disabled	Set this item to Enabled if using	
Legacy Audio	Enabled	software (like DOS games) that	Default
		needs a 'legacy' audio device.	
Sound Blaster	Disabled	Set this item to enabled if your on-	Default
	Enabled	board audio-chip is sound Blaster	
		compatible.	
SB I/O Base	220H	Select the base address for your	Default
Address	240H,260H	sound Blaster (SB) compatible	
	,280H	Audio IC.	
SB IRQ Select	IRQ 5	Select the IRQ your SB Audio IC	Default
	IRQ7,9,10	uses.	
SB DMA	DMA1	Select the DMA channel your SB	Default
Select	DMA2,3,0	Audio IC uses.	



Onboard Legacy Audio (Continued)

This field controls the onboard legacy audio.

	Setting	Description	Note
MPU-401	Disabled	Set this item to enabled to make use	Default
	Enabled	of MIPI.	
MPU-401 I/O	330-333H	Select the I/O address for your MIDI	Default
Address	330-303H	functionality.	
	310-313H		
	320-323H		
Game Port	Disabled	Set this item to enabled to make use	
(200-207H)	Enabled	of the game port.	Default

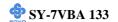
3-8.7 MULTI I/O ADDRESSES

Default settings for multi-I/O addresses are as follows:

Port	I/O Address	IRQ	Status	
LPT1	378H	7	ECP/EPP	
COM1	COM1 3F8H			
COM2	2F8H	3		



Warning: If a default I/O address conflicts with other I/O cards such as sound card, you must change one of the I/O addresses to remedy to this address conflict. (I/O addresses can be adjusted from the BIOS Setup Utility)



3-9 SUPERVISOR PASSWORD

Based on the setting you have made in the [Security Option] of the [BIOS FEATURES SETUP] section, the password prevents access to the system or the setup program by unauthorized users. Follow this procedure to set a new password or disable the password:

- 1. Choose [BIOS FEATURES SETUP] in the Main Menu and press [Enter]. Select the [Security Options] item and set the field to:
 - a. [System]: The password is required every time the system is booted. This means only a person who knows the password can use this computer.
 - b. [Setup]: The password is required only when you attempt to enter the BIOS Setup program.
- 2. Choose [SUPERVISOR PASSWORD] from the Main Menu and press [Enter]. The following prompt appear:

Enter Password:



Warning: If you forget or lose the password, the only way to access the system is to set jumper JP5 to clear the CMOS RAM. All setup information is lost and you must run the BIOS setup program again.



Note: If you do not wish to use the password function, press [Enter] directly and the following message appears:

Password Disabled!!

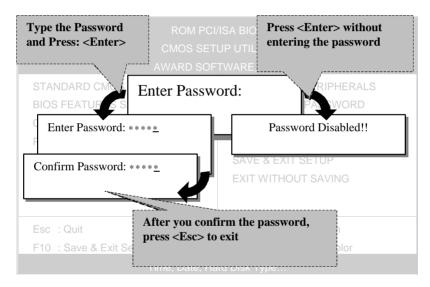


3. Enter your new password and press [Enter]. The following message appears, prompting to confirm the new password:

Confirm Password:

4. Re-enter your password and then press [Enter] to exit to the Main Menu.

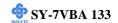
This diagram outlines the password selection procedure:



3-10 USER PASSWORD

When the user password option is on, you are not allowed to change any setting in the [CMOS SETUP UTILITY] except for changing the user's password.

The password setting procedure is similar to that for the [SUPERVISOR PASSWORD] (Refer to section 3-9).



3-11 IDE HDD AUTO DETECTION

This Main Menu function automatically detects the hard disk type and configures the STANDARD CMOS SETUP accordingly.

ROM PCI/ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

HARD DISKS TYPE SIZE CYLS HEAD PRECOMP LANDZ SECTOR MODE

Primary Master

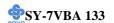
	Sele	ct Primar	y Master	Option (N=Sk	cip) : N		
OPTIONS	SIZE	CYLS	HEAD	PRECOMP	LANDZ	SECTOR	MODE
2(Y)	1707	827	64	0	3308	63	LBA
1	1707	3309	16	65535	3308	63	NORMAL
3	1707	827	64	65535	3308	63	LARGE

Note: Some Oses(SCO-UNIX Before v5.0) must use "NORMAL" for installation





Note: This function is only valid for IDE type of hard disk drives.



Chapter 4

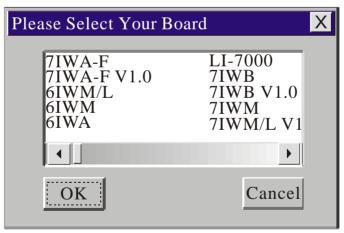
DRIVERS INSTALLATION

Your SY-7VBA 133 Motherboard comes with a CD-ROM labeled "SOYO CD." The SOYO CD contains the user's manual file for your new Motherboard, the drivers software available for installation, and a database in HTML format with information on SOYO Motherboards and other products.

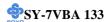
Step 1. Insert the SOYO CD into the CD-ROM drive

The SOYO CD will auto-run, and the SOYO CD Start Up Menu will be as shown.

If you use Windows NT, the SOYO-CD will not detect your motherboard type. In that case the following dialog will pop up, please choose your motherboard and press OK. Now the SOYO-CD Start Up Menu will be shown.



(SOYO CD Start Up Program Menu)

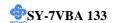


If you use Windows 95 or 98, the SOYO CD Start Up Program automatically detects which SOYO Motherboard you own and displays the corresponding model name.



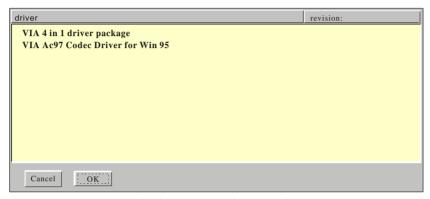
The user's manual files included on the SOYO CD are in PDF (Postscript Document) format. In order to read a PDF file, the appropriate Acrobat Reader software must be installed in your system.

Note: The Start Up program automatically detects if the Acrobat Reader utility is already present in your system, and otherwise prompts you on whether or not you want to install it. You must install the Acrobat Reader utility to be able to read the user's manual file. Follow the instructions on your screen during installation, then once the installation is completed, restart your system and re-run the SOYO CD.



Step 2. Install Drivers

Click the *Install Drivers* button to display the list of drivers software that can be installed with your Motherboard. The Start Up program displays the drivers available for the particular model of Motherboard you own. We recommend that you only install those drivers.



(Driver Installation Menu)

A short description of all available drivers follows:

> VIA 4in1 driver package

The 4 in 1 driver package includes all drivers your motherboard needs. After selecting this driver package, one driver will be installed automatically (the IRQ remapping utility), the other three are installed if selected. By default all three drivers are selected. A description of the 4 drivers follows:

-Bus Master PCI IDE Driver

This driver will speed up the data-transfer rate to and from the harddisk.

-AGP VxD Driver

This driver must be installed in order to be able to make use of the on-board AGP Video functionality.

—VIA Chipset Functions Registry



This driver will make the necessary changes to the Windows registry, in order to make sure that Windows has no problems recognizing your VIA chipset.

—IRQ remapping utility (This driver is installed automatically)

This utility will remap the IRQ lines to make sure that everything functions properly under Windows.

VIA Ac97 Codec Driver for Win 95

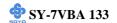
You have to install the drivers before installing any application for the Ac97 codec. Please refer to the back of the manual for installation information for other operating

Step 3. Select which driver you want to install and click OK

- Notice 1: Once you have selected a driver, the system will automatically exit the SOYO CD to begin the driver installation program. When the installation is complete, most drivers require to restart your system before they can become active.
- **Notice 2:** You may click **Cancel** to abort the driver installation and return to the main menu.

Step 4. Check the Latest Releases

Click the 'Check the latest Releases' button to go the SOYO Website to automatically find the latest BIOS, manual and driver releases for your motherboard. This button will only work if your computer is connected to the internet through a network or modem connection. Make sure to get your modem connection up before clicking this button.



Chapter 5

VIA SOUNDCHIP DRIVERS INSTALLATION

Most of the drivers on this CD can be installed automatically. Run the SOYO-CD program and click the 'install drivers' button. Now a list of the available drivers will be displayed. Select a driver and click OK to install the driver. Some drivers will have to be installed by hand. See the explanation below.

VIA Soundchip drivers for Windows 95 & 98

For windows 95/98, after installation of windows go to 'my computer' and select 'properties'. Now the 'system properties' menu will be shown. Select 'hardware' tab, and click the 'device manager' button.

On the device manager a question mark will be shown under 'other devices' -> 'PCI multimedia audio device'.

Do not remove this entry!

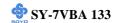
To change the driver, click this entry and select properties -> driver -> update driver. Now an update menu will be shown. If windows asks you for the location of the driver, give it the following directory:

d:\driv-all\VIA Sound\win9x

After the installation is finished, windows 95/89 will properly recognize your VIA sound chip.

VIA Soundchip drivers for Windows 98SE

Windows will detect a multi-media device at start-up, follow the installation steps on the screen. When windows asks you for the location of the drivers, click 'browse' and go the following directory: d:\driv-all\VIA Sound\win98se (where d is your CD-ROM drive letter) Now click OK and the drivers will be installed properly.



VIA Soundchip drivers for Windows 2000

For windows 2000, after installation of windows go to 'my computer' and select 'properties'. Now the 'system properties' menu will be shown. Select 'hardware' tab, and click the 'device manager' button. On the device manager a question mark will be shown under 'multimedia audio controller'. Do not remove this entry!

To change the driver, click this entry and select properties -> driver -> update driver. Now an update menu will be shown. If windows asks you for the location of the driver, give it the following directory:

d:\driv-all\VIA Sound\win2000

After the installation is finished, windows 2000 will properly recognize your VIA sound chip.

VIA Soundchip drivers for Windows NT

Double click 'my computer', and choose 'control panel'. Select the multimedia icon and select the devices tab.

Now select 'Multimedia devices -> Audio devices ' and click the 'add' button below.

Choose 'unlisted or updated driver' and press OK.

Now give the following directory:

d:\driv-all\VIA Sound\winnt4.0

After the installation is finished, windows NT will properly recognize your VIA sound chip.

