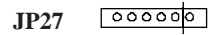


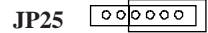
JP11: SRAM type Setting

| Function | JP11 |
|----------|------|
| MIX | |
| 3.3V | |

JP27/25: IrDA hearer



IR connector for Intel defined



IR connector for HP defined

JP8 : PCI Clock Setting

Open:CPU Clock/2

(for system clock=60/66 MHz)

Short:33MHz

(for system clock=50MHz)

JP12: SRAM Module Type Setting

1-2 short :Async. SRAM

2-3 short :Burst/pipeline-Burst

SRAM

* Don't install DIP type SRAM and Cache Module SRAM at the same time.

JP17: Clear CMOS data jumper

1-2 Default short

2-3 Reset internal RTC

J10: Green LED connector

J11: System break switch connector

J12: Turbo/De-turbo switch connector

J13: Keyboard lock connector

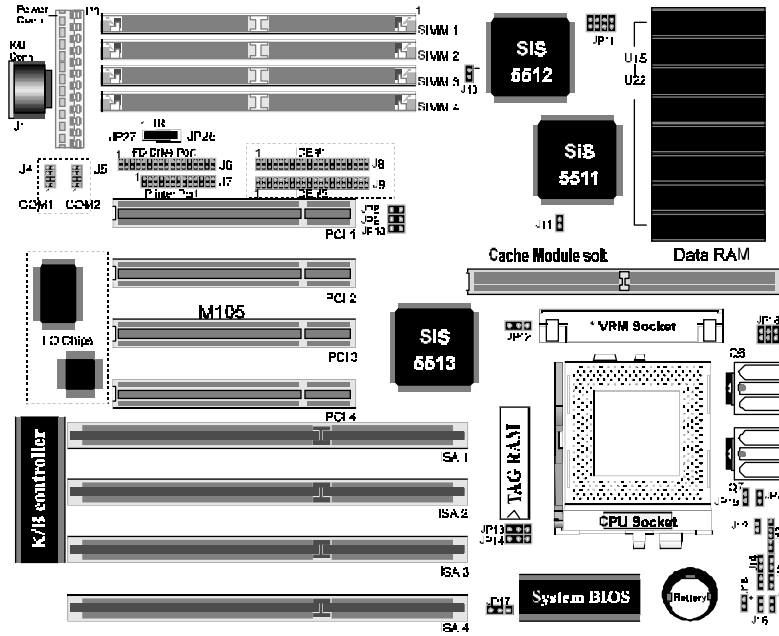
J14: External Battery connector

J15: Speaker connector

J16: Reset switch connector

J17: Turbo LED connector

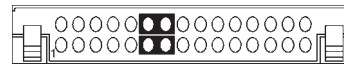
J18: Hard disk LED connector



JP13,JP14: External cache size jumper setting

| Cache size | Jumper Setting | TAG RAM U24 | Data RAM U15-U22 |
|------------|----------------|-------------|------------------|
| 256KB | | 8K x 8 | 32K x 8 |
| 512KB | | 32K x 8 | 64K x 8 |
| 1MB | | 32K x 8 | 128k x 8 |

* VRM (Voltage Regulator Module) Socket
Using on board regulator set, please set VRM P6 & P7, P21 & P22 short.



| CPU Type | S-spce, | CPU Power Voltag (V) | JP18 | System clock (MHz) | JP9, JP10 | CPU Int.speed JP19, JP20 |
|----------|------------------------------------|----------------------|------|--------------------|-----------|--------------------------|
| P54C-75 | QO-666 SX-961 | 3.3 | | 50 | | x1.5 |
| | QO-700 SX-969 | | | | | |
| P54C-90 | QO-653 SX-957 | 3.3 | | 60 | | x1.5 |
| | QO-654 SX-958 | 3.4 | | | | |
| | QO-655 | 3.5 | | | | |
| | QO-699 SX-968 | | | | | |
| P54C-100 | QO-697 SX-963 | 3.5 | | 66 | | x1.5 |
| | QO-698 SX-970 | | | | | |
| P54C-120 | QO-711 QO-732 SX-994 SK-086 | 3.5 | | 60 | | x2 |
| P54C-133 | QO-775 QO-773 SK-103 SK-107 SK-106 | 3.5 | | 66 | | x2 |
| P54C-150 | VRE | 3.5 | | 60 | | x2.5 |

| Intel CPU Speed(MHz) | JP19 | JP20 |
|----------------------|------|------|
| 1.5 x system clock | O | O |
| 2 x system clock | S | O |
| 2.5 x system clock | S | S |
| 3 x system clock | O | S |