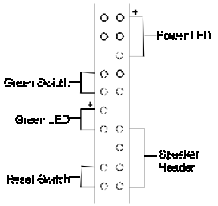
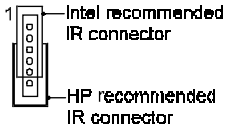
Jumper Setting Quick Reference

J7: Hard disk LED header

J8: Front Panel Set



J9: Infra Red connector

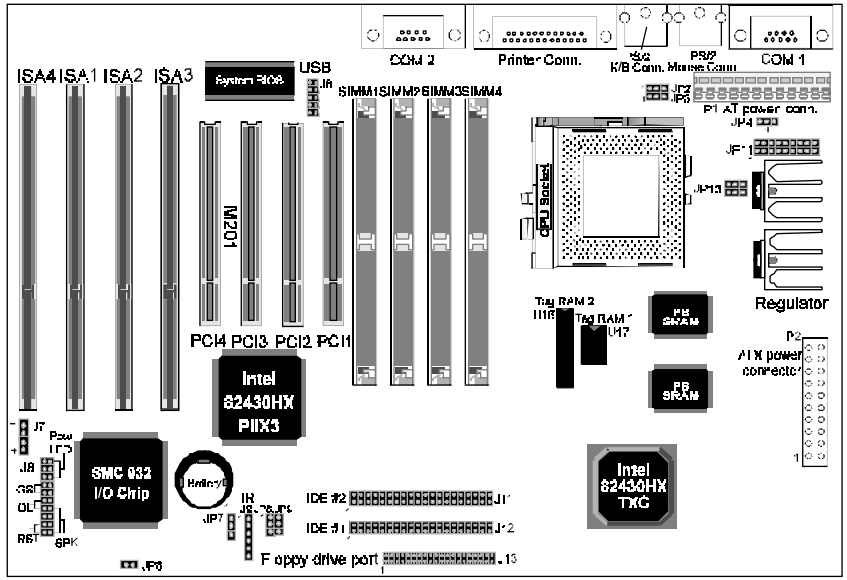


JP4: CPU cooling Fan header

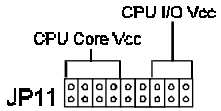
JP6: ATX Soft power switch header

JP7: CMOS data clear setting jumper
2~3 short: Normal (default)
1~2 short: Clear CMOS data

U16: Adding an extra Tag-RAM(32K*8) to expand DRAM cacheable range up to 512MB



For VRT (Voltage Reduction Technology) processor (such as Intel P55C), the split power plan (CPU's core voltage ≠ CPU's I/O voltage) design is required.



CPU Type	Core Vcc	I/O Vcc	JP13	JP11
Normal (P54C, 6x86, K5-ABQ)	3.3			
	3.4			
	3.5			
VRT (P55C, 6x86L, K5-AHQ)	2.5	3.3		
	2.7			
	2.8			
	2.9			

CPU freq.= Freq. rate x System freq.	JP2	JP3
75/90/100 = 1.5 x system clock	2~3	2~3
110/120/133 = 2 x system clock	1~2	2~3
150/166 = 2.5 x system clock	1~2	1~2
180/200 = 3 x system clock	2~3	1~2

CPU-type		S-spce	CPU Power Voltage: I/O Vcc	CPU Power Voltage: Core Vcc	CPU Power Voltage: JP13	CPU Power Voltage: JP11	System Frequency: MHz	System Frequency: JP8, JP9	Frequency ratio: Speed rate	Frequency ratio: JP2, JP3
Intel	P54C-75		3.3		JP13	JP11	50	JP8 JP9	x1.5	JP2 JP3
	P54C-90	QO653 QO655 SZ978 SX957 SX959	3.3			JP11	60	JP8,JP9	x1.5	JP2 JP3
		QO654 SX958	3.4			JP11				
			3.5			JP11				
	P54C-120	QO708	3.3			JP11			x2	JP2 JP3
	P54C-150		3.5			JP11			x2.5	JP2 JP3
	P54C-100	QO656	3.3			JP11	66	JP8,JP9	x1.5	JP2 JP3
		QO657	3.4			JP11				
	P54C-133		3.5			JP11			x2	JP2 JP3
	P54C-166								x2.5	JP2 JP3
	P54C-200								x3	JP2 JP3
	P55C-166		3.3	2.8	JP13	JP11			x2.5	JP2 JP3
	P55C-200								x3	JP2 JP3
Cyrix	6x86-P120 @100MHz	O28	3.5		JP13	JP11	50	JP8 JP9	2	JP2 JP3
	6x86-P133 @110MHz						55	JP8 JP9		
	6x86-P150 @120MHz						60	JP8 JP9		
	6x86-P166 @133MHz						66	JP8 JP9		
AMD	K5-PR75	ABQ	3.5			JP11	50	JP8 JP9	1.5	JP2 JP3
	K5-PR90						60	JP8 JP9		
	K5-PR100						66	JP8 JP9		
	K5-PR120 @90MHz						60	JP8 JP9		
	K5-PR133 @100MHz						66	JP8 JP9		

* CPU frequency = System frequency x Frequency ratio