













### JP30, JP31 CPU Clock Selection

	25MHz	33MHz	40MHz	50MHz
JP30				
JP31				



### JP45 SIMM Module Selection

	Only 72 Pin SIMM Installed, 30 Pin SIMM not Installed
* 	Others



### JP39 UNIX Jumper Select

* 	Normal
	UNIX

### JP2 CMOS Data Discharge Jumper



	Discharge
* 	Normal

### JP18 VL Bus Speed



* 	CPU Speed $\leq$ 33MHz
	CPU Speed > 33MHz

\* = Factory Default Value

### JP19 VL Bus Wait State

	Zero Wait State
* 	One Wait State









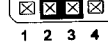





### JP29 Local Bus Signal Adjust

* 	For CPU Speed 33MHz
	For CPU Speed 40/50MHz

\* = Factory Default Value

## CPU Type Select Jumpers

### INTEL 486SX CPU

<b>JP6</b> 	<b>JP7</b> 	<b>JP8</b> 	<b>JP10</b> 	<b>JP11</b> 
<b>JP12</b> 	<b>JP13</b> 	<b>JP14</b> 	<b>JP15</b> 	<b>JP16</b> 
<b>JP17</b> 	<b>JP21</b> 	<b>JP36</b> 	<b>JP37</b> 	<b>JP38</b> 