

DRAM Configuration Table for (72 pin SIMM only)

Bank 0 (30 pin SIMM)	SIM1 (72 pin SIMM)	SIM2 (72 pin SIMM)	Total DRAM Size
X	512K*	X	2MB
X	1M	X	4MB
X	512K*	512K*	4MB
X	512K*	1M	6MB
X	2M*	X	8MB
X	1M	1M	8MB
X	512K*	2M*	10MB
X	1M	2M*	12MB
X	4M	X	16MB
X	2M*	2M*	16MB
X	1M	4M	20MB
X	8M*	X	32MB
X	4M	4M	32MB
X	16M	X	64MB
X	8M*	8M*	64MB
X	32M*	X	128MB
X	16M	16M	128M

Remark: X - Not install
* - Double density SIMM

System Board Configuration

Under some circumstances you may want to change the default configuration of the system board. These changes are made through adapting jumper settings on the system board. The following text will describe the function of jumpers and connectors and their corresponding location on the System Board can be found at the end of this chapter.

Jumper Functions

128K Cache External Cache Size

JP20

U24 installed with 8Kx-15ns SRAM U25, U28, U33, U39 installed with 32Kx8-20ns SRAM



JP22



JP24



256K Cache External Cache Size

JP20

U24 installed with 32Kx8-15ns SRAM U25, U26, U28, U29, U33, U34, U39, U40 installed with 32Kx8-20ns SRAM



JP22, JP24

