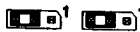

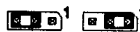





JP17, JP18 - Serial port 2 configuration

JP17 JP18

 Disable COM2, address 2F8H COM3, address 3E8H COM4, address 2E8H**JP19, JP20 - Parallel port configuration**

JP19 JP20

 Disable LPT1, address 378H LPT2, address 278H**JP22 - Game port select (on selected models only)** Disable Enable**JP25 - Parity Check Select** Disable Check DRAM Check DRAM and CACHE

Note: See description on JP26, 27, 28, 29

**JP26, JP27, JP28, JP29 - External CACHE
SRAM type select
(U101 - U104)**

32K x 8



32K x 9



DRAM	SRAM	JP25	WB/WT	Remark
x8	x8/x9	Not Installed	WB/WT	No parity check; prefer set to WB for higher performance.
x9	x9	Short 2-3	WB/WT	Parity check; prefer set to WB for higher performance.
x9	Not Installed	Short 1-2	Not Applicable	External Cache Mode should set to 'Disabled'.
x9	x8	Not Installed	WB/WT	Parity NOT check; prefer set to WB for higher performance.
x9	x8	Short 1-2	WT	Parity checked; Must be in WT. Lower performance but better data checking.

Note: All other combinations are not recommended.

x8 DRAM = SIMM Modules with no parity bit

x9 DRAM = SIMM Modules with parity bit

x8 SRAM = 32k x 8 SRAM

x9 SRAM = 32k x 9 SRAM

WB/WT = Write-Back/Write-Through Selection on Advanced CMOS Setup.